

KEI PALABASAN

FPGA/ASIC Engineer

ABOUT ME

Digital designer in Verilog with training in UVM verification framework. Studying MEMS design and fabrication.

Hobbyist hardware and software developer dabbling in web dev, cloud, IoT and ML/AI applications in robotics. Daily driver Linux user with experience debugging issues in systemd.

WORK EXPERIENCE

- 2022- **Engineering Intern**
now **Smartfox Data Solutions, Inc.**
- Digital design and verification in Verilog HDL and SystemVerilog/UVM
 - Research in AI and experience developing model code in Tensorflow.
 - Some experience with PCIe and ARM AMBA AXI, APB and CHI.
- 2018- **Programming Consultant**
2022 **Rizal National Science High School**
- Microcontroller and interface code programming for Robotics and Science Fair competitions.

ORGANIZATIONS

- 2021- **Chief Executive Officer**
2022 **UP Competitive Robotics Club**
- Manages workshops and competitions and delegates tasks to departments.
- 2020- **Chief of Tournament Operations**
2021 **UP Competitive Robotics Club**
- Managed logistics and handling for the Tournament Organizing Workshop and Robotics Ideathon

PUBLICATIONS

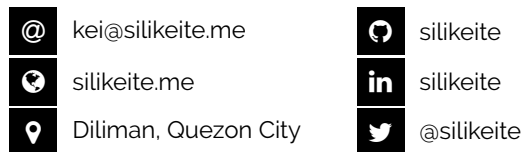
- 2018 **Effectiveness of Anti-Overloading System for Automotive Transport (AOSAT) in Selected Truck Drivers in Binangonan Rizal**
Otiong, K.S.J., Palabasan, I.V.R., Ramirez, J.C.B.
Submitted as Thesis for Senior High School, Submitted for WICO 2018
- 2017 **Breath Analyzer for Automotive Interlock**
Palabasan, I.V.R., Ordonez, J.DJ., Aguinaldo, V.S.C.
Submitted for WICC 2017

LANGUAGES

English - native
Filipino - proficient
Japanese - proficient

HOBBIES

Music, Singing, Guitar, Mechanical Keyboards, Anime and Manga, Gaming, Programming, Amateur Radio, Robotics



SKILLS

Languages: C/C++, Verilog/SystemVerilog/UVM, Python, MATLAB/Octave, LaTeX
OS/Applications: Linux (systemd, deb/rpm/arch, x86_64/ARM), Tensorflow, LTSpice, JAMStack
Miscellaneous: Hardware/software troubleshooting and debugging, Linux system configuration and maintenance, problem-solving, team communication

EDUCATION

- 2019- **BS Electronics Engineering**
2023 **University of the Philippines Diliman - Quezon City, NCR**
Expected Graduation: June 2023
Affiliated with UP Microlab for thesis
- 2017- **Senior High School**
2019 **Rizal National Science High School - Binangonan, Rizal**
Academic Track, STEM Strand
Graduated with High Honors, Best in Science

ACHIEVEMENTS

- 2019 **Synergy Software Solutions Competition**
UP Diliman
1st Place
- 2018 **World Invention Creativity Olympiad**
Seoul, South Korea
Gold Medal
- 2017 **World Invention Creativity Contest**
Seoul, South Korea
Gold Medal, MyRIS Special Award
- 2016 **International Olympiad in Informatics**
Kazan, Russia
Contestant
- 2015 **International Olympiad in Informatics**
Almaty, Kazakhstan
Contestant