SEMESTER 4

COMPUTER SCIENCE AND ENGINEERING

MATHEMATICS FOR COMPUTER AND INFORMATION SCIENCE-4

(Group A)

Course Code	GAMAT401	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	NIL	Course Type	Theory

Course Objectives:

To provide a comprehensive understanding of fundamental concepts of graph theory including paths, cycles, trees, graph algorithms, graph coloring and matrix representations, emphasizing their applications across various disciplines.

Module No.	Syllabus Description	Contact Hours
1	Introduction to Graphs - Basic definition, Application of graphs, finite and infinite graphs, Incidence and Degree, Isolated vertex, Pendant vertex and Null graph. Isomorphism, Sub graphs, Walks, Paths and circuits, Connected graphs, Disconnected graphs and components. [Text 1: Relevant topics from sections 1.1, 1.2, 1.3, 1.4, 1.5, 2.1, 2.2, 2.4, 2.5. Proofs of theorems 2.5, 2.7 are excluded.]	9
2	Euler graphs, Operations on Graphs, Hamiltonian paths and circuits, Travelling Salesman Problem, Connectivity, Edge connectivity, Vertex connectivity, Directed graphs, Types of directed graphs. [Text 1: Relevant topics from sections 2.6, 2.7, 2.8, 2.9, 2.10, 4.1, 4.2, 4.5, 9.1, 9.2. Proofs of theorems 4.6, 4.11, 4.12 are excluded.]	9
3	Trees- properties, Pendant vertices, Distance and centres in a tree, Rooted and binary trees, Counting trees, Spanning trees, Prim's algorithm and Kruskal's algorithm, Dijkstra's shortest path algorithm, Floyd-Warshall shortest path algorithm.	9

	[Text 1: Relevant topics from sections 3.1, 3.2, 3.3, 3.4, 3.5, 3.6, 3.7, 3.10,	
	11.5. Proofs of theorems 3.10, 3.16 are excluded.]	
4	Matrix representation of graphs- Adjacency matrix, Incidence Matrix, Circuit Matrix, Path Matrix, Coloring, Chromatic number, Chromatic polynomial, Greedy colouring algorithm.	0
4	[Text 1: Relevant topics from sections 7.1, 7.3, 7.8, 7.9, 8.1, 8.3. Proofs of theorems 7.4, 7.7, 7.8, 8.2, 8.3, 8.5, 8.6 are excluded.]	9

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
• 2 Questions from each	Each question carries 9 marks.	
module.	Two questions will be given from each module, out	
• Total of 8 Questions, each	of which 1 question should be answered.	
carrying 3 marks	Each question can have a maximum of 3 sub	60
	divisions.	
(8x3 =24marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Understand the fundamental concepts of graph theory such as types of graphs, degree of a vertex, graph isomorphism, connectedness.	K2
CO2	Understand the concepts of Euler graphs, Hamiltonian graphs and connectivity.	K2
CO3	Apply Prim's and Kruskal's algorithms for finding minimum cost spanning tree and Dijkstra's and Floyd-Warshall algorithms for finding shortest paths.	К3
CO4	Illustrate various representations of graphs using matrices and apply vertex coloring in real life problems.	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	2	-	-	-	-	-	-	-	-	2
CO2	3	3	2	-	-	-	-	-	-	-	-	2
CO3	3	3	2	2	-	-	-	-	-	-	-	2
CO4	3	3	2	2	-	-	-	-	-	-	-	2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Graph Theory with Applications to Engineering and Computer Science	Narsingh Deo	Prentice Hall India Learning Private Limited	1st edition, 1979			

	Reference Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Introduction to Graph Theory 2e	Douglas B. West	Pearson Education India	2nd edition, 2015			
2	Introduction to Graph Theory	Robin J. Wilson	Longman Group Ltd.	5th edition, 2010			
3	Graph Theory with Applications	J.A. Bondy and U.S.R. Murty	Elsevier Science Publishing Co., Inc	1976			

Video Links (NPTEL, SWAYAM)				
Module No.	Link ID			
1	https://onlinecourses.nptel.ac.in/noc22_ma10/preview			
2	https://onlinecourses.nptel.ac.in/noc22_ma10/preview			
3	https://onlinecourses.nptel.ac.in/noc21_cs48/preview			
4	https://onlinecourses.nptel.ac.in/noc21 cs48/preview			

DATABASE MANAGEMENT SYSTEMS

(Common to CS/CD/CA/CR/AD/AI/CB/CN/CC/CU/CI/CG)

Course Code	PCCST402	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:1:0:0	ESE Marks	60
Credits	4	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	PCCST303	Course Type	Theory

Course Objectives:

- 1. Equip the students with a comprehensive understanding of fundamental DBMS concepts as well as the principles and applications of NoSQL databases
- 2. Enable students to design, implement, and manage both relational and NoSQL databases

Module	odule Syllabus Description			
No.	Synabus Description			
	Introduction to Databases :- Database System Concepts and Architecture-			
	Data Models, Schemas and Instances, Three-Schema Architecture and Data			
	Independence, Database Languages and Interfaces, Centralized and			
1	Client/Server Architectures for DBMSs.			
_	Conceptual Data Modelling and Database Design:- Data Modelling Using the	11		
	Entity, Relationship (ER) Model - Entity Types, Entity Sets, Attributes, and Keys,			
	Relationship Types, Relationship Sets, Roles, and Structural Constraints, Weak			
	Entity Types. Refining the ER Design for the COMPANY Database.			
	The Relational Data Model and SQL - The Relational Data Model and Relational			
	Database Constraints-Relational Algebra and Relational Calculus - Structured			
2	Query Language (SQL)-Data Definition Language, Data Manipulation Language,			
	Assertions, Triggers, views, Relational Database Design Using ER-to-Relational	11		
	Mapping.			
	Database Design Theory & Normalization - Functional Dependencies -			
	Basic definition; Normalization- First, Second, and Third normal forms.			
3	Transaction Management - Transaction Processing : Introduction, problems and	11		
	failures in transaction, Desirable properties of transaction, Characterizing			
	schedules based on recoverability and serializability; Concurrency Control			

	with Two-Phase Locking Techniques- Database Recovery management:					
	Deferred update-immediate update- shadow paging.					
	Introduction To NoSQL Concepts - types of NoSQL databases- CAP					
4	Theorem- BASE properties- Use Cases and limitations of NoSQL.					
4	SQL architectural Patterns - Key value Stores, Graph Stores, Column	11				
	Family stores and Document Stores.					

Course Assessment Method

(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
2 Questions from each	• Each question carries 9 marks.	
module.	• Two questions will be given from each module, out	
• Total of 8 Questions, each	of which 1 question should be answered.	60
carrying 3 marks	Each question can have a maximum of 3 sub	
	divisions.	
(8x3 =24marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course, students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Summarize and exemplify the fundamental nature and characteristics of database systems	K2
CO2	Model and design solutions for efficiently representing data using the relational model or non-relational model	К3
CO3	Discuss and compare the aspects of Concurrency Control and Recovery in Database systems	К3
CO4	Construct advanced SQL queries to effectively retrieve, filter, and manipulate data from relational databases.	К3
CO5	Experiment with NoSQL databases in real world applications	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	3	3						2	2	3
CO3	3	3	3	3								3
CO4	3	3	3	3								3
CO5	3	3	3	3								3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Te	xt Books		
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Fundamentals of Database Systems [Module 1,2,3,4]	Elmasri, Navathe	Pearson	7/e,
2	Making the Sense of NoSQL : A guide for Managers and rest of us [Module 4]	Dan McCreary and Ann Kelly	Manning	2014

		Reference Books		
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	A., H. F. Korth and S. Sudarshan, Database System Concepts,	Sliberschatz A., H. F. Korth and S. Sudarshan, Database System Concepts, 6/e, McGraw Hill, 2011.	McGraw Hill,	7/e, 2011
2	Beginning Database Design Solutions	Rod Stephens	Wiley	2/e, 2023
2	NoSQL Distilled	Pramod J. Sadalage, Martin Fowler	Addison- Wesley	1/e, 2012
3	NoSQL Data Models: Trends and Challenges (Computer Engineering: Databases and Big Data),	Olivier Pivert	Wiley	2018

	Video Links (NPTEL, SWAYAM)				
Module No.	Link ID				
1	https://onlinecourses.nptel.ac.in/noc21_cs04/preview				
2	https://onlinecourses.nptel.ac.in/noc21_cs04/preview				
3	https://onlinecourses.nptel.ac.in/noc21_cs04/preview				
4	https://archive.nptel.ac.in/courses/106/104/106104135/				

OPERATING SYSTEMS

(Common to CS/CD/CM/CR/CA/AD/AI/CB/CN/CC/CU/CI/CG)

Course Code	PCCST403	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:1:0:0	ESE Marks	60
Credits	4	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- 1. To introduce the structure of a typical operating system and its core functionalities
- 2. To impart to the students, a practical understanding of OS implementation nuances based on the Linux operating system

Module No.	Syllabus Description			
	Introduction to Operating Systems (Book 1 Ch 2 introductory part),	Hours		
	Operating System Services (Book 3 Ch 2) Overview of Operating Systems			
	and Kernels, Linux Versus Classic Unix Kernels (Book 2 Ch 1)			
	Process concepts: Process Creation, Process States, Data Structures, Process			
	API (Book 1 Ch 4, 5), Sharing processor among processes - user and kernel			
	modes, context switching (Book 1 Ch 6), System boot sequence (Book 3 Ch			
	2)			
1	Case study: Linux kernel process management (Book 2, Ch 3)	11		
	Threads and Concurrency: Concept of a thread, Multithreading benefits,			
	Multithreading models (Book 3 Ch 4)			
	Case study: The Linux Implementation of Threads (Book 2, Ch 3)			
	Process scheduling: Concepts and basic algorithms (Book 1 Ch 7), The			
	Multilevel Feedback Queue: Basic Rules (Book 1 Ch 8)			
	Case study: The Linux Completely Fair Scheduler (CFS) (Book 1 Ch 9, Implementation with RB trees not required), The Linux Scheduling Implementation,			

Concurrency and Synchronization - Basic principles (Book 3 Sections 6.1, 6.2), Mechanisms - Locks: The Basic Idea, Building Spin Locks with Test-And-Set, Compare and Swap, Using Queues: Sleeping Instead Of Spinning (Book 1 Ch 28), Semaphores - Definition, Binary Semaphores, The Producer/Consumer (Bounded Buffer) Problem and its solution using semaphores, Reader-Writer Locks (Book 1 Ch 31) 2 Case study: Linux Kernel Synchronization Methods - Spin Locks, Semaphores, Mutexes (Book 2 Ch 10) Concurrency: Deadlock and Starvation - Deadlock Characterization, Deadlock Prevention and Avoidance, Deadlock Detection and recovery (Book 3 Ch 8), Dining Philosophers Problem and its solution (Book 1 Ch 31) Memory management - Address Space, Memory API, Address Translation - An Example, Dynamic (Hardware-based) Relocation, Segmentation: Generalized Base/Bounds, Address translation in segmentation, Support for Sharing (Book 1 Ch 13 to 16) Virtual memory - Paging: Introduction, page tables and hardware support, TLBs, Example: Accessing An Array, - TLB hits and misses, Handling TLB misses, TLB structure, Reducing the page table size (Book 1 Ch 18 to 20)
6.2), Mechanisms - Locks: The Basic Idea, Building Spin Locks with Test-And-Set, Compare and Swap, Using Queues: Sleeping Instead Of Spinning (Book 1 Ch 28), Semaphores - Definition, Binary Semaphores, The Producer/Consumer (Bounded Buffer) Problem and its solution using semaphores, Reader-Writer Locks (Book 1 Ch 31) 2 Case study: Linux Kernel Synchronization Methods - Spin Locks, Semaphores, Mutexes (Book 2 Ch 10) Concurrency: Deadlock and Starvation - Deadlock Characterization, Deadlock Prevention and Avoidance, Deadlock Detection and recovery (Book 3 Ch 8), Dining Philosophers Problem and its solution (Book 1 Ch 31) Memory management - Address Space, Memory API, Address Translation - An Example, Dynamic (Hardware-based) Relocation, Segmentation: Generalized Base/Bounds, Address translation in segmentation, Support for Sharing (Book 1 Ch 13 to 16) Virtual memory - Paging: Introduction, page tables and hardware support, TLBs, Example: Accessing An Array, - TLB hits and misses, Handling TLB
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Philosophers Problem and its solution (Book 1 Ch 31) Memory management - Address Space, Memory API, Address Translation - An Example, Dynamic (Hardware-based) Relocation, Segmentation: Generalized Base/Bounds, Address translation in segmentation, Support for Sharing (Book 1 Ch 13 to 16) Virtual memory - Paging: Introduction, page tables and hardware support, TLBs, Example: Accessing An Array, - TLB hits and misses, Handling TLB
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Generalized Base/Bounds, Address translation in segmentation, Support for Sharing (Book 1 Ch 13 to 16) Virtual memory - Paging: Introduction, page tables and hardware support, TLBs, Example: Accessing An Array, - TLB hits and misses, Handling TLB
Sharing (Book 1 Ch 13 to 16) Virtual memory - Paging: Introduction, page tables and hardware support, TLBs, Example: Accessing An Array, - TLB hits and misses, Handling TLB
Wirtual memory - Paging: Introduction, page tables and hardware support, TLBs, Example: Accessing An Array, - TLB hits and misses, Handling TLB
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misses, TLB structure, Reducing the page table size (Book 1 Ch 18 to 20)
Coing housed physical manners. Sugar areas foult and its control flow
Going beyond physical memory - Swap space, page fault and its control flow, page replacement policies, Thrashing (Book 1 Ch 21, 22)
page replacement poncies, Thrashing (Book 1 Ch 21, 22)
I/O system: Modern System architecture, Programmed I/O, Interrupts,
DMA, Device interaction methods, The Device Driver (Book 1 Ch 36),
Hard disk: Geometry (Book 1 Ch 37), disk scheduling (Book 3 Section
11.2)
Case study: Linux I/O schedulers - Elevator, Complete Fair Queuing (Book
4 2 Ch 14) 10
Files and Directories: The File System Interface - File descriptor, reading
and writing files (sequential and random access), Removing files - Hard links
and Symbolic links, Creating, reading and deleting directories, Permission
bits and Access Control Lists, Mounting a file system (Book 1 Ch 39)

File Organization: The Inode, The Multi-Level Index (Book 1 Ch 40)
Case study: VFS Objects and Their Data Structures - The Inode Object, Inode Operations (Book 2 Ch 13)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
 2 Questions from each module. Total of 8 Questions, each carrying 3 marks 	 Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 subdivisions. 	60
(8x3 = 24marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Apply the concepts of process management and process scheduling mechanisms employed in operating systems.	К3
CO2	Choose various process synchronization mechanisms employed in operating systems.	К3
CO3	Use deadlock prevention and avoidance mechanisms in operating systems.	К3
CO4	Select various memory management techniques in operating systems.	К3
CO5	Understand the storage management in operating systems.	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	3									3
CO3	3	3	3									3
CO4	3	3	3									3
CO5	3	3	3									3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books					
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year		
1	Operating Systems: Three Easy Pieces	Andrea Arpaci-Dusseau, Remzi Arpaci-Dusseau	CreateSpace	1/e, 2018		
2	Linux Kernel Development	Robert Love	Pearson	3/e, 2018		
3	Operating System Concepts	Abraham Silberschatz, Peter B. Galvin, Greg Gagne	Wiley	10/e, 2018		

	Reference Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Modern Operating Systems	Andrew S. Tanenbaum Herbert Bos	Pearson	5/e, 2012			
2	The Design of the UNIX Operating System	Maurice J. Bach	Prentice Hall of India	1/e, 1994			
3	The Little Book of Semaphores	Allen B. Downey	Green Tea Press	1/e, 2016			

	Video Links (NPTEL, SWAYAM)					
No.	No. Link ID					
1	https://archive.nptel.ac.in/courses/106/105/106105214/					
2	https://www.youtube.com/playlist?list=PLDW872573QAb4bj0URobvQTD41IV6gRkx					

COMPUTER ORGANIZATION AND ARCHITECTURE

(Common to CS/CD/CR/CA/AD/CB/CN/CC/CU/CG)

Course Code	PBCST404	CIE Marks	60
Teaching Hours/Week (L: T:P: R)	3:0:0:1	ESE Marks	40
Credits	4	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	GAEST305	Course Type	Theory

Course Objectives

- 1. Introduce principles of computer organization and the basic architectural concepts using RISC
- 2. Introduce the concepts of microarchitecture, memory systems, and I/O systems.

Module No.	Syllabus Description	Contact Hours
1	Basic Structure of computers: - Functional units - Basic operational concepts; Memory map; Endianness. CISC vs RISC architectures: - RISC Introduction - Assembly Language, Assembler directives, Assembling. Programming concepts - Program flow, Branching, Conditional statements, Loops, Arrays, Function calls; Instruction execution cycle. Machine language - Instructions, addressing modes, Stored program concept. Evolution of the RISC Architecture.	11
2	Microarchitecture - Introduction; Performance analysis; Single-Cycle Processor - Single Cycle Datapath, Single Cycle Control; Pipelined Processor - Pipelined Data Path, Pipelined Control: Hazards, Solving Data/Control Hazards, Performance Analysis.	11
3	Memory Systems: Introduction; performance analysis; Caches - basic concepts, Cache mapping, Cache replacement, Multiple-Level Caches, Reducing Miss Rate, Write Policy; Virtual Memory - Address Translation; Page Table; Translation Lookaside Buffer; Memory Protection.	11
4	Input / Output - External Devices; I/O Modules; Programmed I/O, Interrupt Driven I/O; Direct Memory Access; Embedded I/O Systems - Embedded I/O, General Purpose I/O, Serial I/O, Other Peripherals.	11

Suggestion on Project Topics

Use simulators such as Ripes (https://github.com/mortbopet/Ripes) / GEM5 (https://www.gem5.org/) implement components of computer systems such as Various Cache organization and study the effect, Solutions to hazards, TLBs.

Course Assessment Method (CIE: 60 marks, ESE: 40 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Project	Internal Ex-1	Internal Ex-2	Total
5	30	12.5	12.5	60

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
2 Questions from each	2 questions will be given from each module, out of which 1	
module.	question should be answered. Each question can have a	
Total of 8 Questions, each	maximum of 2 subdivisions. Each question carries 6 marks.	40
carrying 2 marks	(4x6 = 24 marks)	
(8x2 =16 marks)		

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Identify the basic structure and functional units of a digital computer and the features of RISC architecture.	K2
CO2	Experiment with the single cycle processor, pipelining, and the associated problems.	К3
CO3	Utilize the memory organization in modern computer systems.	К3
CO4	Experiment with the I/O organization of a digital computer.	К3

Note: K1-Remember, K2-Understand, K3-Apply, K4-Analyse, K5-Evaluate, K6-Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	3	3								3
CO3	3	3	3	3								3
CO4	3	3	3	3								3

	Text Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Digital Design and Computer Architecture - RISC-V Edition	Sarah L. Harris, David Harris	Morgan Kaufmann	1/e, 2022			
2	Computer Organization and Architecture Designing for Performance	William Stallings	Pearson	9/e, 2013			

	Reference Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Computer Organization and Design: The Hardware/Software Interface: RISC-V Edition	David A. Patterson John L. Hennessy	Morgan Kaufaman	1/e,2018			
2	Computer Organization and Embedded Systems	Carl Hamacher, Zvonko Vranesic, Safwat Zaky, Naraig Manjikian	McGraw Hil	6/e, 2012			
3	Modern Computer Architecture and Organization	Jim Ledin	Packt Publishing	1/e,2020			

	Video Links (NPTEL, SWAYAM)					
No.	Link ID					
1	https://archive.nptel.ac.in/courses/106/105/106105163/					
2	https://archive.nptel.ac.in/courses/106/106/106106166/					

PBL Course Elements

L: Lecture	R: Project (1 Hr.), 2 Faculty Members				
(3 Hrs.)	Tutorial	Practical	Presentation		
Lecture delivery	Project identification	Simulation/ Laboratory Work/ Workshops	Presentation (Progress and Final Presentations)		
Group discussion	Project Analysis	Data Collection	Evaluation		
Question answer Sessions/ Brainstorming Sessions	Analytical thinking and self-learning	Testing	Project Milestone Reviews, Feedback, Project reformation (If required)		
Guest Speakers (Industry Experts)	Case Study/ Field Survey Report	Prototyping	Poster Presentation/ Video Presentation: Students present their results in a 2 to 5 minutes video		

Assessment and Evaluation for Project Activity

Sl. No	Evaluation for	Allotted Marks
1	D ' (D1 ' 1D 1	IVIAI KS
1	Project Planning and Proposal	3
2	Contribution in Progress Presentations and Question Answer	4
	Sessions	
3	Involvement in the project work and Team Work	3
4	Execution and Implementation	10
5	Final Presentations	5
6	Project Quality, Innovation and Creativity	3
	Total	30

1. Project Planning and Proposal (5 Marks)

- Clarity and feasibility of the project plan
- Research and background understanding
- Defined objectives and methodology

2. Contribution in Progress Presentation and Question Answer Sessions (4 Marks)

- Individual contribution to the presentation
- Effectiveness in answering questions and handling feedback

3. Involvement in the Project Work and Team Work (3 Marks)

- Active participation and individual contribution
- Teamwork and collaboration

4. Execution and Implementation (10 Marks)

- Adherence to the project timeline and milestones
- Application of theoretical knowledge and problem-solving
- Final Result

5. Final Presentation (5 Marks)

- Quality and clarity of the overall presentation
- Individual contribution to the presentation
- Effectiveness in answering questions

6. Project Quality, Innovation, and Creativity (3 Marks)

- Overall quality and technical excellence of the project
- Innovation and originality in the project
- Creativity in solutions and approaches

SOFTWARE ENGINEERING

(Common to CS/CD/CM/CR/CA/AD/AM/CB/CN/CU/CI)

Course Code	PECST411	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- 1. To Provide fundamental knowledge in the Software Development Process including Software Development, Object Oriented Design, Project Management concepts and technology trends.
- 2. To enable the learners to apply state of the art industry practices in Software development.

Module No.	Syllabus Description	Contact Hours
1	Introduction to Software Engineering and Process Models - Software engineering, Software characteristics and types, Layers of Software Engineering-Process, Methods, Tools and Quality focus. Software Process models - Waterfall, Prototype, Spiral, Incremental, Agile model - Values and Principles. Requirement engineering - Functional, Non-functional, System and User requirements. Requirement elicitation techniques, Requirement validation, Feasibility analysis and its types, SRS document characteristics and its structure.	9
2	Software design - Software architecture and its importance, Software architecture patterns: Component and Connector, Layered, Repository, Client-Server, Publish-Subscribe, Functional independence – Coupling and Cohesion Case study: Ariane launch failure Object Oriented Software Design - UML diagrams and relationships— Static and dynamic models, Class diagram, State diagram, Use case diagram, Sequence diagram Case Studies: Voice mail system, ATM Example Software pattern - Model View Controller, Creational Design Pattern types —	9

	Factory method, Abstract Factory method, Singleton method, Prototype	
	method, Builder method. Structural Design Pattern and its types - Adapter,	
	Bridge, Proxy, Composite, Decorator, Façade, Flyweight. Behavioral Design	
	Pattern	
	Coding, Testing and Maintenance:	
	Coding guidelines - Code review, Code walkthrough and Code inspection,	
	Code debugging and its methods.	
	Testing - Unit testing , Integration testing, System testing and its types, Black	
	box testing and White box testing, Regression testing	
3	Overview of DevOps and Code Management - Code management, DevOps	9
	automation, Continuous Integration, Delivery, and Deployment (CI/CD/CD),	
	Case study – Netflix.	
	Software maintenance and its types- Adaptive, Preventive, Corrective and	
	Perfective maintenance. Boehm's maintenance models (both legacy and non-	
	legacy)	
	Software Project Management - Project size metrics - LOC, Function points	
	and Object points. Cost estimation using Basic COCOMO.	
	Risk management: Risk and its types, Risk monitoring and management model	
	Software Project Management - Planning, Staffing, Organizational structures,	
	Scheduling using Gantt chart. Software Configuration Management and its	
4	phases, Software Quality Management - ISO 9000, CMM, Six Sigma for	9
	software engineering.	
	Cloud-based Software -Virtualisation and containers, Everything as a service	
	(IaaS, PaaS), Software as a service. Microservices Architecture - Microservices,	
	Microservices architecture, Microservice deployment.	

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
 2 Questions from each module. Total of 8 Questions, each carrying 3 marks (8x3 =24marks) 	 Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. (4x9 = 36 marks) 	60

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Plan the system requirements and recommend a suitable software process model	К3
CO2	Model various software patterns based on system requirements	К3
CO3	Apply testing and maintenance strategies on the developed software product to enhance quality	К3
CO4	Develop a software product based on cost, schedule and risk constraints	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	3									3
CO3	3	3	3									3
CO4	3	3	3									3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books									
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year						
1	Software Engineering: A practitioner's approach	Roger S. Pressman	McGraw-Hill International edition	8/e, 2014						
2	Software Engineering	Ian Sommerville	Addison-Wesley	10/e, 2015						
3	Design Patterns, Elements of Reusable Object Oriented Software	Erich Gamma,Richard Helm, Ralph Johnson,John Vlissides	Pearson Education Addison-Wesley	1/e, 2009						

	Reference Books								
Sl. No	Title of the Book	Title of the Book Name of the Author/s		Edition and Year					
1	Pankaj Jalote's Software Engineering: With Open Source and GenAI	Pankaj Jalote	Wiley India	1/e, 2024					
2	Software Engineering: A Primer	Waman S Jawadekar	Tata McGraw-Hill	1/e, 2008					
3	Object-Oriented Modeling and Design with UML	Michael Blaha, James Rumbaugh	Pearson Education.	2/e, 2007					
4	Software Engineering Foundations : A Software Science Perspective	Yingux Wang	Auerbach Publications	1/e, 2008					
5	Object-Oriented Design and Patterns	Cay Horstmann	Wiley India	2/e, 2005					
6	Engineering Software Products: An Introduction to Modern Software Engineering	Ian Sommerville	Pearson Education	1/e, 2020					

Module No.	Link ID
1	https://www.youtube.com/watch?v=Z6f9ckEElsU
2	https://www.youtube.com/watch?v=1xUz1fp23TQ
3	http://digimat.in/nptel/courses/video/106105150/L01.html
4	https://www.youtube.com/watch?v=v7KtPLhSMkU

PATTERN RECOGNITION

(Common to CS/CM/CA/AM/CN/CI)

Course Code	PECST412	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	GAMAT101, GAMAT201, GAMAT301, PCCST303	Course Type	Theory

Course Objectives:

- 1. To introduce a foundational understanding of the fundamental principles, theories, and methods used in pattern recognition.
- 2. To develop practical skills in implementing pattern recognition algorithms and techniques.

Module No.	Syllabus Description					
1	Introduction to Pattern Recognition - Definitions and applications of pattern recognition, Overview of pattern recognition systems (Text 2, Chapter 1) Statistical Pattern Recognition - Bayes decision theory, Parametric methods: Maximum likelihood estimation, Bayesian estimation (Text 1, Chapters 1, 2) Non-Parametric Methods - k-Nearest neighbors, Parzen windows (Text 2, Chapter 4)	9				
2	Feature Extraction and Selection Feature Extraction - Importance of feature extraction, Techniques for feature extraction: PCA, LDA, Feature extraction in image and signal processing (Text 1, Chapter 3) Feature Selection - Importance of feature selection, Techniques for feature	9				

	selection: filter methods, wrapper methods, Feature selection criteria (Text 2,	
	Chapter 6)	
	Supervised and Unsupervised Learning	
	Supervised Learning - Basics of supervised learning, Linear classifiers:	
	perceptron, logistic regression, Support vector machines (SVM) (Text 1,	
3	Chapter 4)	9
	Unsupervised Learning - Basics of unsupervised learning, Clustering	
	techniques: k-means, hierarchical clustering, Gaussian Mixture Models	
	(GMM) (Text 1, Chapter 9)	
	Advanced Topics and Applications	
	Hidden Markov Models (HMMs) - Basics of HMMs, HMM for sequence	
	modeling, Applications of HMMs in speech and language processing (Text	
	1, Chapter 13)	
4	Ensemble Methods - Basics of ensemble methods, Bagging, boosting, and	9
	random forests, Applications and case studies (Text 1, Chapter 14)	
	Applications and Case Studies - Real-world applications of pattern	
	recognition, Case studies in image and speech recognition, Future trends in	

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
2 Questions from each	• Each question carries 9 marks.	
module.	• Two questions will be given from each module, out	
Total of 8 Questions, each	of which 1 question should be answered.	
carrying 3 marks	• Each question can have a maximum of 3 sub	60
	divisions.	
(8x3 =24marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Understand and Explain fundamental Concepts of Pattern Recognition:	K2
CO2	Apply Classification and Clustering Techniques:	К3
CO3	Implement Feature Extraction and Dimensionality Reduction Techniques	К3
CO4	Apply Statistical and Non-Parametric Methods for Pattern Recognition	К3
CO5	Develop Solutions for Real-World Pattern Recognition Problems and Analyze Case Studies:	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	3		3							3
CO3	3	3	3		3							3
CO4	3	3	3		3							3
CO5	3	3	3			3		3				3

	Text Books								
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year					
1	Pattern Recognition and Machine Learning	Christopher M. Bishop	SPRINGER	1/e, 2009					
2	Pattern Classification	Richard Duda, Peter Hart, David Stork	Wiley	2/e, 2007					

	Reference Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	The Nature of Statistical Learning Theory	Vladimir Vapnik	Springer-Verlag New York Inc.	2/e, 2010			
2	The Elements of Statistical Learning	Jerome Friedman, Robert Tibshirani, Trevor Hastie	Springer-Verlag New York Inc	9/e, 2017			
3	Pattern Recognition	S.Theodoridis and K.Koutroumbas	Academic Press	4/e, 2009			

	Video Links (NPTEL, SWAYAM)					
Module No.	Link ID					
1	https://archive.nptel.ac.in/courses/117/105/117105101/					
2	https://archive.nptel.ac.in/courses/117/105/117105101/					
3	https://archive.nptel.ac.in/courses/117/105/117105101/					
4	https://archive.nptel.ac.in/courses/117/105/117105101/					

FUNCTIONAL PROGRAMMING

(Common to CS/CD/CM/CR/CA/AD/AM/CB/CN/CU/CG)

Course Code	PECST413	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	GYEST204	Course Type	Theory

Course Objectives:

- 1. To enable the learner write programs in a functional style and reason formally about functional programs;
- 2. To give the concepts of polymorphism and higher-order functions in Haskell to solve the

Module No.	Syllabus Description	Contact Hours
1	Introducing Functional Programming; Getting Started with Haskell and GHCi; Basic Types and Definitions; Designing and Writing Programs; Data Types, Tuples and Lists. [Text Ch. 1, 2, 3, 4, 5]	9
2	Programming with Lists; Defining Functions over Lists; Playing the Game: I/O in Haskell; Reasoning about Programs; [Text Ch. 6, 7, 8, 9]	9
3	Generalization: Patterns of Computation; Higher-order Functions; Developing Higher-order Programs; Overloading, Type Classes and Type Checking. [Text Ch. 10 11, 12, 13]	9
4	Algebraic Types; Case Study - Huffman Codes; Abstract Data Types; Lazy Programming; Time and Space Behaviour. [Text Ch. 15, 16, 17, 20]	9

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
 2 Questions from each module. Total of 8 Questions, each carrying 3 marks (8x3 =24 marks) 	 Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 subdivisions. (4x9 = 36 marks) 	60

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Write computer programs in a functional style.	K2
CO2	Reason formally about functional programs and develop programs using lists.	К3
CO3	Use patterns of computation and higher-order functions.	К3
CO4	Reason informally about the time and space complexity of programs.	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	2			3							3
CO2	3	3	3		3							3
CO3	3	3	3		3							3
CO4	3	3	3		3							3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books					
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year		
1	HASKELL : The Craft of Functional Programming	Simon Thompson	Addison Wesley	3/e, 2023		

	Reference Books							
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year				
1	Thinking Functionally with Haskell	Richard Bird	Cambridge University Press	1/e, 2015				
2	Programming in Haskell	Graham Hutton	Cambridge University Press	2/e, 2023				
3	Real World Haskell	Bryan O'Sullivan, John Goerzen, Donald Bruce Stewart	O'Reilly	1/e, 2008				

	Video Links (NPTEL, SWAYAM)				
No.	Link ID				
1	https://archive.nptel.ac.in/courses/106/106/106106137/				

CODING THEORY

(Common to CS/CM/AM/CI)

Course Code	PECST414	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- 1. To introduce students to some of the classical methods in coding theory
- 2. To give the concept of code construction through the mathematical foundations and examples.

Module No.	Syllabus Description	Contact Hours
1	Binary block codes, Minimum distance, Error-detecting capability and error-correcting capability. Introduction to linear block codes, generator matrix and parity check matrix. Properties of linear block codes: Syndrome, error detection. Distance properties of linear block codes. Single parity check codes, Hamming codes, Reed Muller codes.	9
2	Cyclic Codes: Generator and Parity-Check Matrices of Cyclic Codes. Encoding of Cyclic Codes, Syndrome Computation and Error Detection, Decoding of Cyclic Codes, Cyclic Hamming Codes, Shortened Cyclic Codes	9
3	Convolutional codes: Encoding, state diagram, trellis diagram, Classification, realization, distance properties. Viterbi algorithm, BCJR algorithm. Performance bounds for convolutional codes	9
4	Turbo codes: Turbo decoding, Distance properties of turbo codes, Convergence of turbo codes. Automatic repeat request schemes. Applications of linear codes	9

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
• 2 Questions from each	Each question carries 9 marks.	
module.	Two questions will be given from each module, out	
• Total of 8 Questions, each	of which 1 question should be answered.	
carrying 3 marks	• Each question can have a maximum of 3	60
	subdivisions.	
(8x3 =24 marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Construct the encoder and decoder of linear block codes	К3
CO2	Understand the concept of error correction coding	К2
CO3	Understand the implementation of cyclic codes	К2
CO4	Apply Viterbi algorithm for decoding convolutional codes	К3
CO5	Experiment with turbo codes using iterative map and BCJR algorithm	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									2
CO2	3	3	3	2								2
CO3	3	3	3	2								2
CO4	3	3	3									2
CO5	3	3	3	2								

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books							
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year				
1	Error Control Coding	Shu Lin and Daniel J. Costello, Jr.	PHI	2/e, 2004				
2	Error Correction Coding	Todd K. Moon	Wiley-Interscience	1/e, 2006				

		Reference Books		
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	The Theory of Error-Correcting Codes	F. J. MacWilliams, N. J. A. Sloane	North-Holland, Amsterdam	1/e, 1977
2	Algebraic Codes for Data Transmission	R. E. Blahut	Cambridge University Press	1/e, 2003
3	Fundamentals of Error- Correcting Codes	Cary W. Huffman, Vera Pless	Cambridge University Press	1/e, 2003

	Video Links (NPTEL, SWAYAM)					
Mod. No.	Link ID					
1	https://archive.nptel.ac.in/courses/108/104/108104092/					
2	https://nptel.ac.in/courses/108102117					
3	https://archive.nptel.ac.in/courses/108/104/108104092/					
4	https://archive.nptel.ac.in/courses/108/104/108104092/					

SIGNALS AND SYSTEMS

(Common to CS/CD/CM/CA/AM/CB/CN/CU/CI)

Course Code	PECST416	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- 1. To teach the concept of a Discrete Time (DT) signal
- **2.** To enable the learner to analyze the spectral information of any DT signal and its transformed version.
- **3.** To provide the learner the concepts of a DT system, how it behaves to an arbitrary input, and also to analyze the behaviour of a given DT system based on z-transform

Module No.	Syllabus Description	Contact Hours		
	1D Signals - A general introduction to real time signals - CT and DT signals,			
	Sinusoids, Spectrum representation, Sampling and Aliasing (Concept only),			
	Analog frequency and Digital frequency.			
	Elementary sequences- Real Sinusoidal Sequences, Complex Exponential			
	Sequences Unit impulse, step and ramp sequences, Representation of			
	discrete time signals- (Graphical representation, Functional representation,			
	Sequence representation)			
	Properties of DT Signals - Even and Odd, Periodic and non periodic signal,			
1	Energy and Power signals. Periodicity and Symmetry property of DT signals,	8		
	support of sequences, Bounded Sequences.			
	Operations on Signals - Time shifting (Translation), Time Reversal			
	(Reflection), Time scaling - Upsampling and downsampling			
	DTFS - Determining the Fourier-Series Representation of a Sequence,			
	Properties of Discrete-Time Fourier Series - Linearity, Translation (Time			
	Shifting), Modulation (Frequency Shifting), Reflection (Time Reversal),			
	Conjugation, Duality, Multiplication, Parseval's Relation, Even/Odd			
	symmetry, Real sequence.			

	(Practice of Visualization of a discrete time signal and operations on the DT					
	signal using python. Demonstration of sampling and reconstruction using					
	Python/Matlab.)					
	Discrete-Time Fourier Transform for Aperiodic Sequences - Properties of the					
	Discrete-Time Fourier Transform (Periodicity, Linearity, Translation (Time					
	Shifting), Modulation (Frequency-Domain Shifting), Conjugation, Time					
	Reversal, Convolution, Multiplication, Frequency-Domain Differentiation,					
2	Differencing, Parseval's theorem, Even/Odd symmetry, real sequences)	10				
	DTFT of periodic sequences - Frequency Spectra of Sequences, Bandwidth of					
	Sequences, Energy density spectra, Characterizing LTI Systems Using the					
	Fourier Transform.					
	Discrete time systems - Block diagram representation and mathematical					
	representation of discrete-time systems-Some common elements of Discrete-					
	time systems (adder, constant multiplier, signal multiplier, unit delay, unit					
	advance), Recursive DT systems and non recursive discrete time systems,					
	Relaxed system, Linearity and time invariance property of a DT system.					
3	Discrete time LTI systems - Discrete time convolution, Properties of	9				
	Convolution, Characterizing LTI Systems and Convolution - Impulse					
	response of an LTI system, Difference equation, Properties of an LTI system -					
	Causality, Memory, Invertibility, BIBO Stability, Eigen Sequences/ eigen					
	functions for discrete-Time LTI Systems.					
	Z transform - motivation for z transform, Relationship Between z Transform					
	and Discrete-Time Fourier Transform, Region of Convergence for the z					
	Transform.					
	Properties of z transform - Translation (Time Shifting), Complex Modulation					
	(z-Domain Scaling), Conjugation, Time Reversal, Upsampling (Time					
	Expansion, Downsampling, Convolution, z-Domain Differentiation,					
4	Differencing, Initial and Final Value Theorems	9				
	Determination of the Inverse z Transform					
	LTI systems and difference equations, Characterizing LTI systems using z					
	transform, Transfer function of an LTI system. Solving Difference Equations					
	Using the Unilateral z Transform					
	Block Diagram Representation of Discrete-Time LTI Systems,					
	Interconnection of LTI systems.					

(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	
 2 Questions from each module. Total of 8 Questions, each carrying 3 marks 	 Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. 	60
(8x3 = 24marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Bloom's Knowledge Level (KL)		
CO1	Demonstrate the concept and different types of DT signals and the effect of different operations on the signals.	K2	
CO2	CO2 Explain how DTFS can be used to represent a periodic DT signal.		
CO3	Apply the concept of DTFT for an aperiodic signal to determine the frequency spectrum.	К3	
CO4	Utilize the properties of a DT system based on its impulse response and z transform.	К3	
CO5	Identify the response of a DT LTI system to an arbitrary input sequence.	К3	

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	3	3								3
CO3	3	3	2	2								3
CO4	3	3	3	3								3
CO5	3	3	3	3								3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Signals and Systems	Michael D. Adams	University of Victoria, British Columbia, Canada	3/e 2020			
2	Signals and systems	Barry Van Veen, Simon Haykins	Wiley	2/e, 2007			
3	Signals and systems	A Nagoor Khani	McGraw Hill	2/e, 2022			

	Reference Books					
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year		
1	Fundamentals of Signals and Systems Using the Web and MATLAB		Pearson	3/e, 2014		

	Video Links (NPTEL, SWAYAM)						
No.	No. Link ID						
1	https://archive.nptel.ac.in/courses/108/104/108104100/						
2	https://archive.nptel.ac.in/courses/108/106/108106163/						

SOFT COMPUTING

(Common to CS/CD/CM/CR/CA/AD/AI/AM/CB/CN/CI)

Course Code	PECST417	CIE Marks	40
Teaching Hours/Week (L:T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- 1. To give exposure on soft computing, various types of soft computing techniques, and applications of soft computing
- 2. To impart solid foundations on Neural Networks, its architecture, functions and various algorithms involved, Fuzzy Logic, various fuzzy systems and their functions, and Genetic algorithms, its applications and advances.

Module	Syllabus Description	
No.		
1	Introduction to Soft Computing. Difference between Hard Computing & Soft Computing. Applications of Soft Computing. Artificial Neurons Vs Biological Neurons. Basic models of artificial neural networks — Connections, Learning, Activation Functions. McCulloch and Pitts Neuron. Hebb network, Perceptron Networks— Learning rule, Training and testing algorithm. Adaptive Linear Neuron— Architecture, Training and testing algorithm.	10
2	Fuzzy logic, Fuzzy sets – Properties, Fuzzy membership functions, Features of Fuzzy membership functions. operations on fuzzy set. Linguistic variables, Linguistic hedges Fuzzy Relations, Fuzzy If-Then Rules, Fuzzification, Defuzzification— Lamda cuts, Defuzzification methods. Fuzzy Inference mechanism - Mamdani and Sugeno types.	9
3	Evolutionary Computing, Terminologies of Evolutionary Computing, Concepts of genetic algorithm. Operators in genetic algorithm - coding,	8

	selection, cross over, mutation. Stopping condition for genetic algorithm.			
4	Multi-objective optimization problem. Principles of Multi-objective optimization, Dominance and pareto-optimality. Optimality conditions. Collective Systems, Biological Self-Organization, Particle Swarm Optimization, Ant Colony Optimization, Swarm Robotics.	9		

Course Assessment Method

(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
2 Questions from each	Each question carries 9 marks.	
module.	Two questions will be given from each module, out	
• Total of 8 Questions, each	of which 1 question should be answered.	
carrying 3 marks	• Each question can have a maximum of 3	
	subdivisions.	
(8x3 =24 marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Describe the techniques used in soft computing and outline the fundamental models of artificial neural networks	К2
CO2	Solve practical problems using neural networks	К3
CO3	Illustrate the operations, model, and applications of fuzzy logic.	К3
CO4	Illustrate the concepts of evolutionary algorithms such as Genetic Algorithm	К3
CO5	Describe the concepts of multi-objective optimization models and collective systems.	К2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	2	2								3
CO3	3	3	3	2								3
CO4	3	3	2	2								3
CO5	3	3	3									3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Principles of Soft Computing	S.N.Sivanandam, S.N. Deepa	John Wiley & Sons.	3/e, 2018			
2	Multi-objective Optimization using Evolutionary Algorithms	Kalyanmoy Deb,	John Wiley & Sons	1/e, 2009			
3	Computational intelligence: synergies of fuzzy logic, neural networks and evolutionary computing.	Siddique N, Adeli H.	John Wiley & Sons	1/e, 2013			
4	Bio-inspired artificial intelligence: theories, methods, and technologies.	Floreano D, Mattiussi C.	MIT press; 2008 Aug 22.	1/e, 2023			

	Reference Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Fuzzy Logic with Engineering Applications	Timothy J Ross,	John Wiley & Sons,	3/e, 2011			
2	Neural Networks, Fuzzy Logic & Genetic Algorithms Synthesis and Applications	T.S.Rajasekaran, G.A.Vijaylakshmi Pai	Prentice-Hall India	1/e, 2003			
3	Neural Networks- A Comprehensive Foundation	Simon Haykin	Pearson Education	2/e, 1997			
4	Fuzzy Set Theory & Its Applications	Zimmermann H. J,	Allied Publishers Ltd.	4/e, 2001			

	Video Links (NPTEL, SWAYAM)
No.	Link ID
1	https://archive.nptel.ac.in/courses/106/105/106105173/

COMPUTATIONAL GEOMETRY

(Common to CS/CM)

Course Code	PECST418	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	GAMAT101, PCCST303	Course Type	Theory

Course Objectives:

- 1. To develop a solid understanding of the fundamental principles, techniques, and algorithms used in computational geometry, including geometric data structures, convex hulls, Voronoi diagrams, and Delaunay triangulations.
- 2. To equip students with the skills to apply computational geometry algorithms and techniques to address real-world problems in areas such as computer graphics, robotics, and geographic information systems (GIS).

Module No.	Syllabus Description	Contact Hours
1	Introduction to Computational Geometry: Basics of Computational Geometry - Introduction and applications of computational geometry, Geometric objects, and their representations, Basic geometric primitives: points, lines, segments, polygons (Text 1, Chapters 1, 2) Convex Hulls - Definition and properties of convex hulls, Graham's scan algorithm, Jarvis's march (gift wrapping) algorithm, Divide and conquer algorithm for convex hulls (Text 2, Section 33.3) Line Segment Intersection - Problem definition and applications, Plane	9

	sweep algorithm, Bentley-Ottmann algorithm (Text 3, Chapter 7)	
	Polygon Triangulation and Voronoi Diagrams:-	
	Polygon Triangulation - Definition and applications, Triangulation of monotone polygons, Ear clipping method, Chazelle's algorithm (Text 1, Chapter 3)	
2	Voronoi Diagrams - Definition and properties, Incremental construction algorithm, Fortune's sweep line algorithm (Text 1, Chapter 7)	9
	Delaunay Triangulations - Definition and properties, Relationship with Voronoi diagrams, Bowyer-Watson algorithm, Lawson's flip algorithm (Text 1, Chapter 9)	
	Range Searching and Point Location :-	
	Range Searching - Problem definition and applications, 1-dimensional range searching, K-dimensional range trees, Fractional cascading (Text 1, Chapter 5)	
3	Point Location - Problem definition and applications, Trapezoidal map and randomized incremental algorithm, Kirkpatrick's point location algorithm (Text 1, Chapter 6)	9
	Binary Space Partitioning - Definition and applications, BSP trees construction and properties, Use in computer graphics and collision detection (Text 1, Chapter 12)	
	Advanced Topics and Applications :-	
	Arrangements of Lines and Duality - Arrangements of lines and complexity, Zone theorem, Duality transform and its applications (Text 1, Chapter 8)	
4	Motion Planning and Geometric Optimization - Problem definition and applications, Visibility graphs and shortest path problems, Art gallery problem, Linear programming in geometry (Text 1, Chapters 10, 11)	
	Computational Geometry in Practice - Computational geometry libraries and software, Applications in robotics, computer graphics, GIS (Text 3, Chapters 9, 10)	

Course Assessment Method

(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
• 2 Questions from each	Each question carries 9 marks.	
module.	Two questions will be given from each module, out	
• Total of 8 Questions, each	of which 1 question should be answered.	60
carrying 3 marks	• Each question can have a maximum of 3	60
	subdivisions.	
(8x3 =24 marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Understand Fundamental Concepts and Applications of Computational Geometry	К2
CO2	Apply Algorithms for Convex Hulls and Line Segment Intersection Algorithms	К3
CO3	Perform Polygon Triangulation and Understand Voronoi Diagrams	К3
CO4	Build Delaunay Triangulations and Range Searching Techniques	К3
CO5	Apply Advanced Computational Geometry Techniques and Algorithms	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3									3
CO2	3	3	3		3							3
CO3	3	3	3		3							3
CO4	3	3	3		3							3
CO5	3	3	3	3	3							3

	Text Books								
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year					
1	Computational Geometry: Algorithms and Applications	Mark de Berg, Otfried Cheong, Marc van Kreveld, and Mark Overmars	Springer India	3/e, 2011					
2	Introduction to Algorithms	Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, and Clifford Stein	MIT Press	4/e, 2022					
3	Computational Geometry in C	Joseph O'Rourke	Cambridge University Press	2/e, 1998					

	Reference Books							
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year				
1	Discrete and Computational Geometry Hardcover	Joseph O'Rourke , Satyan L. Devadoss	Princeton University Press	1/e,2011				
2	Computational Geometry: An Introduction	Franco P. Preparata, Michael I. Shamos	Springer-Verlag New York Inc	5/e, 1993				
3	Geometric Algorithms and Combinatorial Optimization	Martin Grötschel, Laszlo Lovasz, Alexander Schrijver	Springer-Verlag Berlin and Heidelberg GmbH & Co. K	2/e, 1993				

	Video Links (NPTEL, SWAYAM)						
Module No.	Link ID						
1	https://archive.nptel.ac.in/courses/106/102/106102011/						
2	https://archive.nptel.ac.in/courses/106/102/106102011/						
3	https://archive.nptel.ac.in/courses/106/102/106102011/						
4	https://archive.nptel.ac.in/courses/106/102/106102011/						

CYBER ETHICS, PRIVACY AND LEGAL ISSUES

(Common to CS/CM/CA/AM)

Course Code	PECST419	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- To provide a comprehensive understanding of the fundamental concepts of cyberspace and cyber law, enabling them to analyse and address the challenges of regulating and securing the digital world
- **2.** To explain cybercrime, intellectual property, cyber ethics, and ethical issues in emerging technologies, enabling them to tackle related challenges effectively.
- **3.** To give awareness on data protection and privacy in cyberspace, and to learn legal frameworks protecting privacy, enabling them to address and manage privacy-related challenges effectively

Module No.	Syllabus Description	Contact Hours
1	Fundamentals of Cyber Law and Cyber Space: Introduction to cyber law, Contract aspects in cyber law, Security aspects of cyber law, Intellectual property aspects in cyber law and Evidence aspects in cyber law, Criminal aspects in cyber law, Need for Indian cyber law Cyberspace- Web space, Web hosting and web development agreement, Legal and Technological Significance of domain Names, Internet as a tool for global access.	9
2	Cyber crime and Cyber Ethics:- Cyber crime and Cyber Ethics:- Introduction to cybercrime- Definition and Origins of Cyber crime- Classifications of Cybercrime, Cyber Offences- Strategic Attacks, Types of Attacks, Security Challenges Faced by Mobile Devices. Organizational Measures for Handling Mobile Phones. Cyber Ethics: The Importance of Cyber Law, Significance of Cyber Ethics, Need for Cyber regulations Based on Cyber Ethics, Ethics in Information	9

	society, Artificial Intelligence Ethics- Ethical Issues in AI and core Principles, Block chain Ethics- Definition and Description.	
3	Data Protection and Privacy Concerns in Cyberspace: Need to protect data in cyberspace, Types of data, Legal framework of data protection, Data protection bill -an overview, GDPR, Concept of privacy, Privacy concerns of cyberspace, Constitutional framework of privacy, Judicial interpretation of privacy in India, Privacy Law and Regulation, Organizational Response, Privacy and Data Surveillance	9
4	Security Policies and Information Technology Act Need for an Information Security policy, Information Security Standards-ISO, Introducing various security policies and their review process, Information Technology Act, 2000, Penalties, Adjudication and appeals under the IT Act,2000, Offences under IT Act, 2000, Right to Information Act, 2005, IT Act,2008 and its amendments.	9

Course Assessment Method (CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
• 2 Questions from each	Each question carries 9 marks.	
module.	Two questions will be given from each module, out	
• Total of 8 Questions, each	of which 1 question should be answered.	
carrying 3 marks	• Each question can have a maximum of 3	60
	subdivisions.	
(8x3 =24 marks)	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Describe the concepts of cyber law and the various components and challenges associated with cyberspace.	К2
CO2	Discuss the concept of cybercrime and computer crime, the challenges faced by law enforcement, and the importance of intellectual property in the digital age.	K2
CO3	Explain the importance of cyber law and ethics, the need for regulations, and the ethical considerations in emerging technologies like AI and blockchain.	K2
CO4	Identify data protection and privacy issues in cyberspace and describe various laws and regulations to address these challenges in the digital age, ensuring comprehensive privacy protection and compliance.	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	2	2										2
CO2	2	2										2
CO3	2	2										2
CO4	2	2										2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Reference Books									
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year					
1	Cyber Security and Cyber Laws	Nilakshi Jain, Ramesh Menon	Wiley	1/e, 2020					
2	Cyber Security understanding Cyber Crimes, Computer Forensics and Legal Perspectives	Sumit Belapure , Nina Godbole	Wiley India Pvt.Ltd.	1/e, 2011					
3	Cyber Ethics 4.0: Serving Humanity with Values	Christoph Stückelberger, Pavan Duggal	Globethics	1/e, 2018					
4	Cyber Laws: Intellectual property & E Commerce, Security	K. Kumar	Dominant Publisher	1/e,2011					
5	Introduction to Information Security and Cyber Laws	Surya Prakash Tripathi, Ritendra Goel, Praveen Kumar Shukla	Dreamtech Press	1/e, 2014					
6	Cyber Law: The Law of the Internet and Information Technology	Craig B	Pearson Education	First Edition,201					

	Video Links (NPTEL, SWAYAM)								
No.	Link ID								
1	https://www.wbnsou.ac.in/NSOU-MOOC/mooc_cyber_security.shtml								
2	https://onlinecourses.swayam2.ac.in/cec22_lw07/preview								
3	https://www.coursera.org/learn/data-security-privacy#modules								
4	https://jurnal.fh.unila.ac.id/index.php/fiat/article/download/2667/1961/12044								

VLSI DESIGN

(Common to CS/CN/CI)

Course Code	PECST415	CIE Marks	40
Teaching Hours/Week	3:0:0:0	ESE Marks	60
(L: T:P: R)			
Credits	5/3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	GAEST305	Course Type	Elective

Course Objectives:

- 1. To impart the key concepts of MOS technology including characteristics of CMOS and its application in digital VLSI circuits to design basic CMOS logic gates.
- 2. To impart the key concepts of Integrated Circuit Design and introduce various design flows.
- **3.** To equip the learner to implement both combinational and sequential logic circuits using both semi-custom and FPGA design flow.

Module No.	Syllabus Description	Contact Hours
1	CMOS Fundamentals for Digital VLSI Design: CPN junction, MOS transistor theory and operation, PMOS, NMOS, CMOS, CMOS Inverter, Voltage Transfer Curve, CMOS logic gates, Tristate Inverter, Tristate buffer. Combinational Circuits Timing - Rise Time, Fall time, Propagation Delay. Introduction to sequential logic circuits, flip-flops and latches, Timing analysis - Set-up time, Hold Time, Propagation Delay, Frequency of Operation, Static and Dynamic Timing Analysis, Pipelining	9
2	Introduction to Integrated Circuits (ICs): CMOS fabrication process overview- Photolithography, Structure of an Integrated Circuit, Types of Design flow - Custom design, Semi-custom design, array based design. A System Perspective, Hardware – Software Partitioning, example Video compression, Functional Specification to RTL, Behavioural Synthesis.	9

	Semi-custom Design flow				
3	Abstraction in VLSI Design Flow- Gajski-Kuhn's Y-chart, Hardware design using hardware description Languages, Design Verification- Simulation using Testbench, Property Checking, Equivalence Checking, Static Timing				
	Analysis, Logic Synthesis, Physical Design- Min-cut Partitioning, Floor plan-, Global and Detailed Placement, Global and Detailed Routing, Micro project*				
4	Finite State Machines (FSMs): Mealy and Moore models. Verilog HDL Design and implementation of RISC stored programmed Machine. Field Programmable Gate Arrays (FPGAs): FPGA Architecture-Programming Technology, Programmable logical blocks, Programmable Interconnects, Programmable I/O blocks, FPGA Design Flow, SoC Design on FPGA, Micro project*.	9			

^{*} Micro-project on FPGA / Semi-Custom Flow.

Course Assessment Method

(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Internal Ex	Evaluate	Analyse	Total
5	15	10	10	40

Criteria for Evaluation (Evaluate and Analyse): 20 marks

- Ability to capture the specification and ability for RTL coding,
- Ability to analyze the circuit for resource utilization such as area consumption and power consumption. Analyze the circuit for timing violations. Optimize performance.

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
• 2 Questions from each	• 2 questions will be given from each	
module.	module, out of which 1 question should be	
• Total of 8 Questions,	answered.	
each carrying 3 marks	• Each question can have a maximum of 3	60
(8x3 =24marks)	sub divisions.	
	• Each question carries 9 marks.	
	(4x9 = 36 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Utilize the MOS Circuits and design basic circuits using CMOS.	К3
CO2	Explain IC design flow and design a system using hardware software co-design strategy.	К3
CO3	Design, simulate and implement systems design in HDL using semi- custom flow.	К4
CO4	Design, simulate and implement digital systems using programmable devices.	K4

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3		3							3
CO2	3	3	3		3							3
CO3	3	3	3		3							3
CO4	3	3	3		3							3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Introduction to VLSI Design Flow	Sneh Saurabh	Cambridge University Press	1/e, 2023			
2	Digital Integrated Circuits: A Design Perspective.	Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolic	Pearson Education	2/e, 2003			
2	Digital Systems Design Using Verilog	Charles H. Roth Jr., Lizy Kurian John, Beyeong Kil Lee,	CL Engineering	1/e, 2015			
3	Advanced Digital Design with the Verilog HDL	Micahel D. Ciletti		2/e, 2017			

	Reference Books					
Sl. No	Title of the Book	e of the Book Name of the Author/s		Edition and Year		
1	Digital Design and Computer Architecture - RISC-V Edition Sarah L. Harris, David Harris		Morgan Kaufmann	1/e, 2022		
2	Digital Design: With an Introduction to the Verilog HDL	M. Morris Mano, Michael D. Ciletti	Pearson India	5/e, 2012		
3	Verilog HDL – A guide to digital design & Synthesis	Samir Palnitkar	Pearson	2/e, 2003		
4	FPGA Based System Design	Wayne Wolf	Pearson	1/e, 2004		
5	Embedded Core Design with FPGAs	Zainalabedin Navabi	McGraw-Hill	1/e, 2006		

	Video Links (NPTEL, SWAYAM)				
No.	Link ID				
1	Introduction to Digital VLSI Design Flow, Introduction to Digital VLSI Design Flow, IIT Guwahati https://nptel.ac.in/courses/106103116				
2	Introduction to VLSI Design by Prof. S. Srinivasan , IIT Madras, https://nptel.ac.in/courses/117106092				
3	VLSI Physical Design by Prof. Indranil Sengupta, IIT Kharagpur, https://onlinecourses.nptel.ac.in/noc21_cs12/preview				
4	Digital System Design using PLDs and FPGAs , Prof. Kuruvilla Varghese from IISc Bangalore https://archive.nptel.ac.in/courses/117/108/117108040/				

ADVANCED DATA STRUCTURES

(Common to CS/CD/CM/CA/AM/CB/CN/CC/CU/CI/CG)

Course Code	PECST495	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	5/3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	PCCST303	Course type	Theory

Course Objectives:

- 1. To equip students with comprehensive knowledge of advanced data structures utilized in cutting-edge areas of computer science, including database management, cyber security, information retrieval, and networked systems.
- **2.** To prepare students to address challenges in emerging fields of computer science by applying advanced data structures to practical, real-world problems.

Module	Syllabus Description	Contact
No.	Syllabus Description	
1	Foundational Data Structures- Overview of Arrays and Linked Lists, implementation of pointers and objects, Representing rooted trees, Hashing - Hash Tables, Hash functions, Cuckoo Hashing; Bloom Filters - Count-Min Sketch, Applications to Networks - Click Stream Processing using Bloom Filters, Applications to Data Science - Heavy Hitters and count-min structures.	9
2	Advanced Tree Data Structures - Balanced Trees - AVL Trees (review), Red-Black Trees, Suffix Trees and Arrays, Segment Trees, Heaps and Related Structures - Binomial heap, Fibonacci Heaps, Merkle Trees, Applications to information Retrieval and WWW - AutoComplete using Tries.	9

3	Specialized Data Structures - Spatial Data Structures - Quadtree, K-D Trees (k-dimensional tree); R-trees; Temporal Data Structures- Persistence, Retroactivity; Search and Optimization Trees - Skip List, Tango Trees; Applications to Data Science - Approximate nearest neighbor search, Applications to information Retrieval and WWW, Posting List intersection.	9
4	Data Structure applications - Distributed and Parallel Data Structures - Distributed Hash Tables (DHTs); Consistent Hashing; Distributed BST; Data Compression and Transformations - Burrows-Wheeler Transform; Histogram; Wavelet Trees; Cryptographic Applications - Hashing.	9

Course Assessment Method

(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Internal Ex	Evaluate	Analyse	Total
5	15	10	10	40

Criteria for Evaluation (Evaluate and Analyze): 20 marks

Implement various real world problems using multiple suitable data structures and compare the performance.

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
 2 Questions from each module. Total of 8 Questions, each carrying 3 marks (8x3 =24 marks) 	 2 questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 subdivisions. Each question carries 9 marks. (4x9 = 36 marks) 	60

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Implement and use arrays, linked lists, rooted trees and hashing techniques in various programming scenarios.	К3
CO2	Design and implement advanced tree data structures for information retrieval.	К3
CO3	Use spatial and temporal data structures in data science problems.	К3
CO4	Analyze data structures in special scenarios such as distributed, parallel and data compression areas.	K5

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3	2	3						2	3
CO2	3	3	3	3	3						2	3
CO3	3	3	3	3	3						2	3
CO4	3	3	3	3	3						2	2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year	
1	Advanced Data Structures: Theory and Applications	Suman Saha, Shailendra Shukla	CRC Press	1/e, 2019	
2	Advanced Data Structures	Peter Brass	Cambridge University Press	1/e, 2008	
3	Introduction to Algorithms	Thomas H Cormen, Charles E Leiserson, Ronald L Rivest, Clifford Stein	MIT Press	4/e, 2022	
4	Fundamentals of Computer Algorithms Ellis Horowitz, SatrajSahani and Rajasekharam Universit		University Press	2/e, 2009	
5	Advanced Data Structures	Reema Thareja, S. Rama Sree	Oxford University Press	1/e, 2018	
6	Data Structures and Algorithm Analysis in C++,	Mark Allen Weiss	Pearson	2/e, 2004.	
7	Design and Analysis of Algorithms	M T Goodrich, Roberto Tamassia	Wiley	1/e, 2021	

	Video Links (NPTEL, SWAYAM)					
Module No.	Link ID					
1	https://web.stanford.edu/class/cs166/					

ECONOMICS FOR ENGINEERS

(Common to All Branches)

Course Code	UCHUT346	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	2:0:0:0	ESE Marks	50
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- 1. Understanding of finance and costing for engineering operation, budgetary planning and control
- 2. Provide fundamental concept of micro and macroeconomics related to engineering industry
- 3. Deliver the basic concepts of Value Engineering.

Module No.	Syllabus Description				
1	Basic Economics Concepts - Basic economic problems - Production Possibility Curve - Utility - Law of diminishing marginal utility - Law of Demand - Law of supply - Elasticity - measurement of elasticity and its applications - Equilibrium- Changes in demand and supply and its effects Production function - Law of variable proportion - Economies of Scale - Internal and External Economies - Cobb-Douglas Production Function	6			
2	Cost concepts – Social cost, private cost – Explicit and implicit cost – Sunk cost - Opportunity cost - short run cost curves - Revenue concepts Firms and their objectives – Types of firms – Markets - Perfect Competition – Monopoly - Monopolistic Competition - Oligopoly (features and equilibrium of a firm)	6			

3	Monetary System – Money – Functions - Central Banking –Inflation - Causes and Effects – Measures to Control Inflation - Monetary and Fiscal policies – Deflation Taxation – Direct and Indirect taxes (merits and demerits) - GST National income – Concepts - Circular Flow – Methods of Estimation and Difficulties - Stock Market – Functions- Problems faced by the Indian stock market-Demat Account and Trading Account – Stock market Indicators-SENSEX and NIFTY	6
4	Value Analysis and value Engineering - Cost Value, Exchange Value, Use Value, Esteem Value - Aims, Advantages and Application areas of Value Engineering - Value Engineering Procedure - Break-even Analysis - Cost-Benefit Analysis - Capital Budgeting - Process planning	6

Course Assessment Method (CIE: 50 marks, ESE: 50 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project Internal Examination-1 (Written) Internal Examination- 2 (Written)		Total	
10	15	12.5	12.5	50

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
Minimum 1 and	• 2 questions will be given from each module, out	
Maximum 2 Questions	of which 1 question should be answered.	
from each module.	Each question can have a maximum of 2 sub	
• Total of 6 Questions,	divisions.	50
each carrying 3 marks	• Each question carries 8 marks.	
(6x3 =18marks)	(4x8 = 32 marks)	

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Understand the fundamentals of various economic issues using laws and learn the concepts of demand, supply, elasticity and production function.	K2
CO2	Develop decision making capability by applying concepts relating to costs and revenue and acquire knowledge regarding the functioning of firms in different market situations.	К3
CO3	Outline the macroeconomic principles of monetary and fiscal systems, national income and stock market.	K2
CO4	Make use of the possibilities of value analysis and engineering, and solve simple business problems using break even analysis, cost benefit analysis and capital budgeting techniques.	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	-	-	-	-	-	1	-	-	-	-	1	-
CO2	-	-	-	-	-	1	1	-	-	-	1	-
CO3	-	-	-	-	1	-	-	-	-	-	2	-
CO4	-	-	-	-	1	1	-	-	-	_	2	-

	Text Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Managerial Economics	Geetika, Piyali Ghosh and Chodhury	Tata McGraw Hill,	2015			
2	Engineering Economy	H. G. Thuesen, W. J. Fabrycky	PHI	1966			
3	Engineering Economics	R. Paneerselvam	PHI	2012			

	Reference Books						
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year			
1	Engineering Economy	Leland Blank P.E, Anthony Tarquin P. E.	Mc Graw Hill	7 TH Edition			
2	Indian Financial System	Khan M. Y.	Tata McGraw Hill	2011			
3	Engineering Economics and analysis	Donald G. Newman, Jerome P. Lavelle	Engg. Press, Texas	2002			
4	Contemporary Engineering Economics	Chan S. Park	Prentice Hall of India Ltd	2001			

SEMESTER S3/S4

ENGINEERING ETHICS AND SUSTAINABLE DEVELOPMENT

Course Code	UCHUT347	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	2:0:0:0	ESE Marks	50
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

- 1. Equip with the knowledge and skills to make ethical decisions and implement gender-sensitive practices in their professional lives.
- 2. Develop a holistic and comprehensive interdisciplinary approach to understanding engineering ethics principles from a perspective of environment protection and sustainable development.
- 3. Develop the ability to find strategies for implementing sustainable engineering solutions.

Module No.	Syllabus Description				
1	Fundamentals of ethics - Personal vs. professional ethics, Civic Virtue, Respect for others, Profession and Professionalism, Ingenuity, diligence and responsibility, Integrity in design, development, and research domains, Plagiarism, a balanced outlook on law - challenges - case studies, Technology and digital revolution-Data, information, and knowledge, Cybertrust and cybersecurity, Data collection & management, High technologies: connecting people and places-accessibility and social impacts, Managing conflict, Collective bargaining, Confidentiality, Role of confidentiality in moral integrity, Codes of Ethics. Basic concepts in Gender Studies - sex, gender, sexuality, gender	6			

	spectrum: beyond the binary, gender identity, gender expression, gender	
	stereotypes, Gender disparity and discrimination in education,	
	employment and everyday life, History of women in Science & Technology,	
	Gendered technologies & innovations, Ethical values and practices in	
	connection with gender - equity, diversity & gender justice, Gender policy	
	and women/transgender empowerment initiatives.	
	Introduction to Environmental Ethics: Definition, importance and	
	historical development of environmental ethics, key philosophical theories	
	(anthropocentrism, biocentrism, ecocentrism). Sustainable Engineering	
	Principles: Definition and scope, triple bottom line (economic, social and	
	environmental sustainability), life cycle analysis and sustainability metrics.	
2	Ecosystems and Biodiversity: Basics of ecosystems and their functions,	6
	Importance of biodiversity and its conservation, Human impact on	
	ecosystems and biodiversity loss, An overview of various ecosystems in	
	Kerala/India, and its significance. Landscape and Urban Ecology:	
	Principles of landscape ecology, Urbanization and its environmental impact,	
	Sustainable urban planning and green infrastructure.	
	Hydrology and Water Management: Basics of hydrology and water cycle,	
	Water scarcity and pollution issues, Sustainable water management practices,	
	Environmental flow, disruptions and disasters. Zero Waste Concepts and	
	Practices: Definition of zero waste and its principles, Strategies for waste	
	reduction, reuse, reduce and recycling, Case studies of successful zero waste	
	initiatives. Circular Economy and Degrowth: Introduction to the circular	
3	economy model, Differences between linear and circular economies,	6
	degrowth principles, Strategies for implementing circular economy practices	
	and degrowth principles in engineering. Mobility and Sustainable	
	Transportation: Impacts of transportation on the environment and climate,	
	Basic tenets of a Sustainable Transportation design, Sustainable urban	
	mobility solutions, Integrated mobility systems, E-Mobility, Existing and	
	upcoming models of sustainable mobility solutions.	
4	Renewable Energy and Sustainable Technologies: Overview of renewable	6
_	energy sources (solar, wind, hydro, biomass), Sustainable technologies in	U

energy production and consumption, Challenges and opportunities in renewable energy adoption. Climate Change and Engineering Solutions: Basics of climate change science, Impact of climate change on natural and human systems, Kerala/India and the Climate crisis, Engineering solutions to mitigate, adapt and build resilience to climate change. Environmental Policies and Regulations: Overview of key environmental policies and regulations (national and international), Role of engineers in policy implementation and compliance, Ethical considerations in environmental policy-making. Case Studies and Future Directions: Analysis of real-world case studies, Emerging trends and future directions in environmental ethics and sustainability, Discussion on the role of engineers in promoting a sustainable future.

Course Assessment Method (CIE: 50 marks, ESE: 50)

Continuous Internal Evaluation Marks (CIE):

Continuous internal evaluation will be based on individual and group activities undertaken throughout the course and the portfolio created documenting their work and learning. The portfolio will include reflections, project reports, case studies, and all other relevant materials.

- The students should be grouped into groups of size 4 to 6 at the beginning of the semester. These groups can be the same ones they have formed in the previous semester.
- Activities are to be distributed between 2 class hours and 3 Self-study hours.
- The portfolio and reflective journal should be carried forward and displayed during the 7th Semester Seminar course as a part of the experience sharing regarding the skills developed through various courses.

Sl. No.	Item	Particulars	Group/I ndividua l (G/I)	Marks	
1	Reflective Journal	Weekly entries reflecting on what was learned, personal insights, and how it can be applied to local contexts.	I	5	
2	Micro project	1 a) Perform an Engineering Ethics Case Study analysis and prepare a report	G	8	
	(Detailed documentation of	1 b) Conduct a literature survey on 'Code of Ethics for Engineers' and prepare a sample code of ethics			
	the project, including methodologies, findings, and	2. Listen to a TED talk on a Gender-related topic, do a literature survey on that topic and make a report citing the relevant papers with a specific analysis of the Kerala context	G	5	
	reflections)	3. Undertake a project study based on the concepts of sustainable development* - Module II, Module III & Module IV	G	12	
3	Activities	2. One activity* each from Module II, Module III & Module IV	G	15	
4	Final Presentation	A comprehensive presentation summarising the key takeaways from the course, personal reflections, and proposed future actions based on the learnings.	G	5	
		Total Marks		50	

Evaluation Criteria:

- **Depth of Analysis**: Quality and depth of reflections and analysis in project reports and case studies.
- **Application of Concepts**: Ability to apply course concepts to real-world problems and local contexts.
- Creativity: Innovative approaches and creative solutions proposed in projects and reflections.
- **Presentation Skills**: Clarity, coherence, and professionalism in the final presentation.

^{*}Can be taken from the given sample activities/projects

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Develop the ability to apply the principles of engineering ethics in their professional life.	К3
CO2	Develop the ability to exercise gender-sensitive practices in their professional lives	K4
CO3	Develop the ability to explore contemporary environmental issues and sustainable practices.	K5
CO4	Develop the ability to analyse the role of engineers in promoting sustainability and climate resilience.	K4
CO5	Develop interest and skills in addressing pertinent environmental and climate-related challenges through a sustainable engineering approach.	К3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1						3	2	3	3	2		2
CO2		1				3	2	3	3	2		2
CO3						3	3	2	3	2		2
CO4		1				3	3	2	3	2		2
CO5						3	3	2	3	2		2

		Reference Books		
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Ethics in Engineering Practice and Research	Caroline Whitbeck	Cambridge University Press & Assessment	2nd edition & August 2011
2	Virtue Ethics and Professional Roles	Justin Oakley	Cambridge University Press & Assessment	November 2006
3	Sustainability Science	Bert J. M. de Vries	Cambridge University Press & Assessment	2nd edition & December 2023
4	Sustainable Engineering Principles and Practice	Bhavik R. Bakshi,	Cambridge University Press & Assessmen	2019
5	Engineering Ethics	M Govindarajan, S Natarajan and V S Senthil Kumar	PHI Learning Private Ltd, New Delhi	2012
6	Professional ethics and human values	RS Naagarazan	New age international (P) limited New Delhi	2006.
7	Ethics in Engineering	Mike W Martin and Roland Schinzinger,	Tata McGraw Hill Publishing Company Pvt Ltd, New Delhi	4" edition, 2014

Suggested Activities/Projects:

Module-II

- Write a reflection on a local environmental issue (e.g., plastic waste in Kerala backwaters or oceans) from different ethical perspectives (anthropocentric, biocentric, ecocentric).
- Write a life cycle analysis report of a common product used in Kerala (e.g., a coconut, bamboo or rubber-based product) and present findings on its sustainability.
- Create a sustainability report for a local business, assessing its environmental, social, and economic impacts
- Presentation on biodiversity in a nearby area (e.g., a local park, a wetland, mangroves, college campus etc) and propose conservation strategies to protect it.
- Develop a conservation plan for an endangered species found in Kerala.
- Analyze the green spaces in a local urban area and propose a plan to enhance urban ecology using native plants and sustainable design.
- Create a model of a sustainable urban landscape for a chosen locality in Kerala.

Module-III

- Study a local water body (e.g., a river or lake) for signs of pollution or natural flow disruption and suggest sustainable management and restoration practices.
- Analyse the effectiveness of water management in the college campus and propose improvements calculate the water footprint, how to reduce the footprint, how to increase supply through rainwater harvesting, and how to decrease the supply-demand ratio
- Implement a zero waste initiative on the college campus for one week and document the challenges and outcomes.
- Develop a waste audit report for the campus. Suggest a plan for a zero-waste approach.
- Create a circular economy model for a common product used in Kerala (e.g., coconut oil, cloth etc).
- Design a product or service based on circular economy and degrowth principles and present a business plan.
- Develop a plan to improve pedestrian and cycling infrastructure in a chosen locality in Kerala

Module-IV

- Evaluate the potential for installing solar panels on the college campus including cost-benefit analysis and feasibility study.
- Analyse the energy consumption patterns of the college campus and propose sustainable alternatives to reduce consumption What gadgets are being used? How can we reduce demand using energy-saving gadgets?
- Analyse a local infrastructure project for its climate resilience and suggest improvements.
- Analyse a specific environmental regulation in India (e.g., Coastal Regulation Zone) and its impact on local communities and ecosystems.
- Research and present a case study of a successful sustainable engineering project in Kerala/India (e.g., sustainable building design, water management project, infrastructure project).
- Research and present a case study of an unsustainable engineering project in Kerala/India highlighting design and implementation faults and possible corrections/alternatives (e.g., a housing complex with water logging, a water management project causing frequent floods, infrastructure project that affects surrounding landscapes or ecosystems).

OPERATING SYSTEMS LAB

(Common to CS/CD/CM/CR/CA/AI/CB/CN/CC/CU/CI/CG)

Course Code	PCCSL407	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	0:0:3:0	ESE Marks	50
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	GYEST204	Course Type	Lab

Course Objectives:

- 1. To familiarize various Linux commands related to Operating systems.
- **2.** To give practical experience for learners on implementing different functions of Operating systems such as process management, memory management, and disk management.

Expt. No.	Experiments							
1	Familiarisation with basic Linux programming commands: ps, strace, gdb, strings, objdump, nm, file, od, xxd, time, fuser, top							
	Use /proc file system to gather basic information about your machine:							
	(a) Number of CPU cores							
	(b) Total memory and the fraction of free memory							
2	(c) Number of processes currently running.							
2	(d) Number of processes in the running and blocked states.							
	(e) Number of processes forked since the last bootup. How do you compare							
	this value with the one in (c) above?							
	(f) The number of context switches performed since the last bootup for a							
	particular process.							
	Write a simple program to print the system time and execute it. Then use the /proc file							
3	system to determine how long this program (in the strict sense, the corresponding process)							
	ran in user and kernel modes.							
4	Create a new process using a fork system call. Print the parent and child process IDs. Use							
4	the pstree command to find the process tree for the child process starting from the init							

	process.					
5	Write a program to add two integers (received via the command line) and compile it to an executable named "myadder". Now write another program that creates a new process using a fork system call. Make the child process add two integers by replacing its image with the "myadder" image using execvp system call.					
6	Create a new process using a fork system call. The child process should print the string "PCCSL407" and the parent process should print the string "Operating Systems Lab". Use a wait system call to ensure that the output displayed is "PCCSL407 Operating Systems Lab"					
	Inter-process Communication (https://www.linuxdoc.org/LDP/lpg/node7.html)					
7	 (a) Using Pipe – Evaluate the expression √b² 4ac. The first process evaluates b². The second process evaluates 4ac and sends it to the first process which evaluates the final expression and displays it. (b) Using Message Queue - The first process sends a string to the second process. The second process reverses the received string and sends it back to the first process. The first process compares the original string and the reversed string received from the second one and then prints whether the string is a palindrome or not. (c) Using Shared Memory - The first process sends three strings to the second process. The second process concatenates them to a single string (with whitespace being inserted between the two individual strings) and sends it back to the first process. The first process prints the concatenated string in the flipped case, that is if the concatenated string is "Hello S4 Students", the final output should be "hELLO s4 sTUDENTS" 					
8	Write a multithreaded program that calculates the mean, median, and standard deviation for a list of integers. This program should receive a series of integers on the command line and will then create three separate worker threads. The first thread will determine the mean value, the second will determine the median and the third will calculate the standard deviation of the integers. The variables representing the mean, median, and standard deviation values will be stored globally. The worker threads will set these values, and the parent thread will output the values once the workers have exited.					
9	Input a list of processes, their CPU burst times (integral values), arrival times, and priorities. Then simulate FCFS, SRTF, non-preemptive priority (a larger priority number implies a higher priority), and RR (quantum = 3 units) scheduling algorithms on the					

	process mix, determining which algorithm results in the minimum average waiting time (over all processes).
10	Use semaphores to solve the readers-writers problem with writers being given priority over readers.
11	Obtain a (deadlock-free) process mix and simulate the banker's algorithm to determine a safe execution sequence.
12	Obtain a process mix and determine if the system is deadlocked.
13	Implement the deadlock-free semaphore-based solution for the dining philosopher's problem.
14	Simulate the address translation in the paging scheme as follows: The program receives three command line arguments in the order • size of the virtual address space (in megabytes) • page size (in kilobytes) • a virtual address (in decimal notation) The output should be the physical address corresponding to the virtual address in <frame number,="" offset=""/> format. You may assume that the page table is implemented as an array indexed by page numbers. (NB: If the page table has no index for the page number determined from the virtual address, you may just declare a page table miss!)
15	Simulate the FIFO, LRU, and optimal page-replacement algorithms as follows: First, generate a random page-reference string where page numbers range from 0 to 9. Apply the random page-reference string to each algorithm, and record the number of page faults incurred by each algorithm. Assume that demand paging is used. The length of the reference string and the number of page frames (varying from 1 to 7) are to be received as command line arguments.
16	Simulate the SSTF, LOOK, and CSCAN disk-scheduling algorithms as follows: Your program will service a disk with 5,000 cylinders numbered 0 to 4,999. The program will generate a random series of 10 cylinder requests and service them according to each of the algorithms listed earlier. The program will be passed the initial position of the disk head (as a parameter on the command line) and will report the total number of head movements required by each algorithm.

Course Assessment Method (CIE: 50 marks, ESE: 50 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Preparation/Pre-Lab Work experiments, Viva and Timely completion of Lab Reports / Record (Continuous Assessment)	Internal Examination	Total
5	25	20	50

End Semester Examination Marks (ESE):

Procedure/ Preparatory work/Design/ Algorithm	Conduct of experiment/ Execution of work/ troubleshooting/ Programming	Result with valid inference/ Quality of Output	Viva voce	Record	Total
10	15	10	10	5	50

- Submission of Record: Students shall be allowed for the end semester examination only upon submitting the duly certified record.
- Endorsement by External Examiner: The external examiner shall endorse the record

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Illustrate the use of various systems calls in Operating Systems.	К3
CO2	Implement process creation and inter-process communication in Operating Systems	К3
CO3	Compare the performance of various CPU scheduling algorithms	K4
CO4	Compare the performance of various disk scheduling algorithms	K4

Note: K1-Remember, K2-Understand, K3-Apply, K4-Analyse, K5-Evaluate, K6-Create

CO- PO Mapping (Mapping of Course Outcomes with Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3	3				3				3
CO2	3	3	3	3				3				3
CO3	3	3	3	3				3				3
CO4	3	3	3	3				3				3
CO5	3	3	3	3				3				3

^{1:} Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books					
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year		
1	Operating Systems: Three Easy Pieces	Andrea Arpaci- Dusseau, Remzi Arpaci-Dusseau	CreateSpace	1/e, 2018		
2	Linux Kernel Development	Robert Love	Pearson	3/e, 2018		
3	Unix Network Programming - Volume 2: Interprocess Communications	Richard Stevens	Prentice Hall	2/e, 1999		

	Reference Books/Websites					
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year		
1	The Design of the UNIX Operating System	Maurice J. Bach	Prentice Hall of India	1/e, 1994		
2	The Little Book of Semaphores	Allen B. Downey	Green Tea Press	1/e, 2016		

	Video Links (NPTEL, SWAYAM)
Module No.	Link ID
1	https://archive.nptel.ac.in/courses/106/105/106105214/
2	https://www.youtube.com/playlist?list=PLDW872573QAb4bj0URobvQTD41IV6gRkx

Continuous Assessment (25 Marks)

1. Preparation and Pre-Lab Work (7 Marks)

- Pre-Lab Assignments: Assessment of pre-lab assignments or quizzes that test understanding of the upcoming experiment.
- Understanding of Theory: Evaluation based on students' preparation and understanding of the theoretical background related to the experiments.

2. Conduct of Experiments (7 Marks)

- Procedure and Execution: Adherence to correct procedures, accurate execution of experiments, and following safety protocols.
- Skill Proficiency: Proficiency in handling equipment, accuracy in observations, and troubleshooting skills during the experiments.
- Teamwork: Collaboration and participation in group experiments.

3. Lab Reports and Record Keeping (6 Marks)

- Quality of Reports: Clarity, completeness and accuracy of lab reports. Proper documentation of experiments, data analysis and conclusions.
- Timely Submission: Adhering to deadlines for submitting lab reports/rough record and maintaining a well-organized fair record.

4. Viva Voce (5 Marks)

• Oral Examination: Ability to explain the experiment, results and underlying principles during a viva voce session.

Final Marks Averaging: The final marks for preparation, conduct of experiments, viva, and record are the average of all the specified experiments in the syllabus.

Evaluation Pattern for End Semester Examination (50 Marks)

1. Procedure/Preliminary Work/Design/Algorithm (10 Marks)

- Procedure Understanding and Description: Clarity in explaining the procedure and understanding each step involved.
- Preliminary Work and Planning: Thoroughness in planning and organizing materials/equipment.
- Algorithm Development: Correctness and efficiency of the algorithm related to the experiment.
- Creativity and logic in algorithm or experimental design.

2. Conduct of Experiment/Execution of Work/Programming (15 Marks)

 Setup and Execution: Proper setup and accurate execution of the experiment or programming task.

3. Result with Valid Inference/Quality of Output (10 Marks)

- Accuracy of Results: Precision and correctness of the obtained results.
- Analysis and Interpretation: Validity of inferences drawn from the experiment or quality of program output.

4. Viva Voce (10 Marks)

- Ability to explain the experiment, procedure results and answer related questions
- Proficiency in answering questions related to theoretical and practical aspects of the subject.

5. Record (5 Marks)

• Completeness, clarity, and accuracy of the lab record submitted

DBMS LAB

(Common to CS/CD/CR/CA/AD/AI/CB/CN/CC/CU/CI/CG)

Course Code	PCCSL408	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	0:0:3:0	ESE Marks	50
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Lab

Course Objectives:

- 1. To equip students with comprehensive skills in SQL, PL/SQL, and NoSQL databases.
- 2. To enable the learner to proficiently design, implement, and manage relational and non-relational databases to meet diverse data management needs

Expt. No.	Experiments
1	Design a database schema for an application with ER diagram from a problem description.
2	Creation of database schema - DDL (create tables, set constraints, enforce relationships, create indices, delete and modify tables). Export ER diagram from the database and verify relationships (with the ER diagram designed in step 1).
3	Database initialization - Data insert, Data import to a database (bulk import using UI and SQL Commands).
4	Practice SQL commands for DML (insertion, updating, altering, deletion of data, and viewing/querying records based on condition in databases).
5	Implementation of various aggregate functions, Order By, Group By & Having clause in SQL.
6	Implementation of set operators nested queries, and join queries.
7	Practice of SQL TCL DCL commands like Rollback, Commit, Savepoint, Practice of SQL DCL commands for granting and revoking user privileges.
8	Practice of SQL commands for creation of views and assertions.
9	Creation of Procedures, Triggers and Functions.
10	Creation of Packages and cursors.
11	Design a database application using any front-end tool for any problem selected in experiment number 1. The application constructed should have five or more tables**.
12	Perform basic CRUD (Create, Read, Update, Delete) operations on a Cassandra table.
13	Write and execute CQL queries to retrieve specific data from Cassandra tables
14	Create a simple application using Mongodb with python

^{**} The problem must be designed to convey the difference of NoSQL from SQL databases.

Course Assessment Method (CIE: 50 marks, ESE: 50 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Preparation/Pre-Lab Work experiments, Viva and Timely completion of Lab Reports / Record (Continuous Assessment)	Internal Examination	Total
5	25	20	50

End Semester Examination Marks (ESE):

Procedure/ Preparatory work/Design/ Algorithm	Conduct of experiment/ Execution of work/ troubleshooting/ Programming	Result with valid inference/ Quality of Output	Viva voce	Record	Total
10	15	10	10	5	50

- Submission of Record: Students shall be allowed for the end semester examination only upon submitting the duly certified record.
- Endorsement by External Examiner: The external examiner shall endorse the record

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Develop database schema for a given real world problem-domain using standard design and modeling approaches	К3
CO2	Construct queries using SQL for database creation, interaction, modification, and updation.	К3
CO3	Plan and implement triggers and cursors, procedures, functions, and control structures using PL/SQL	К3
CO4	Perform CRUD operations in NoSQL Databases	К3
CO5	Design database applications using front-end tools and back-end DBMS	K5

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO- PO Mapping (Mapping of Course Outcomes with Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	3	3	1						3		3
CO2	3	3	3	1						3		3
CO3	3	3	3	1						3		3
CO4	3	3	3	2	3					3		3
CO5	3	3	3	2	3					3	3	3

1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

	Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year	
1	Fundamentals of Database Systems	Elmasri, Navathe	Pearson	7/e, 2017	
2	Professional NoSQL	Shashank Tiwari	Wiley	1/e, 2011	

	Reference Books					
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year		
1	Database System Concepts,	Sliberschatz Korth and S. Sudarshan	McGraw Hill,	7/e, 2017		
2	NoSQL for Dummies	Adam Fowler	John Wiley & Sons	1/e, 2015		
3	NoSQL Data Models: Trends and Challenges (Computer Engineering: Databases and Big Data),		Wiley	1/e, 2018		
4	Making the Sense of NoSQL : A guide for Managers and Rest of us.	Dan McCreary and Ann Kelly	Manning	1/e, 2014		

	Video Links (NPTEL, SWAYAM)				
Module No.	Link ID				
1	https://onlinecourses.nptel.ac.in/noc21_cs04/preview				
2	https://onlinecourses.nptel.ac.in/noc21_cs04/preview				
3	https://onlinecourses.nptel.ac.in/noc21_cs04/preview				
4	https://archive.nptel.ac.in/courses/106/104/106104135/				

Continuous Assessment (25 Marks)

1. Preparation and Pre-Lab Work (7 Marks)

- Pre-Lab Assignments: Assessment of pre-lab assignments or quizzes that test understanding of the upcoming experiment.
- Understanding of Theory: Evaluation based on students' preparation and understanding of the theoretical background related to the experiments.

2. Conduct of Experiments (7 Marks)

- Procedure and Execution: Adherence to correct procedures, accurate execution of experiments, and following safety protocols.
- Skill Proficiency: Proficiency in handling equipment, accuracy in observations, and troubleshooting skills during the experiments.
- Teamwork: Collaboration and participation in group experiments.

3. Lab Reports and Record Keeping (6 Marks)

- Quality of Reports: Clarity, completeness and accuracy of lab reports. Proper documentation of experiments, data analysis and conclusions.
- Timely Submission: Adhering to deadlines for submitting lab reports/rough record and maintaining a well-organized fair record.

4. Viva Voce (5 Marks)

 Oral Examination: Ability to explain the experiment, results and underlying principles during a viva voce session.

Final Marks Averaging: The final marks for preparation, conduct of experiments, viva, and record are the average of all the specified experiments in the syllabus.

Evaluation Pattern for End Semester Examination (50 Marks)

1. Procedure/Preliminary Work/Design/Algorithm (10 Marks)

- Procedure Understanding and Description: Clarity in explaining the procedure and understanding each step involved.
- Preliminary Work and Planning: Thoroughness in planning and organizing materials/equipment.

- Algorithm Development: Correctness and efficiency of the algorithm related to the experiment.
- Creativity and logic in algorithm or experimental design.

2. Conduct of Experiment/Execution of Work/Programming (15 Marks)

 Setup and Execution: Proper setup and accurate execution of the experiment or programming task.

3. Result with Valid Inference/Quality of Output (10 Marks)

- Accuracy of Results: Precision and correctness of the obtained results.
- Analysis and Interpretation: Validity of inferences drawn from the experiment or quality of program output.

4. Viva Voce (10 Marks)

- Ability to explain the experiment, procedure results and answer related questions
- Proficiency in answering questions related to theoretical and practical aspects of the subject.

5. Record (5 Marks)

• Completeness, clarity, and accuracy of the lab record submitted