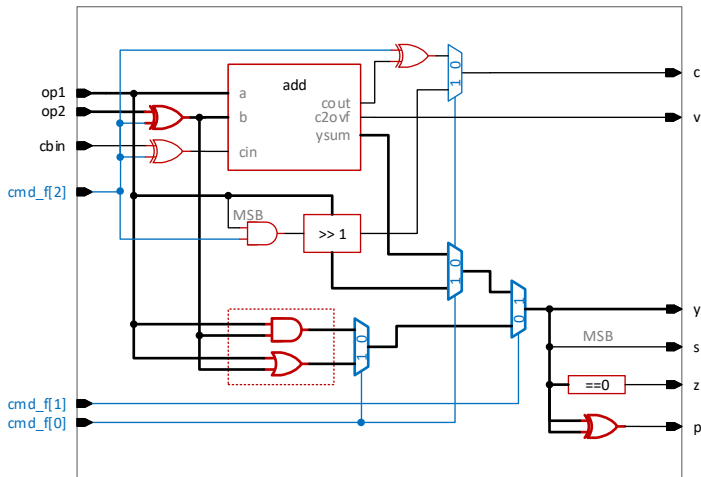


# Block Diagram alu\_6\_8fun.vhd

— control  
— data  
— operative



```
cmd_f = "000" : cmd_and    (op1 & op2)
cmd_f = "001" : cmd_or     (op1 | op2)
cmd_f = "010" : cmd_add    (op1 + op2 + cbin)
cmd_f = "011" : cmd_srl    (op1 >> 1 (insert 0))
cmd_f = "100" : cmd_and*   (op1 & !op2)
cmd_f = "101" : cmd_or*    (op1 | !op2)
cmd_f = "110" : cmd_sub    (op1 - op2 - cbin)
cmd_f = "111" : cmd_sra    (op1 >> 1 (insert MSB))
```