

F526U

Board ID	Doc Version	Date	Author	ASIC ID	Production
F526U	0.1	19.05.2022	L. Ghislotti	526	US

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.796
			Yes	IVDD [mA]	132.2
		Digital	Yes	DVDD [V]	1.792
			Yes	IDVDD [mA]	5
		Calibration	Yes	3V3 [V]	2.996
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	4.90
		Reference Voltages	Yes	VCMSH [V]	1.197
			Yes	VCM [V]	0.883
			Yes	RVCM [V]	0.898
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1079
				T [°C]	29.100
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	6.679
				Ch 7 [keV]	27.674
				Ch 15 [keV]	6.368
				Ch 16 [keV]	18.519
				Ch 23 [keV]	27.172
				Ch 31 [keV]	12.765
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	4.903
				After FT [DAC]	2.182
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	8.175

Notes:

scratches on top