

F498U

Board ID	Doc Version	Date	Author	ASIC ID	Production
F498U	0.1	19.05.2022	L. Ghislotti	498	US

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.798
			Yes	IVDD [mA]	135.1
		Digital	Yes	DVDD [V]	1.801
			Yes	IDVDD [mA]	5
		Calibration	Yes	3V3 [V]	3.000
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	4.97
		Reference Voltages	Yes	VCMSH [V]	1.199
			Yes	VCM [V]	0.884
			Yes	RVCM [V]	0.899
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1064
				T [°C]	27.888
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	2.580
				Ch 7 [keV]	9.481
				Ch 15 [keV]	4.265
				Ch 16 [keV]	3.276
				Ch 23 [keV]	8.722
				Ch 31 [keV]	3.089
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	5.948
				After FT [DAC]	3.377
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	2.786

Notes:

scratches on top
scratches on bottom