

# F224I

Board ID	Doc Version	Date	Author	ASIC ID	Production
F224I	0.1	30.03.2022	L. Ghislotti	224	Italian

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.801
			Yes	IVDD [mA]	137.0
		Digital	Yes	DVDD [V]	1.793
			Yes	IDVDD [mA]	4
		Calibration	Yes	3V3 [V]	2.997
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	5.03
		Reference Voltages	Yes	VCM SH [V]	1.201
			Yes	VCM [V]	0.893
			Yes	RVCM [V]	0.900
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1058
				T [°C]	27.403
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	5.151
				Ch 7 [keV]	30.645
				Ch 15 [keV]	8.509
				Ch 16 [keV]	6.435
				Ch 23 [keV]	26.931
				Ch 31 [keV]	5.229
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	4.859
				After FT [DAC]	2.747
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	7.400

**Notes:**