

F512U

Board ID	Doc Version	Date	Author	ASIC ID	Production
F512U	0.1	19.05.2022	L. Ghislotti	512	US

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.802
			Yes	IVDD [mA]	134.1
		Digital	Yes	DVDD [V]	1.800
			Yes	IDVDD [mA]	5
		Calibration	Yes	3V3 [V]	2.989
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	4.94
		Reference Voltages	Yes	VCMSH [V]	1.202
			Yes	VCM [V]	0.885
			Yes	RVCM [V]	0.900
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1069
				T [°C]	28.292
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	2.700
				Ch 7 [keV]	9.461
				Ch 15 [keV]	4.700
				Ch 16 [keV]	3.299
				Ch 23 [keV]	8.001
				Ch 31 [keV]	3.165
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	4.694
				After FT [DAC]	2.439
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	2.691

Notes:

scratches on top