

# F521U

Board ID	Doc Version	Date	Author	ASIC ID	Production
F521U	0.1	19.05.2022	L. Ghislotti	521	US

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.795
			Yes	IVDD [mA]	132.4
		Digital	Yes	DVDD [V]	1.802
			Yes	IDVDD [mA]	5
		Calibration	Yes	3V3 [V]	2.995
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	4.91
		Reference Voltages	Yes	VCMSH [V]	1.197
			Yes	VCM [V]	0.883
			Yes	RVCM [V]	0.898
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1087
				T [°C]	29.746
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	7.191
				Ch 7 [keV]	25.226
				Ch 15 [keV]	6.049
				Ch 16 [keV]	18.237
				Ch 23 [keV]	29.747
				Ch 31 [keV]	12.374
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	5.506
				After FT [DAC]	3.032
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	8.027

## Notes:

scratches on bottom