

Board ID	Doc Version	Date	Author	ASIC ID	Production
F222I	0.1	30.03.2022	L. Ghislotti	222	Italian

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	
			Yes	IVDD [mA]	
		Digital	Yes	DVDD [V]	
			Yes	IDVDD [mA]	
		Calibration	Yes	3V3 [V]	
			Yes	I3V3 [mA]	
		Bias current	Yes	Ibias [mA]	
		Reference Voltages	Yes	VCM SH [V]	
			Yes	VCM [V]	
			Yes	RVCM [V]	
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	
				T [°C]	
Analog	4	Input-output characteristic	Yes	# of non resp. ch	
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	
				Ch 7 [keV]	
				Ch 15 [keV]	
				Ch 16 [keV]	
				Ch 23 [keV]	
				Ch 31 [keV]	
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	
				After FT [DAC]	
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	

Notes: