

# F518U

Board ID	Doc Version	Date	Author	ASIC ID	Production
F518U	0.1	19.05.2022	L. Ghislotti	518	US

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.794
			Yes	IVDD [mA]	131.9
		Digital	Yes	DVDD [V]	1.797
			Yes	IDVDD [mA]	5
		Calibration	Yes	3V3 [V]	2.991
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	4.92
		Reference Voltages	Yes	VCMSH [V]	1.196
			Yes	VCM [V]	0.882
			Yes	RVCM [V]	0.897
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1096
				T [°C]	30.472
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	9.978
				Ch 7 [keV]	59.725
				Ch 15 [keV]	13.415
				Ch 16 [keV]	21.049
				Ch 23 [keV]	59.466
				Ch 31 [keV]	15.579
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	5.204
				After FT [DAC]	2.842
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	16.041

## Notes:

solid defect on bottom