

F507U

Board ID	Doc Version	Date	Author	ASIC ID	Production
F507U	0.1	19.05.2022	L. Ghislotti	507	US

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.795
			Yes	IVDD [mA]	134.8
		Digital	Yes	DVDD [V]	1.797
			Yes	IDVDD [mA]	5
		Calibration	Yes	3V3 [V]	2.987
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	4.93
		Reference Voltages	Yes	VCMSH [V]	1.197
			Yes	VCM [V]	0.882
			Yes	RVCM [V]	0.898
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1067
				T [°C]	28.130
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	6.358
				Ch 7 [keV]	26.218
				Ch 15 [keV]	6.558
				Ch 16 [keV]	16.402
				Ch 23 [keV]	22.583
				Ch 31 [keV]	11.437
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	3.626
				After FT [DAC]	1.360
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	7.498

Notes:

scratches on bottom