

F502U

Board ID	Doc Version	Date	Author	ASIC ID	Production
F502U	0.1	19.05.2022	L. Ghislotti	502	US

	#	Test Description	Pass	Note	
Visual	1	Visual inspection	Yes	See notes (if any)	
Bias	2	Analog	Yes	AVDD [V]	1.798
			Yes	IVDD [mA]	134.2
		Digital	Yes	DVDD [V]	1.796
			Yes	IDVDD [mA]	5
		Calibration	Yes	3V3 [V]	2.994
			Yes	I3V3 [mA]	3
		Bias current	Yes	Ibias [mA]	4.97
		Reference Voltages	Yes	VCMSH [V]	1.199
			Yes	VCM [V]	0.883
			Yes	RVCM [V]	0.899
Digital	3	Configuration test	Yes		
		Wrong address test	Yes		
		Temperature Sensor	Yes	ADC	1061
				T [°C]	27.645
Analog	4	Input-output characteristic	Yes	# of non resp. ch	0
	5	Noise ENC FWHM @ tp=6	Yes	Ch 0 [keV]	3.291
				Ch 7 [keV]	14.301
				Ch 15 [keV]	5.372
				Ch 16 [keV]	4.150
				Ch 23 [keV]	16.750
				Ch 31 [keV]	3.642
	6	Threshold dispersion @ tp=6	Yes	Before FT [DAC]	4.381
				After FT [DAC]	2.096
	7	Pedestal dispersion @ tp=6	Yes	Dispersion [ADC]	4.047

Notes:

scratches on top
scratches on bottom