



Luca Ledda

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Born 19 November 1993 - Carbonia (CA) 09013 - Italy

WORK EXPERIENCE

04/2018 – now

Stage at Magneti Marelli S.p.A. - Master Thesis Development

Magneti Marelli, Venaria Reale (TO) (Italy) – <https://www.magnetimarelli.com/>

Thesis Title: "AUTOMATIC TOOL INTEGRATION INTO AN INDUSTRIAL ELECTRONIC BOARD PRODUCTION HARDWARE FAULT COVERAGE ANALYSIS PROCESS".

Abstract: Focused on the Powertrain business line products, the purpose of this project is to integrate the hardware fault coverage process (involving AOI, ICT and FT test strategies) with a customized tool in order to obtain as much as possible an automatic flow aiming to break down timing and human error barriers.

EDUCATION

09/2016 – now

MSc in Computer Engineering, Embedded Systems

Polytechnic University of Turin, Turin (Italy) – www.polito.it

Language Course : English

Topics :

- Computer Architectures
- Real-time Operating Systems.
- Digital and Analog Electronics.
- RTL and Logic Level Design for ASICs and FPGA.
- EDA and CAD-based Synthesis and Optimization of Digital Systems.
- Testing Techniques for Electronic Circuits and Systems.
- Model-based Software Design for Automotive Embedded Systems (ISO26262).
- Automated Design Energy issues of Embedded Systems.

01/2016 – 06/2016

General English Intensive Course

English Language Center (ELC Bristol), Bristol (United Kingdom) – www.elcbristol.co.uk
General English full time course in preparation to the First Certificate of English (FCE Cambridge - B2).

09/2012 – 11/2015

BCs in Computer Engineering

University of Pisa, Pisa (Italy) – www.unipi.it

Thesis Title: "INTEGRATION OF AN INTERACTIVE WHITEBOARD SOFTWARE AIMING TO ENHANCE THE ACADEMIC TEACHING IN PROGRAMMING COURSES".

Abstract: An interactive whiteboard environment has been extended with a programming editor pane in order to allow the Professor to both write down charts and manage code during the lectures.

Final Grade : 106/110

09/2007 – 08/2012

High School Diploma - Computer and Telecommunications

I.I.S. IT Minerario "Asproni", Iglesias (Italy) – www.asproni-fermi.gov.it

Final Grade : 100/100

PROJECTS

2017 **Dual-Vth Low Power TCL Command**

Type : Team project.

Development of a TCL command to be integrated in Prime Time that performs a post-synthesis Dual-Vth cell assignment for leakage power optimization.

2017 **Model-Based Software Design of a DC Motor Controller**

Type : Team project.

V-shaped model-based design approach of a DC Motor Controller through MIL (Model in the loop), SIL (Software in the loop) and HIL (Hardware in the loop) testing phases. Matlab, Simulink and Veristand softwares have been used.

2017 **Self-Test Module**

Type : Individual project.

Design and simulation of a System-On-Chip including a Control Unit, Datapath and a Xilinx IP peripheral. The code is written in VHDL.

2016 **Signal Analyzer**

Type : Individual project.

Development of an application and a device driver to be deployed into a STM32F746DISCO board (Embedded Linux OS). The system acquires a digital square waveform and computes its period and frequency. The code is written in C.

2016 **Alarm System**

Type : Individual project.

Development of a Micrium mC/OS-III project to be deployed into a FRDM-K64F board. The system monitors the voltage of a given analog input and informs the user using visual indications through the RGB LEDs. The code is written in C.

2014 **Estate Agent Database**

Type : Team project.

Development of a Database aiming to collect information for a high number of Estate Agents spread in a local area. E/R diagram and SQL queries have been provided.

EVENTS

2016 **Hackathon - Make it Wearable!**

Turin – www.wearabletechtorino.it

Type : Team project.

Development of a prototype-application for Android Smartwatches capable of controlling other Android devices through gestures.

SKILLS

Language **Italian** – mother tongue

English – B2 level (First Certificate of Cambridge - 06/2016) – fluent

Programming Language C, C++, Java, Java SE, Web Programming, SQL, jQuery, VHDL, SystemC, TCL

EDA Tool Design Compiler, Prime Time, Tetra Max, ModelSim

Operating System Windows, Linux, Micrium mC/OS-III, Embedded Linux

Software Matlab, Simulink, Veristand, J-Link Compiler, Kinetis Design Studio, TestWay Express

Technical Matured individual and team-working adaptive worker.

ADDITIONAL INFO

Driving License Category : B (EU)

About Me I like spending time with my friends and family.
I love travelling both on my own and with my friends.
I am fond of football. I heartily support a local team.
I am a runner for long distances and I have completed two half marathons.
I always try to put myself out there.
I devote all myself to achieve the goals that I have in mind.