➡️ Exercício sobre Portas lógicas
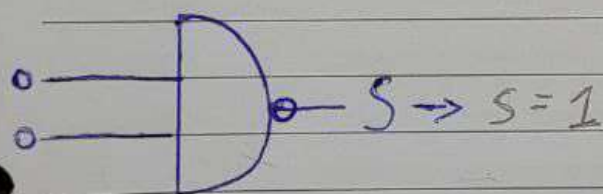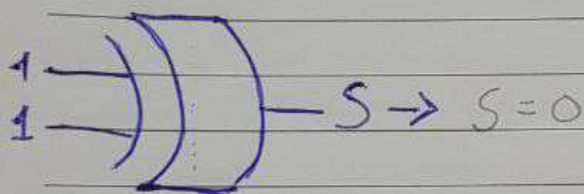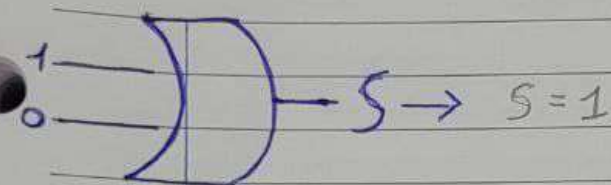
① Dê a saída das portas lógicas abaixo:

$S \rightarrow S = 0$

$S \rightarrow S = 1$

$S \rightarrow S = 0$

$S \rightarrow S = 1$

$S \rightarrow S = 1$
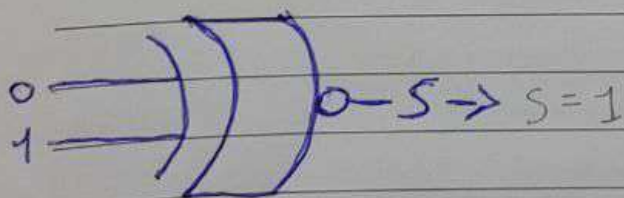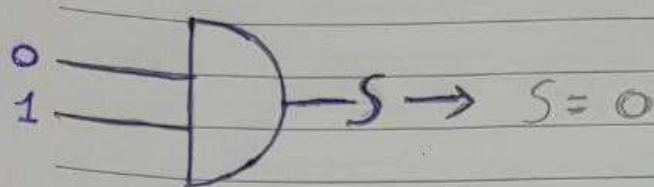
A	B	$A \wedge B$	$A \vee B$	$A \underline{\vee} B$	$\neg(A \wedge B)$	$A \leftrightarrow B$
0	0	0	0	0	1	0
0	1	0	1	1	1	1
0	1	0	1	1	1	1
1	0	0	1	1	1	1
1	1	1	1	0	0	0

➡ Exercício sobre circuitos a partir de portas lógicas

① Encontre a imagem do circuito dada a expressão

$S = \overline{(A.B)} + \overline{(C.D)}$
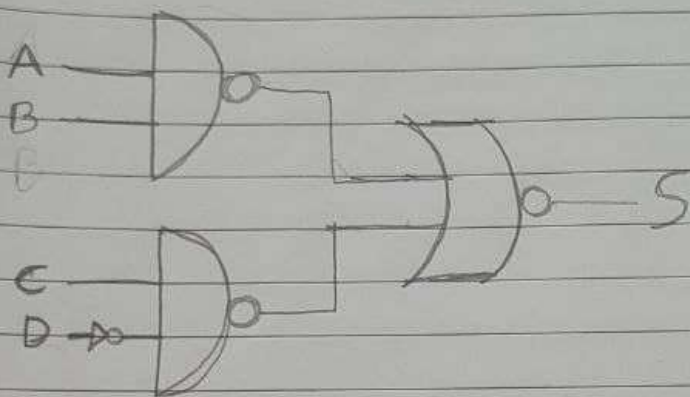
② Encontre a expressão do circuito abaixo
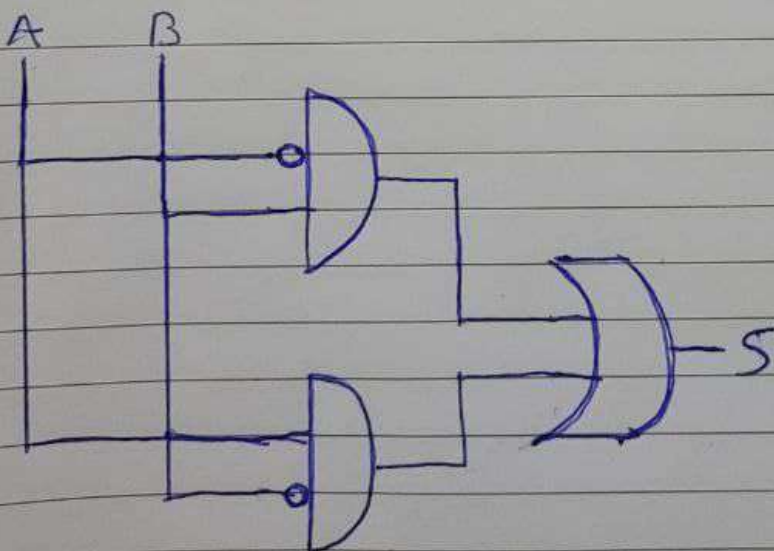
$S = (\bar{A}.B) + (A.\bar{B})$

➡ Exercícios sobre Tabela verdade

Ⓘ Encontra a Tabela verdade da expressão $S = \overline{(A \oplus B)} C$

A	B	C	A⊕B	$\overline{A \oplus B}$	$\overline{A \oplus B} . C$
0	0	0	0	1	0
0	0	1	0	1	1
0	1	0	1	0	0
0	1	1	1	0	0
1	0	0	1	0	0
1	0	1	1	0	0
1	1	0	0	1	0
1	1	1	0	1	1