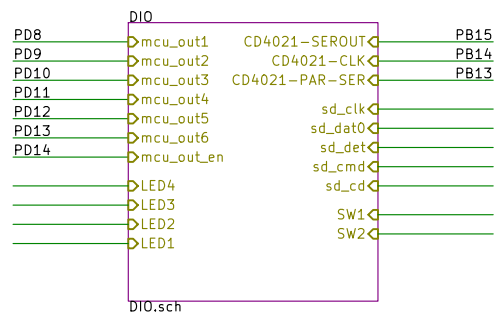


Notas personales

Falta pull up en i2c y pines que se dejen vacantes.

Puntos a definir/ revisar

A definir:
 -Conexión OTA correcta.
 -Conexiones vacantes

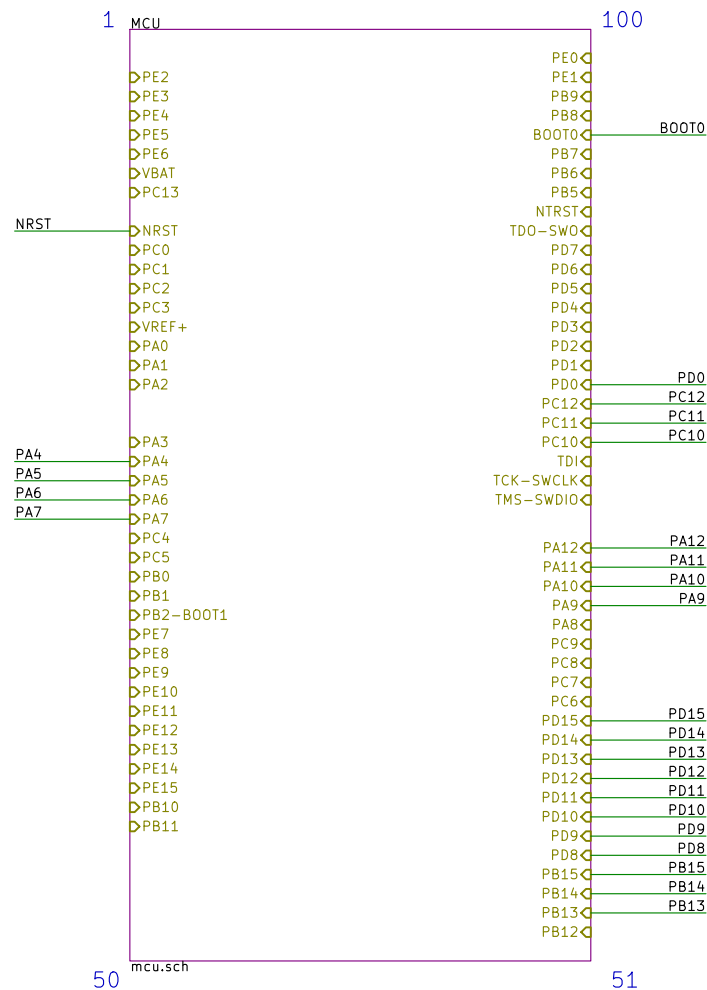


Alimentacion

Alimentacion.sch

analogico

analogico.sch



Pines para programación OTA

USART1 (BOOT)

USART1 (BOOT)

USART1 (BOOT)

USART1 (BOOT)

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Autor: Lucas Dórdolo

Trenes Argentinos Operaciones

Sheet: /

File: Placa_base.sch

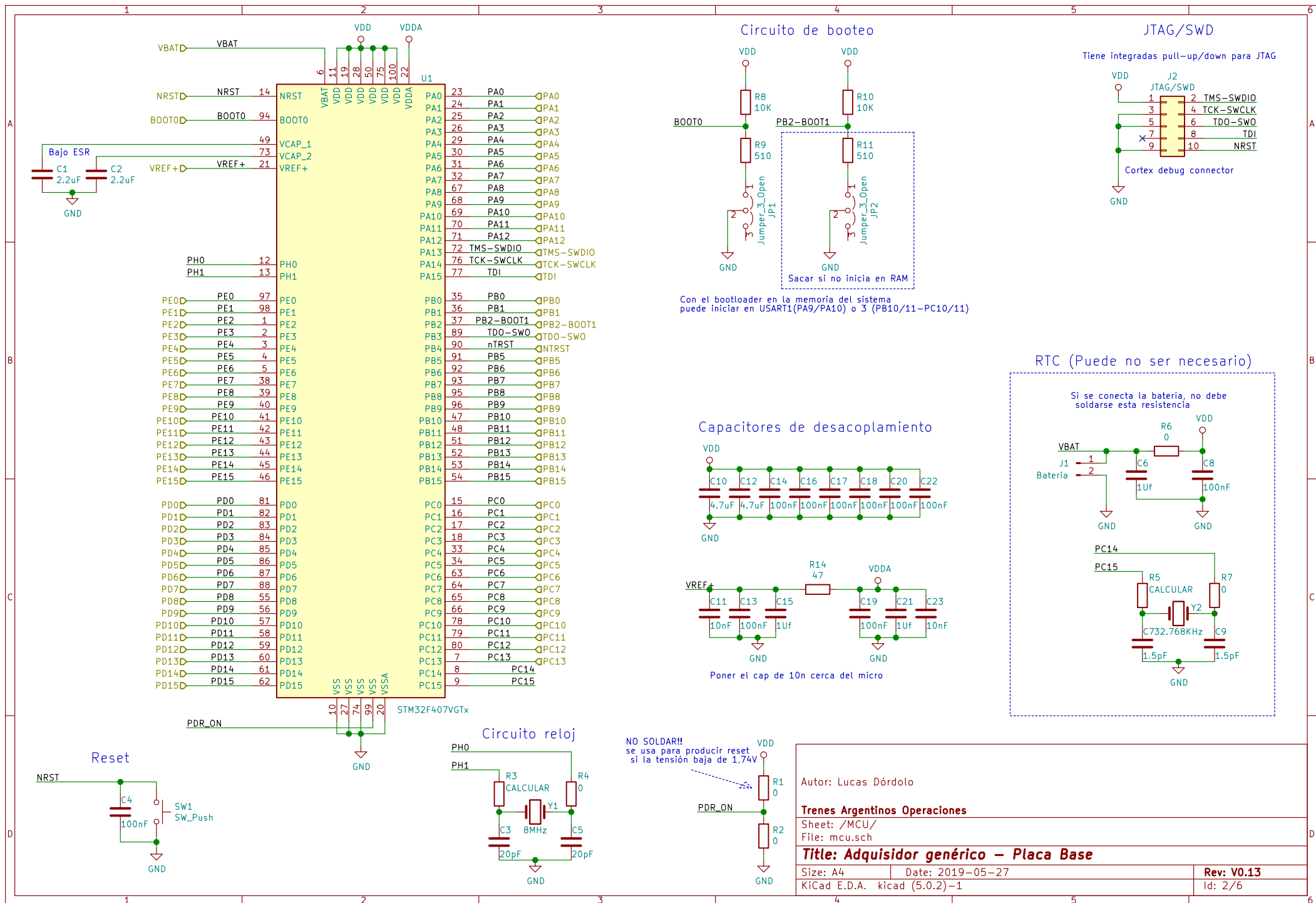
Title: Adquisidor genérico – Placa Base

Size: A4 Date: 2019-05-27

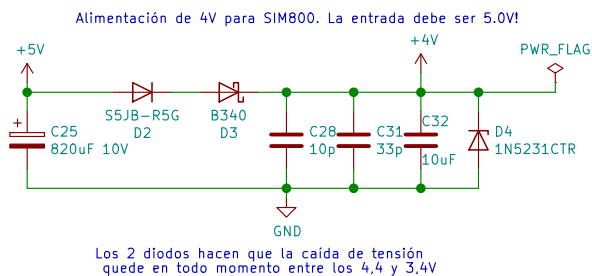
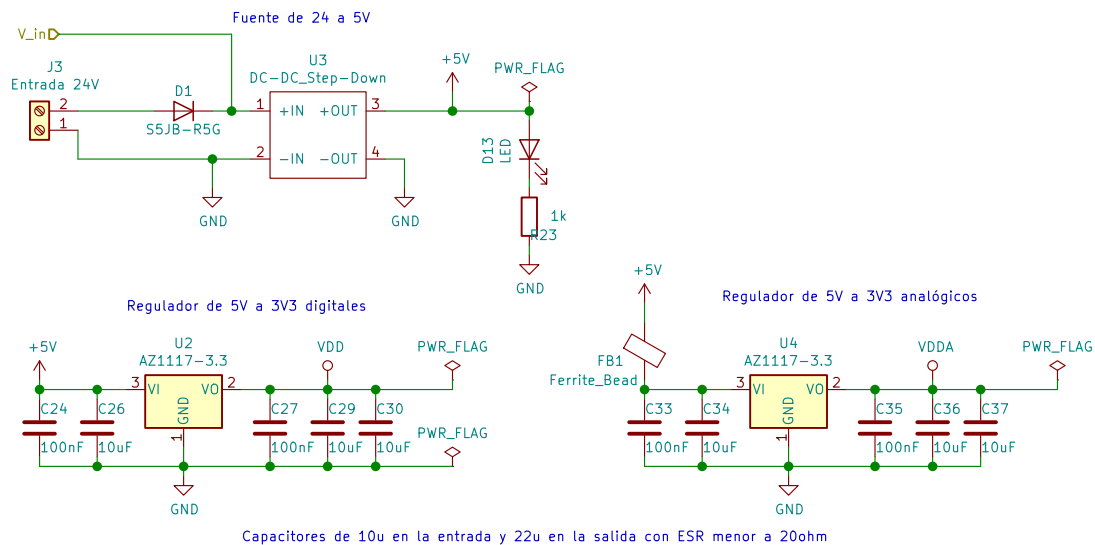
KiCad E.D.A. kicad (5.0.2)-1

Rev: V0.13

Id: 1/6

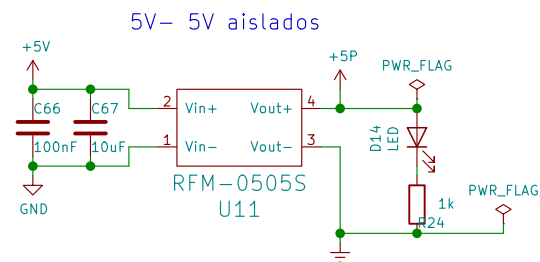


Convertor DC/DC de 24V a 5V y reguladores lineales



SIM800: If the voltage > 4.4V, the module will be automatically powered off

SIM800: If the voltage < 3.39V, the module will be automatically powered off



Autor: Lucas Dórdolo

Trenes Argentinos Operaciones

Sheet: /Alimentacion/

File: Alimentacion.sch

Title: Adquisidor genérico – Placa Base

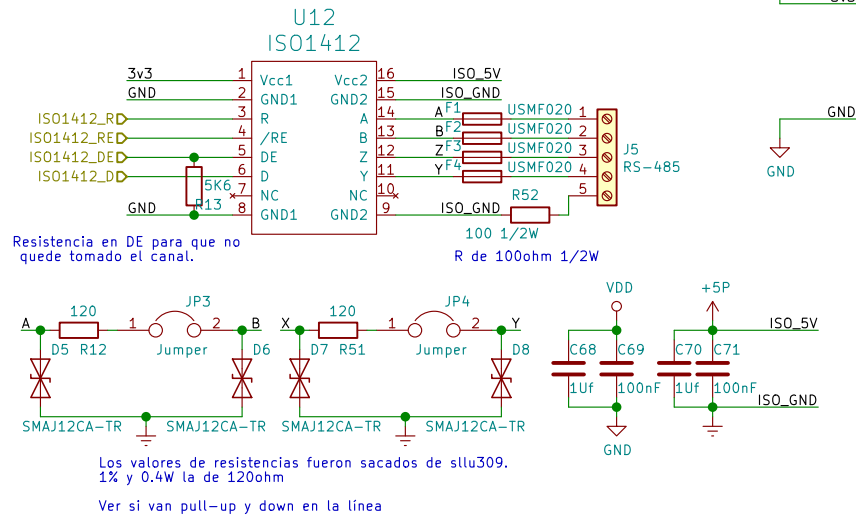
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KiCad E.D.A. kicad (5.0.2)-1

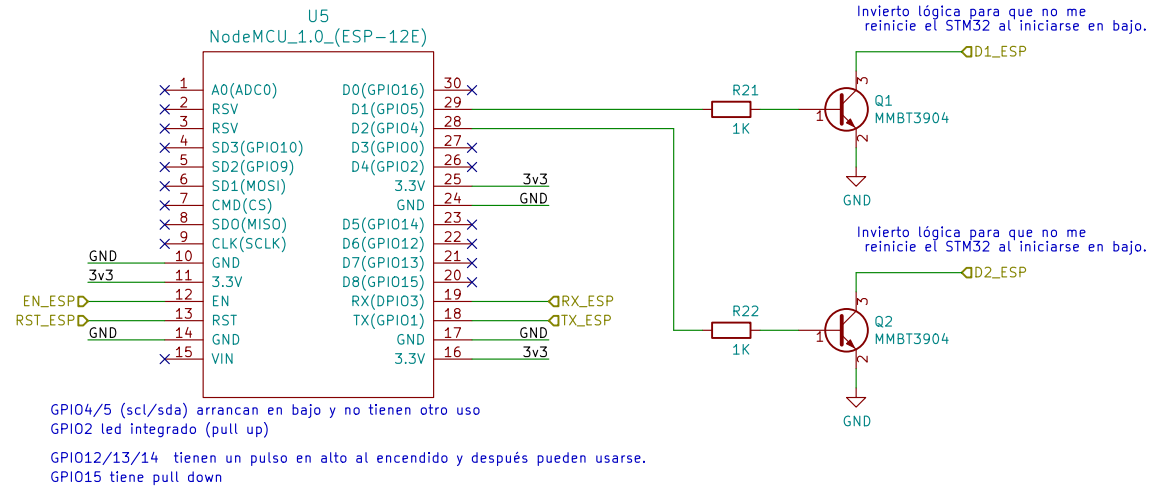
Rev: V0.13

Id: 3/6

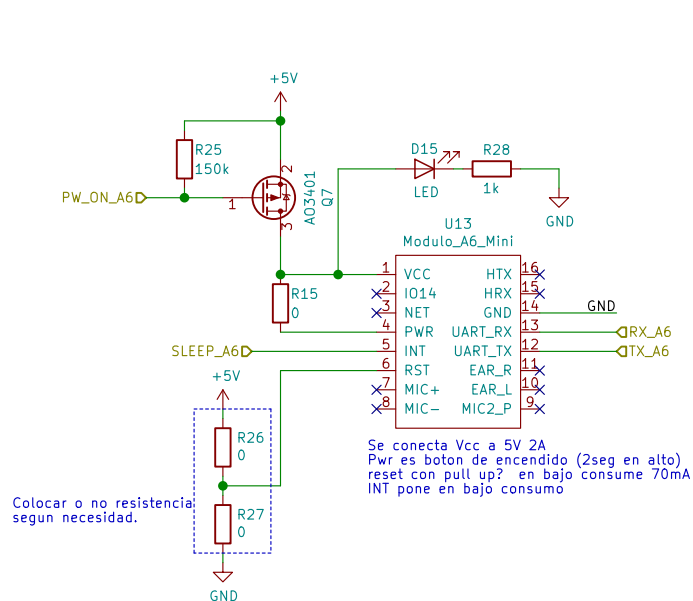
RS-485 Full-Duplex aislado



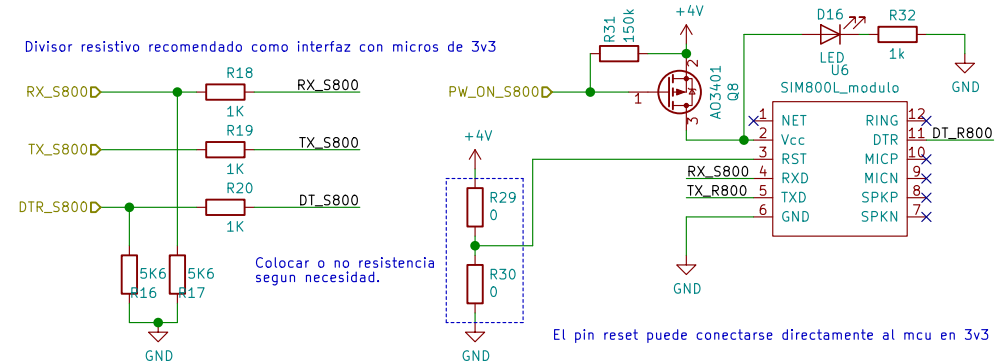
Módulo Wi-Fi para comunicación y programación OTA



Módulo GSM/GPRS para comunicación auxiliar redundante (A6 mini)



Módulo GSM/GPRS (SIM800) para comunicación auxiliar y circuito de adaptación de nivel



Autor: Lucas Dórdolo

Trenes Argentinos Operaciones

Sheet: /WiFi/

File: WiFi.sch

Title: Adquisidor genérico – Placa Base

Size: A4 Date: 2019-05-27

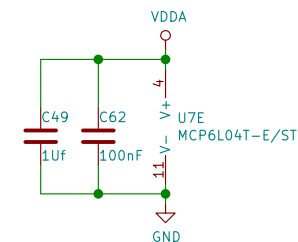
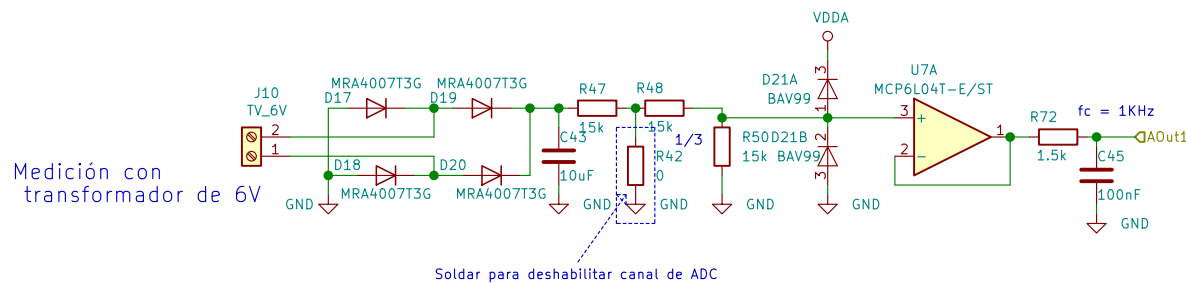
KiCad E.D.A. kicad (5.0.2)-1

Rev: V0.13

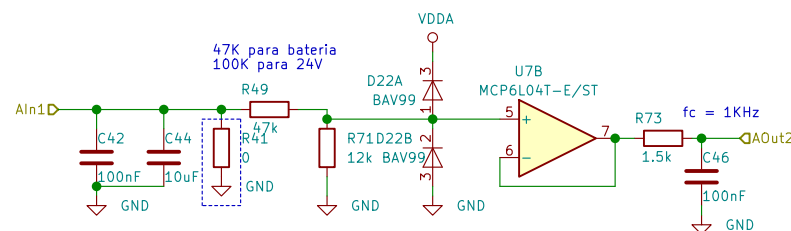
Id: 4/6

Entradas analógicas, acondicionamiento y filtrado

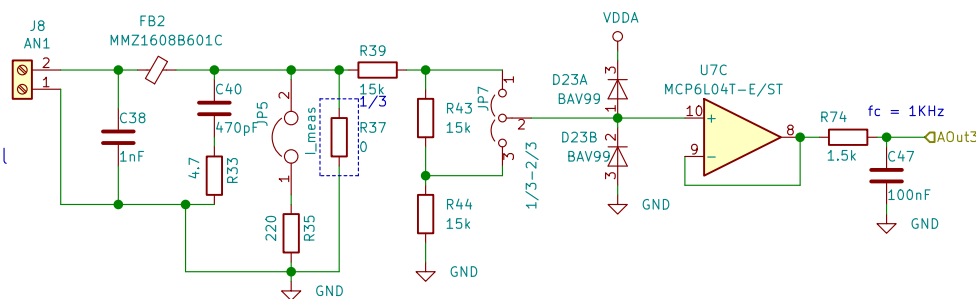
TLV9064IPWT es casi ideal pero sale \$0.8 cada uno. MCP6L04T-E/ST sale \$0.4 y son compatibles pin a pin. El LMV324 es sólo por el footprint.



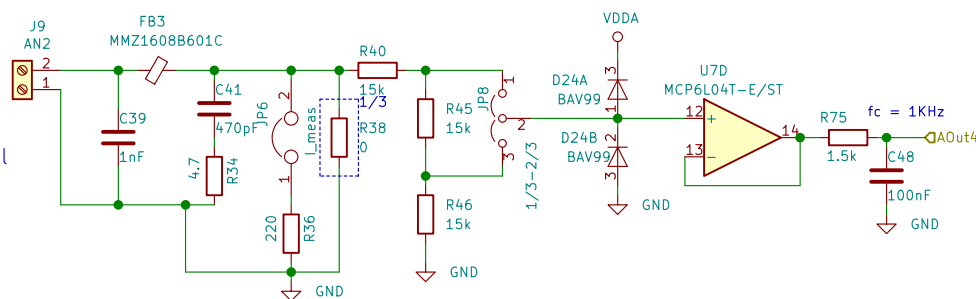
Medición tensión entrada/batería



Medición 4-20mA o 0-10V adicional



Medición 4-20mA o 0-10V adicional



Autor: Lucas Dórdolo

Trenes Argentinos Operaciones

Sheet: /analogico/

File: analogico.sch

Title: Adquisidor genérico – Placa Base

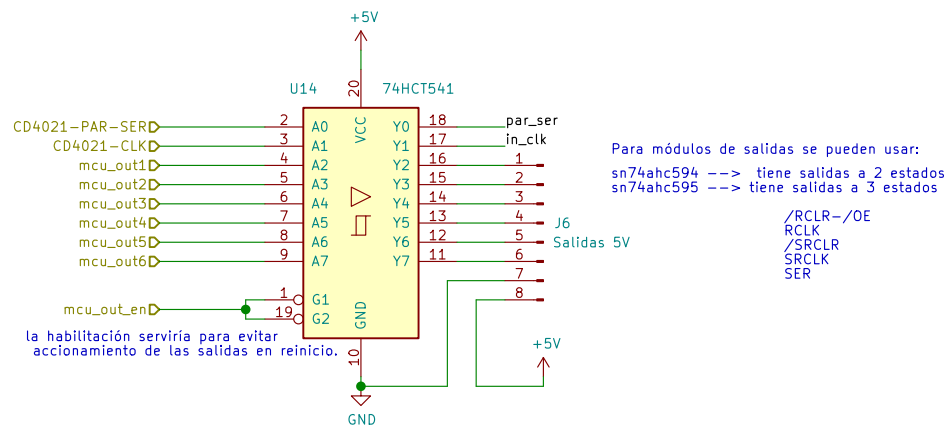
Size: A4 Date: 2019-05-27

KiCad E.D.A. kicad (5.0.2)-1

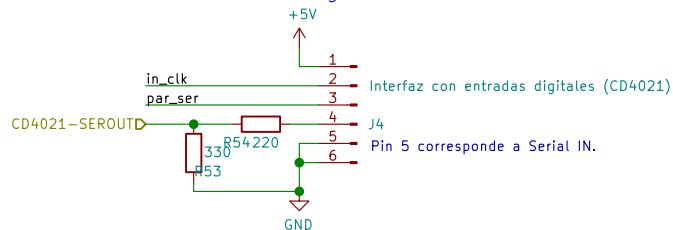
Rev: V0.13

Id: 5/6

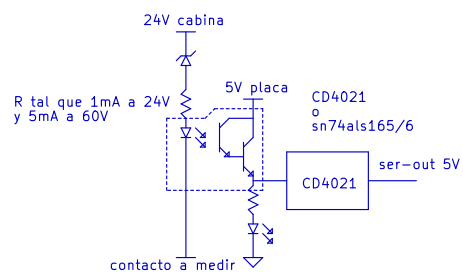
Salidas digitales



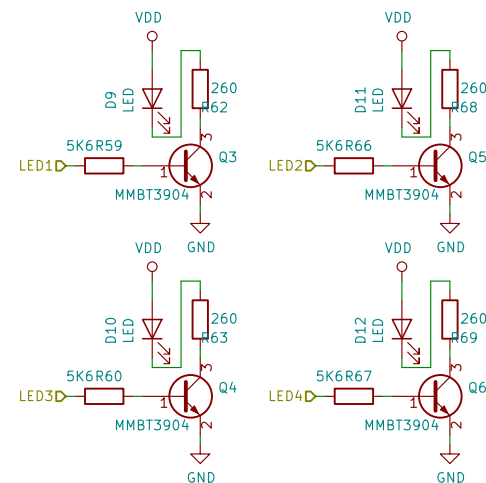
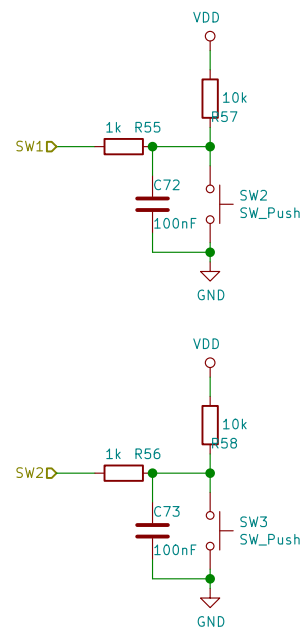
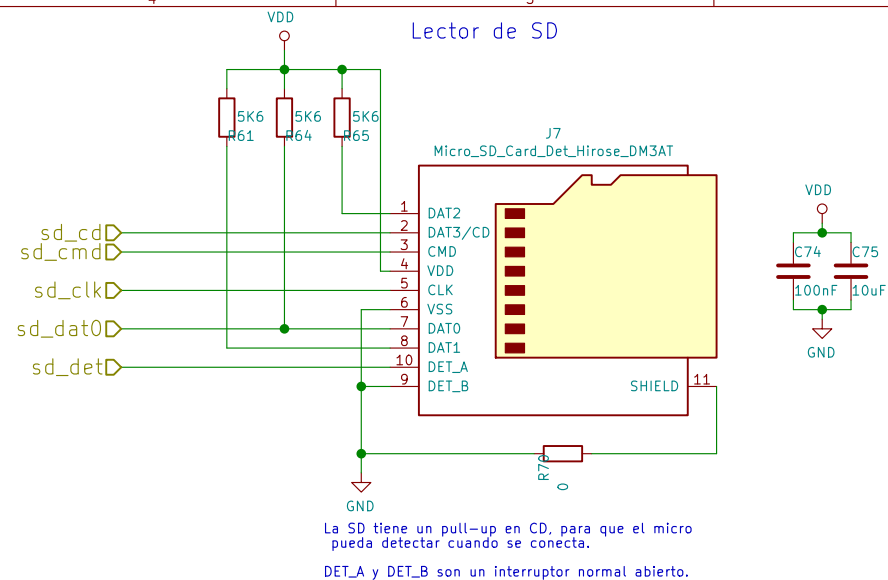
Entradas digitales



Circuito de módulo de entradas



Lector de SD



Autor: Lucas Dórdolo

Trenes Argentinos Operaciones

Sheet: /DIO/

File: DIO.sch

Title: Adquisidor genérico – Placa Base

Size: A4 Date: 2019-05-27

KiCad E.D.A. kicad (5.0.2)-1

Rev: V0.13

Id: 6/6