

A

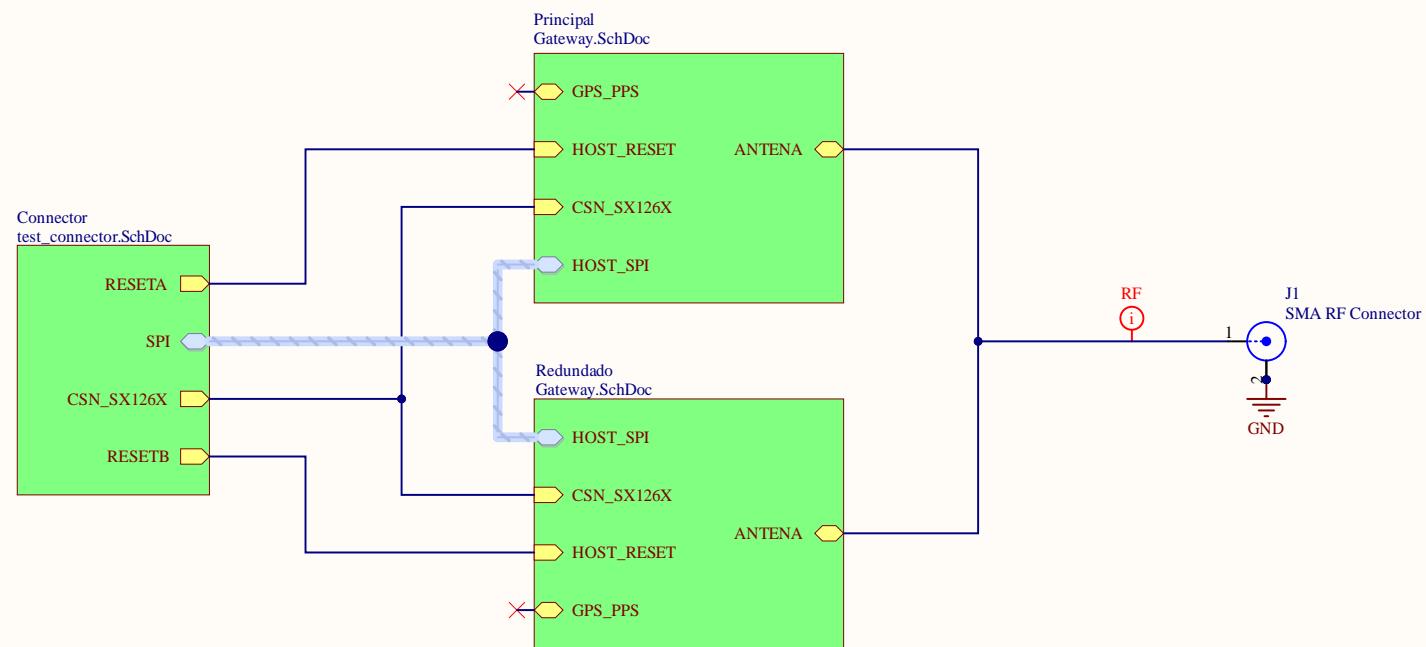
A

LoRaWAN Gateway:

Radio submodule for Cubesat

B

B



C

C

D

D

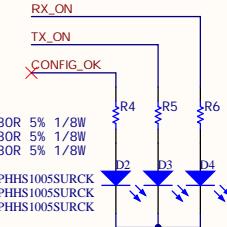
Project Title: **LoRaWAN Gateway**

Author: Lucas Lliaño | Revision: 1

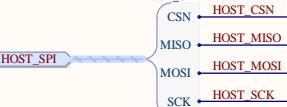
Sheet Title: System | Date: 25/7/2022

Sheet 1 of 8

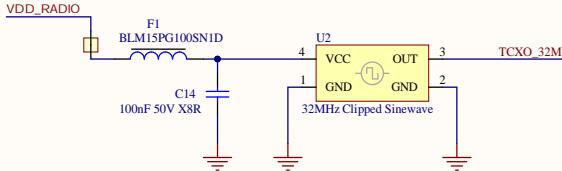
LEDS



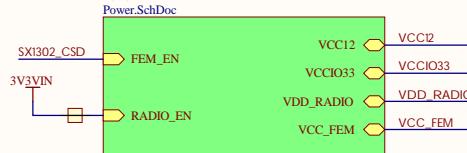
SPI BUS



32MHz CLOCK



POWER DISTRIBUTION



A

A

B

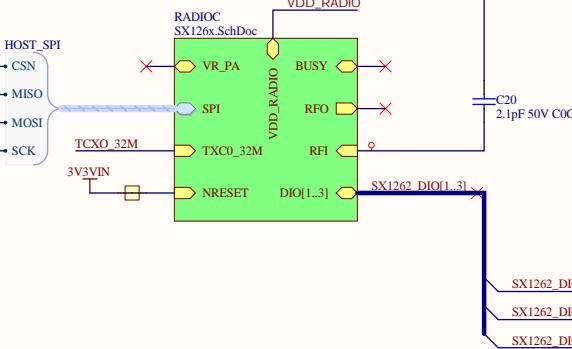
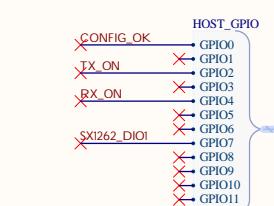
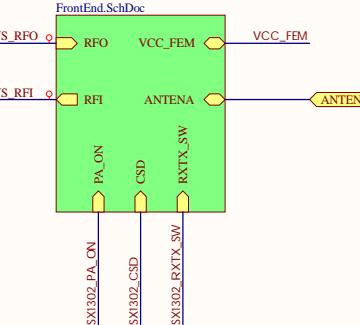
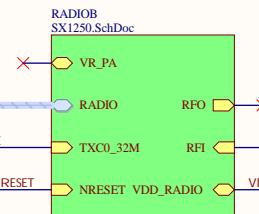
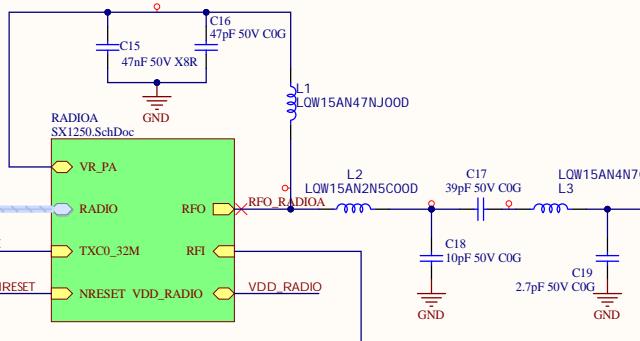
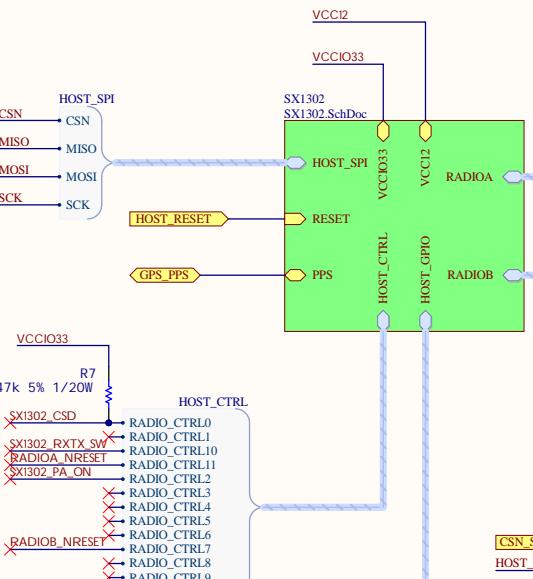
B

C

C

D

D

Project Title: **Lorawan Gateway**

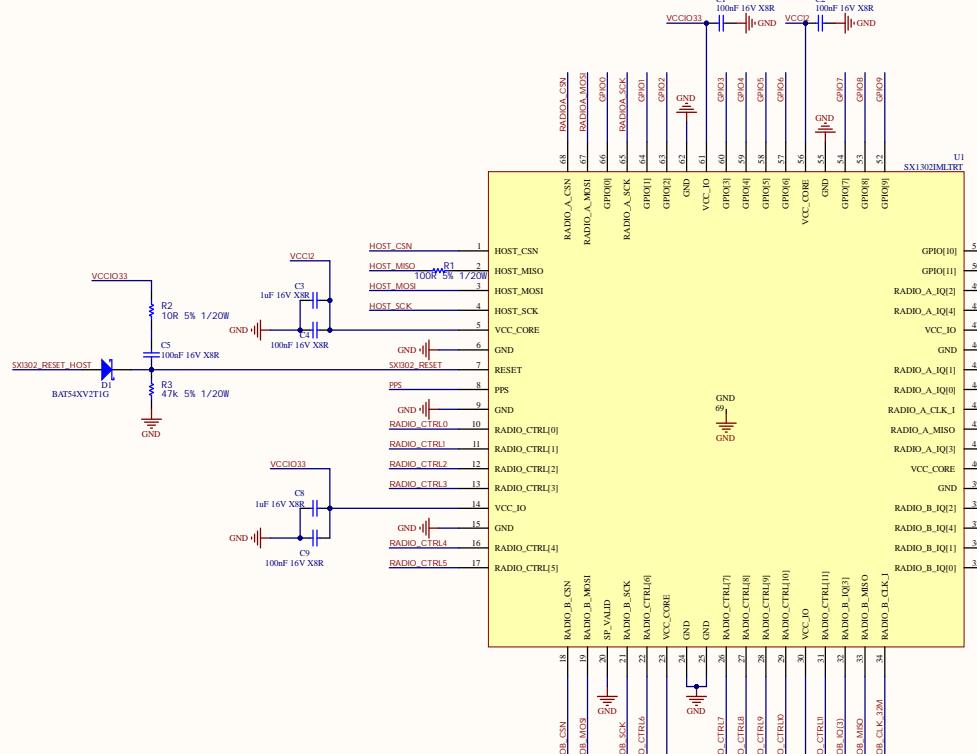
Author: Lucas Liao Revision: 1

Sheet Title: **Gateway**

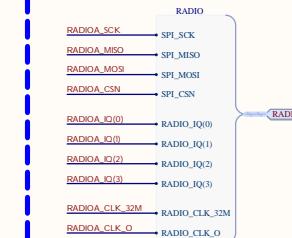
Date: 25/7/2022

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SX1302 CONCENTRATOR DESIGN



RADIO PORTS



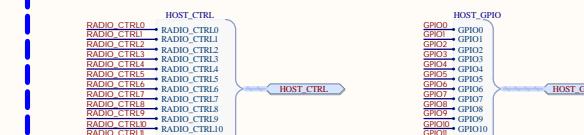
CONTROL SIGNALS

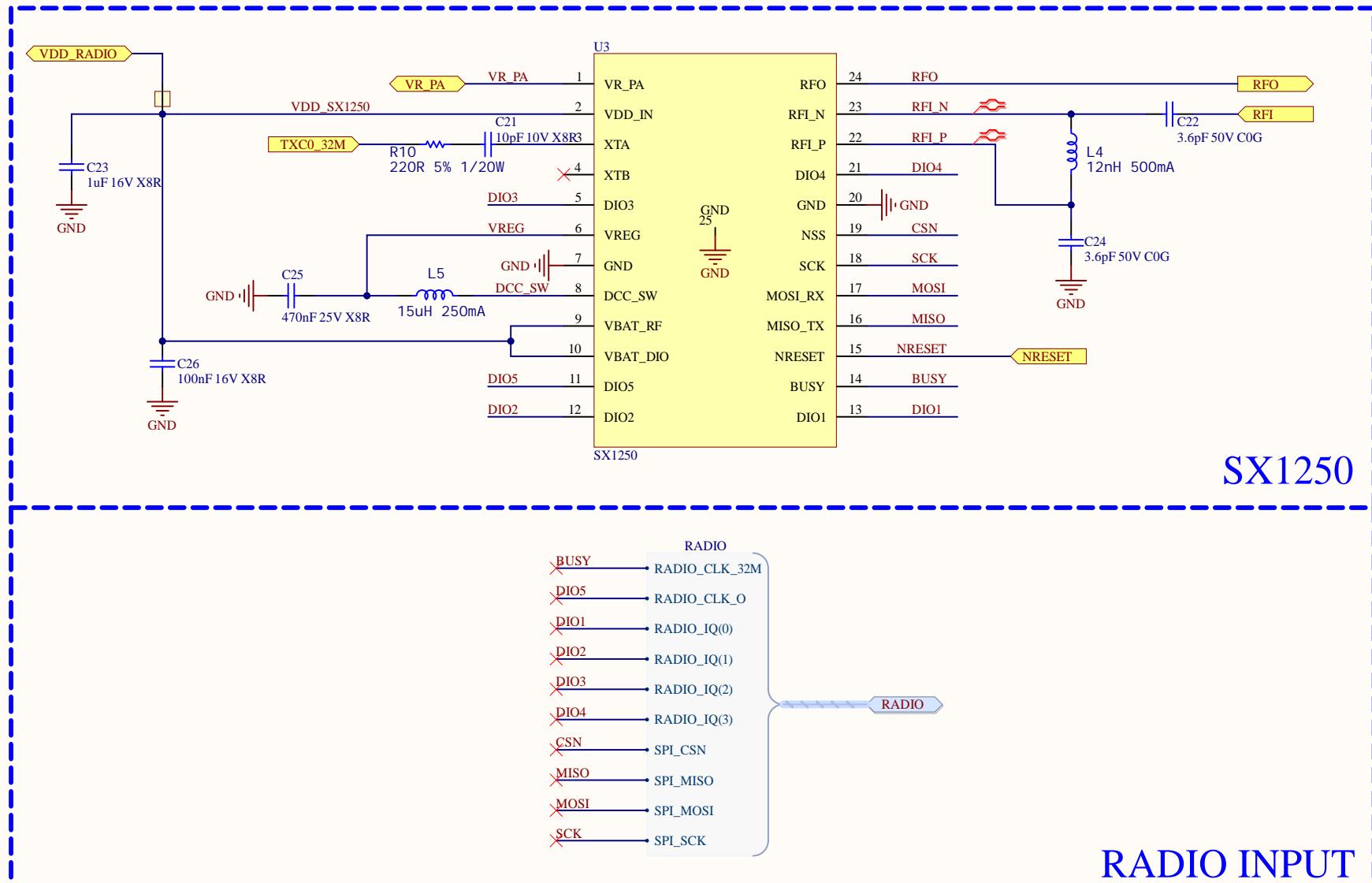


POWER SIGNALS



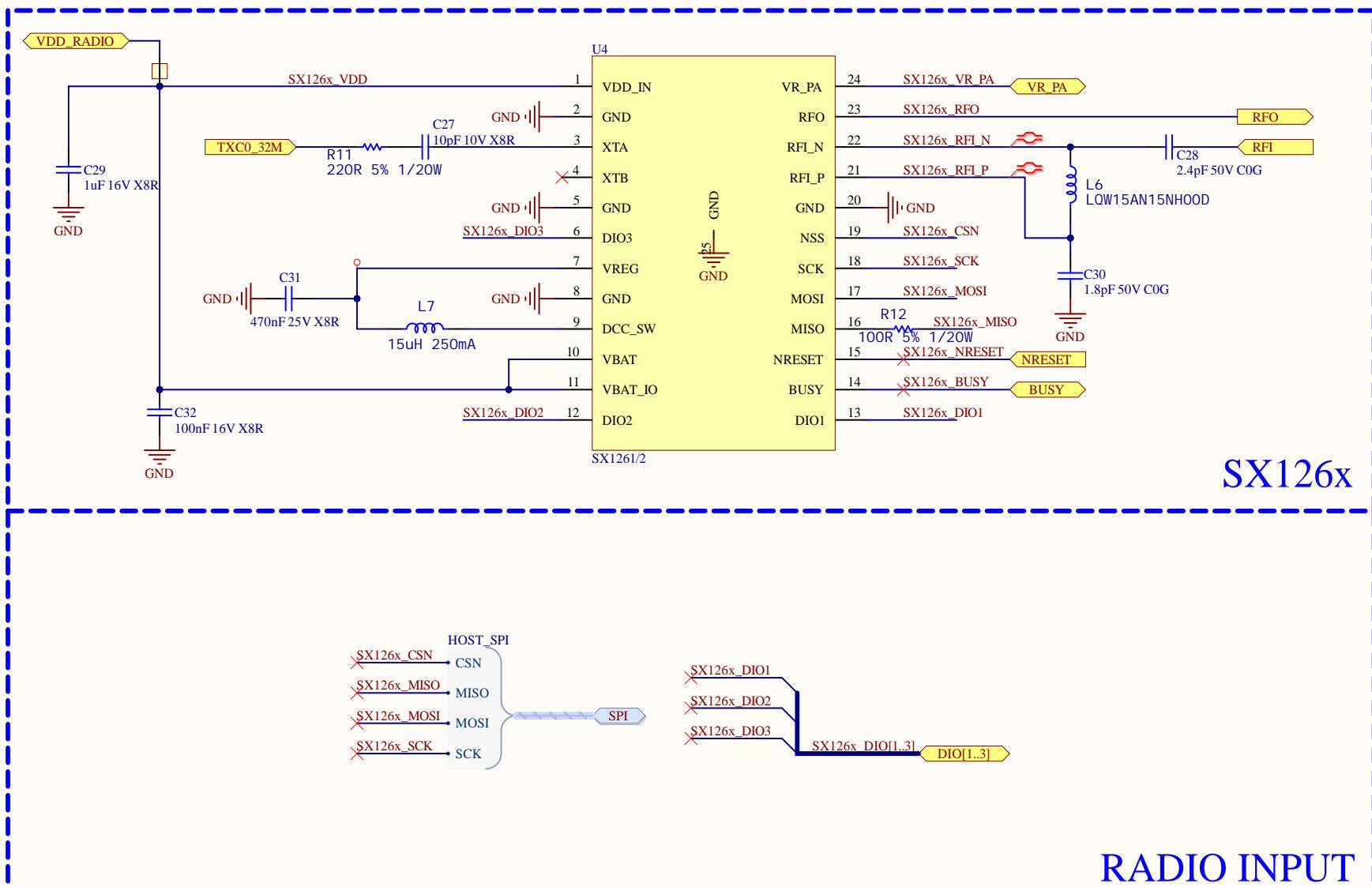
I/O PORTS





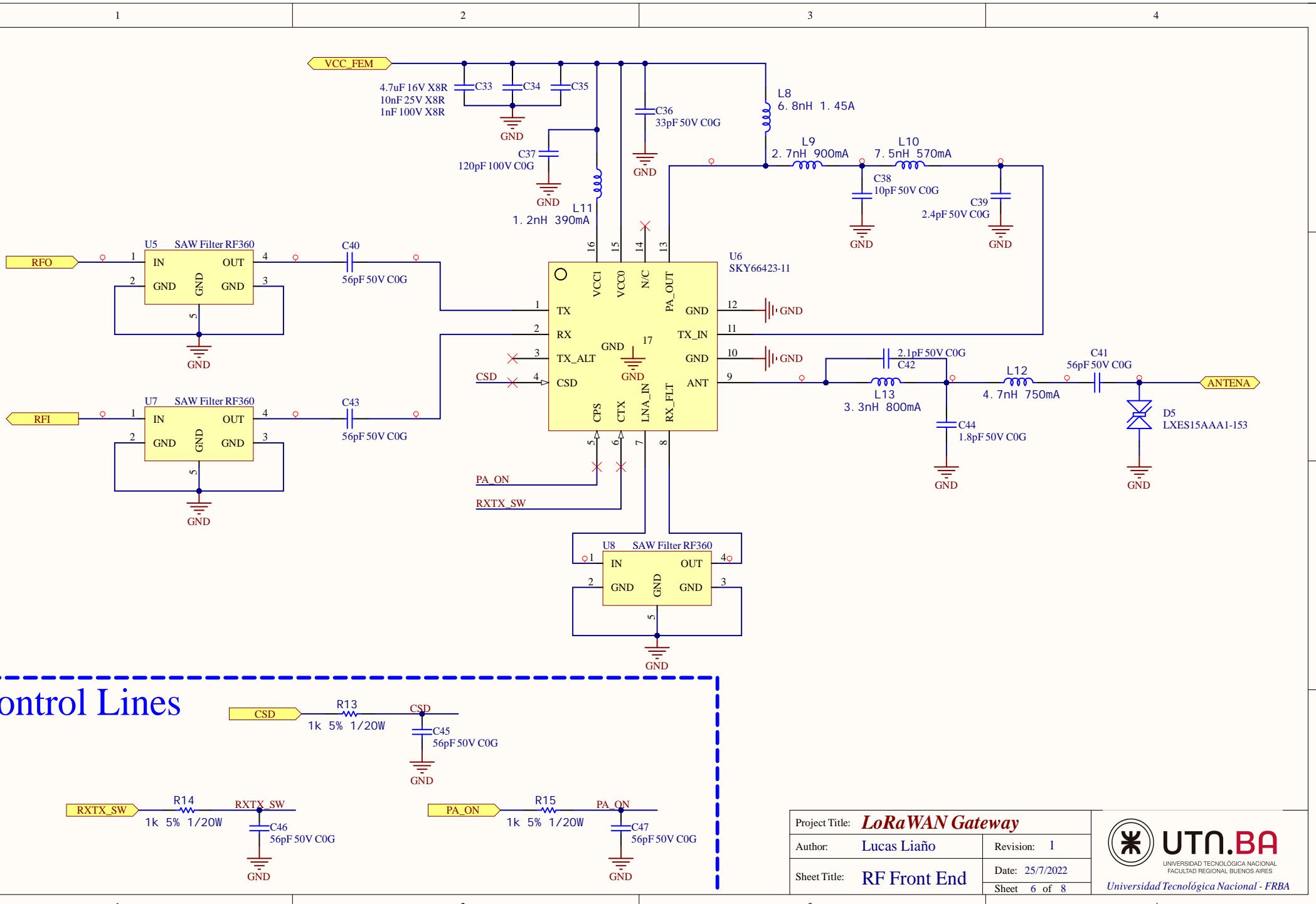
Project Title:	<i>LoRaWAN Gateway</i>	
Author:	Lucas Liaño	Revision: 1
Sheet Title:	SX1250	Date: 25/7/2022
	Sheet 4 of 8	

1 2 3 4



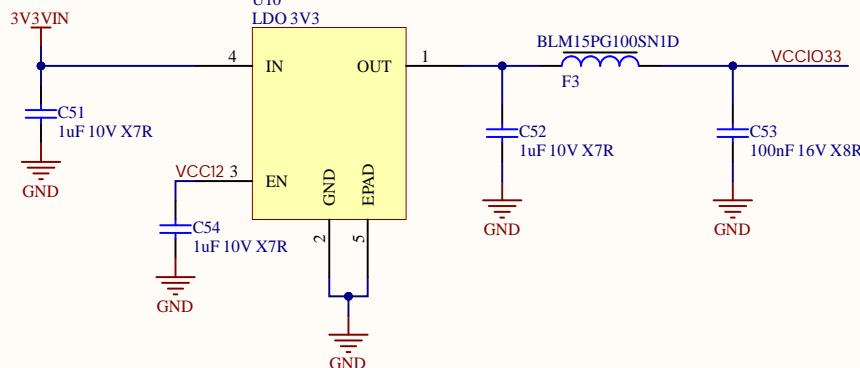
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Author:	Lucas Lliaño	Revision: 1
Sheet Title:	SX126x	Date: 25/7/2022

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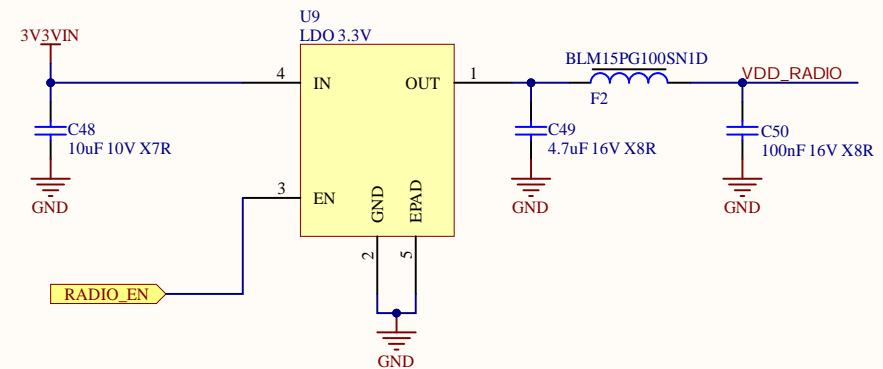


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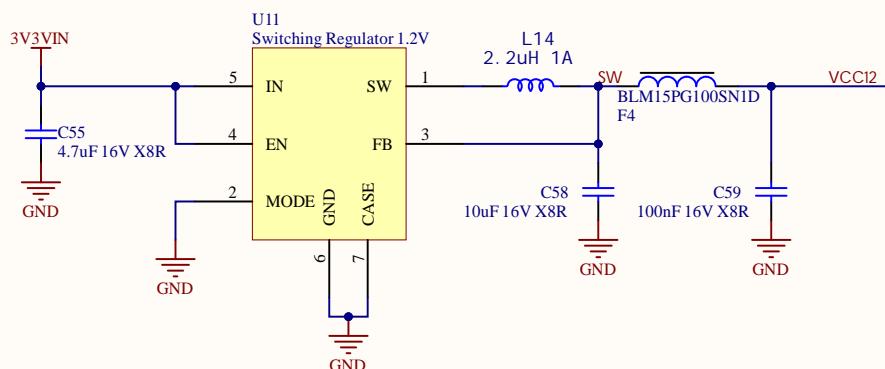
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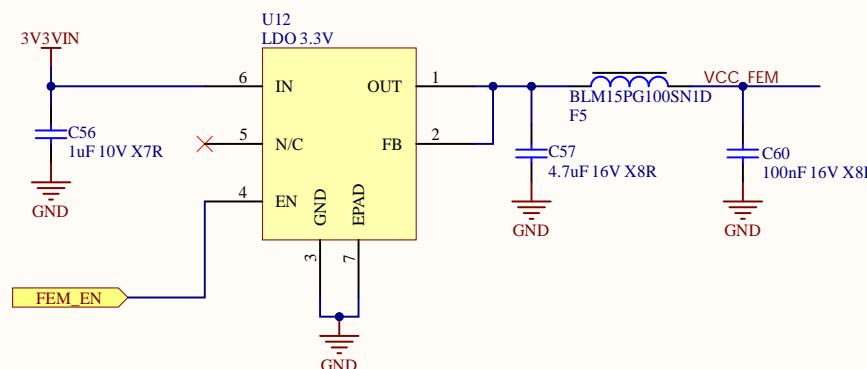
B



A



C



B

C

D

VCCIO33
VCC12

VDD_RADIO
VCC_FEM

POWER PORTS

Project Title: *LoRaWAN Gateway*

Author: Lucas Liaño Revision: 1

Sheet Title: Power banks Date: 25/7/2022

Sheet 7 of 8

A

A

B

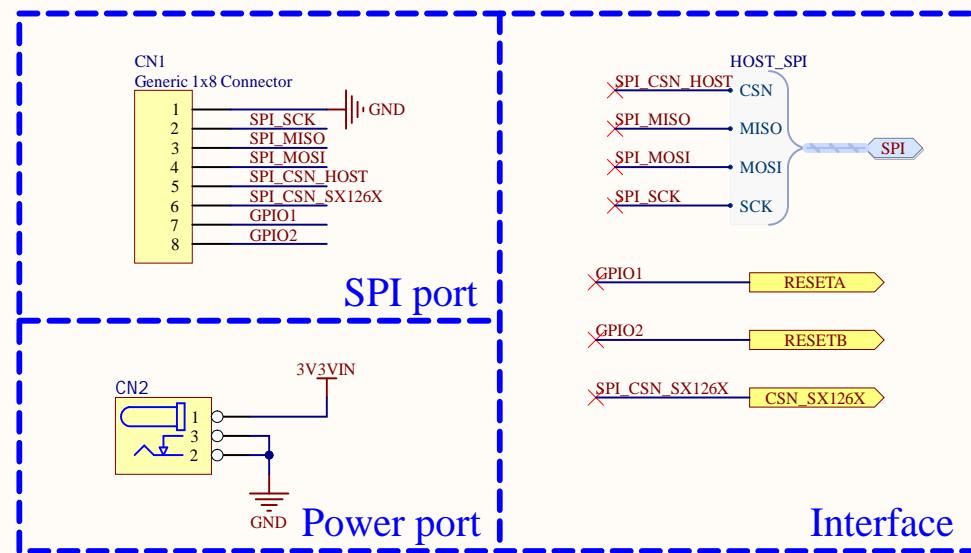
B

C

C

D

D

Project Title: **LoRaWAN Gateway**Author: **Lucas Lliaño** Revision: **1**Sheet Title: **Test Connector** Date: **25/7/2022**Sheet **8** of **8**