

A

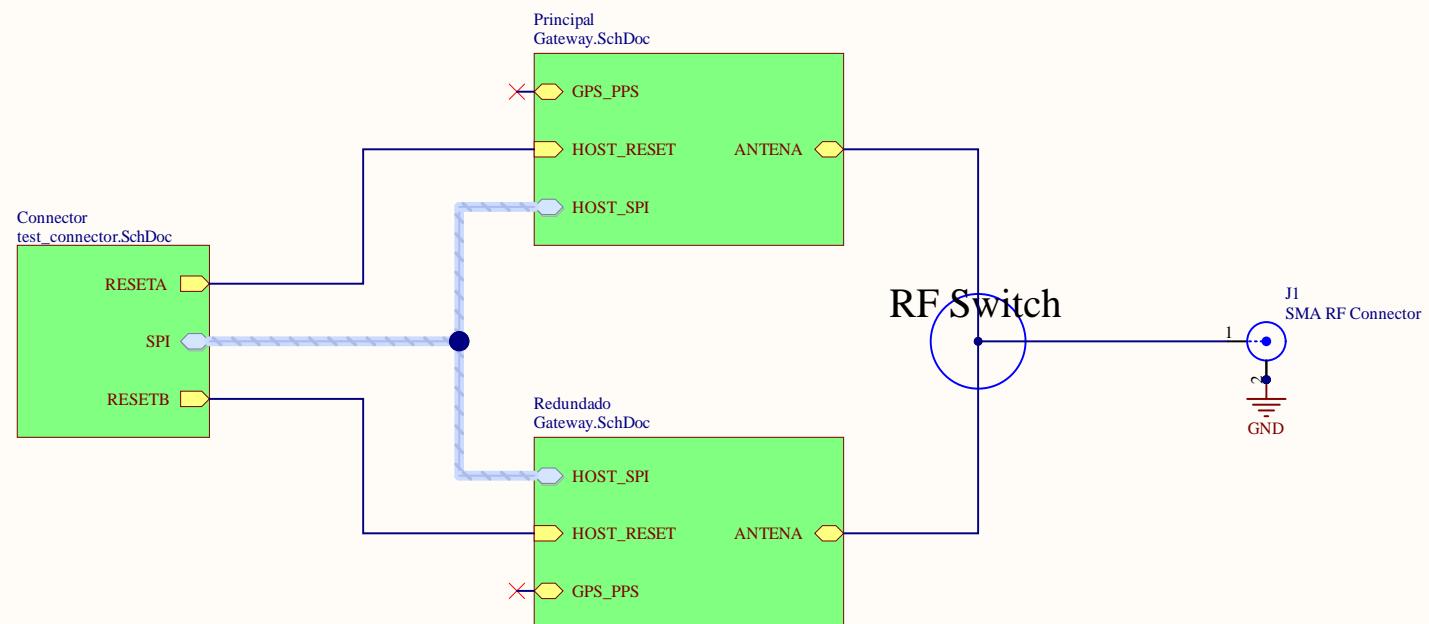
A

LoRaWAN Gateway:

Radio submodule for Cubesat

B

B



C

C

D

D

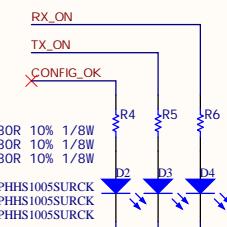
Project Title: **LoRaWAN Gateway**

Author: Lucas Lliaño Revision: 1

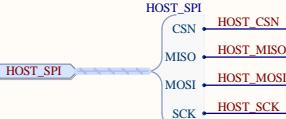
Sheet Title: System Date: 28/6/2022

Sheet 1 of 8

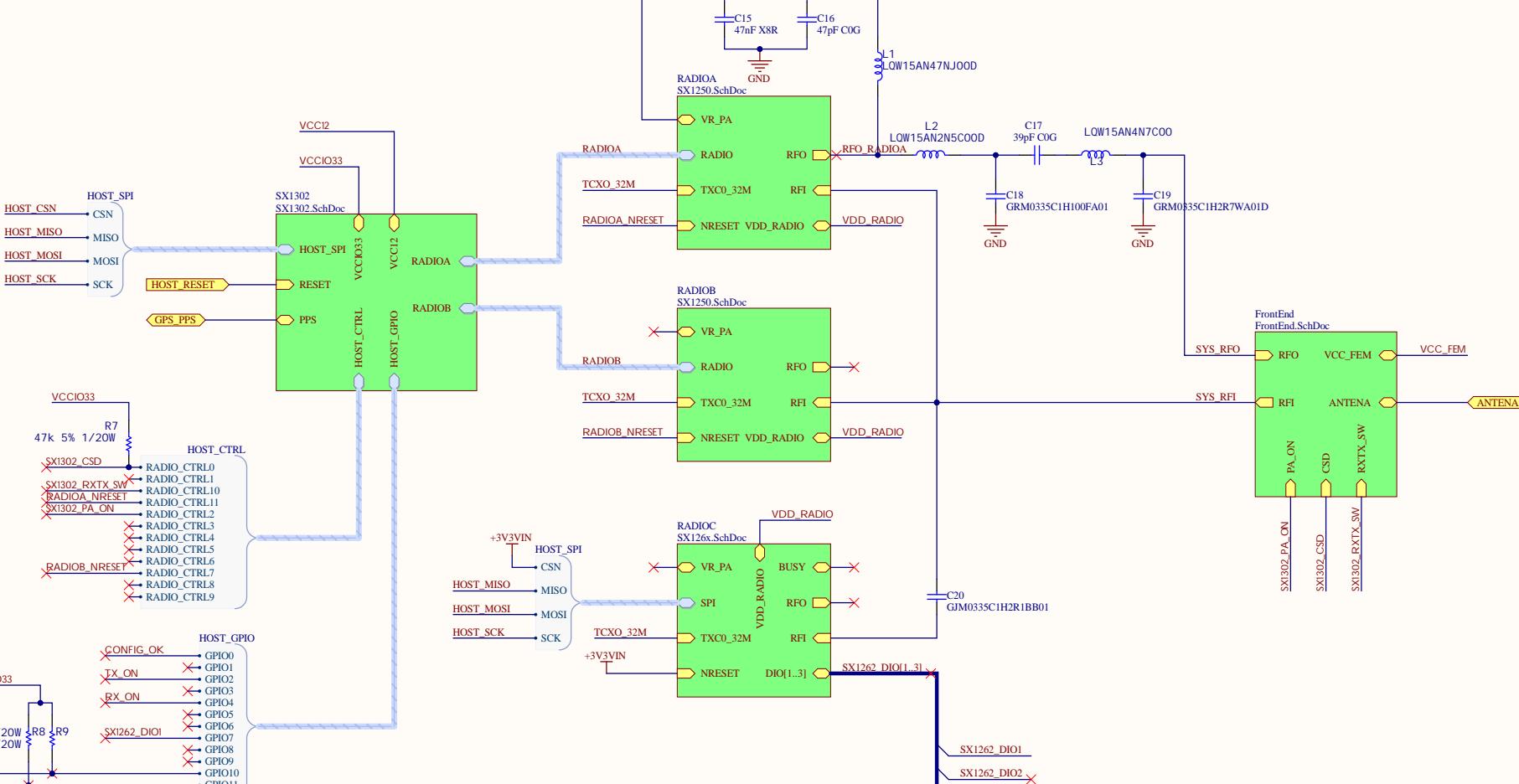
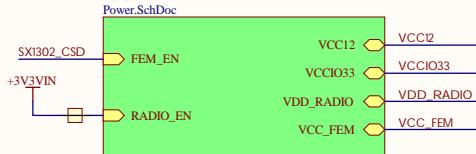
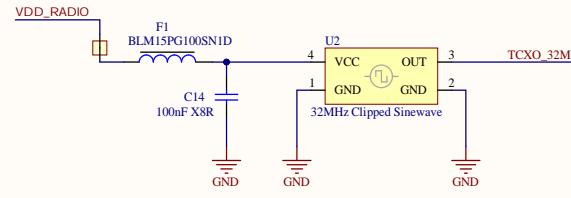
LEDS



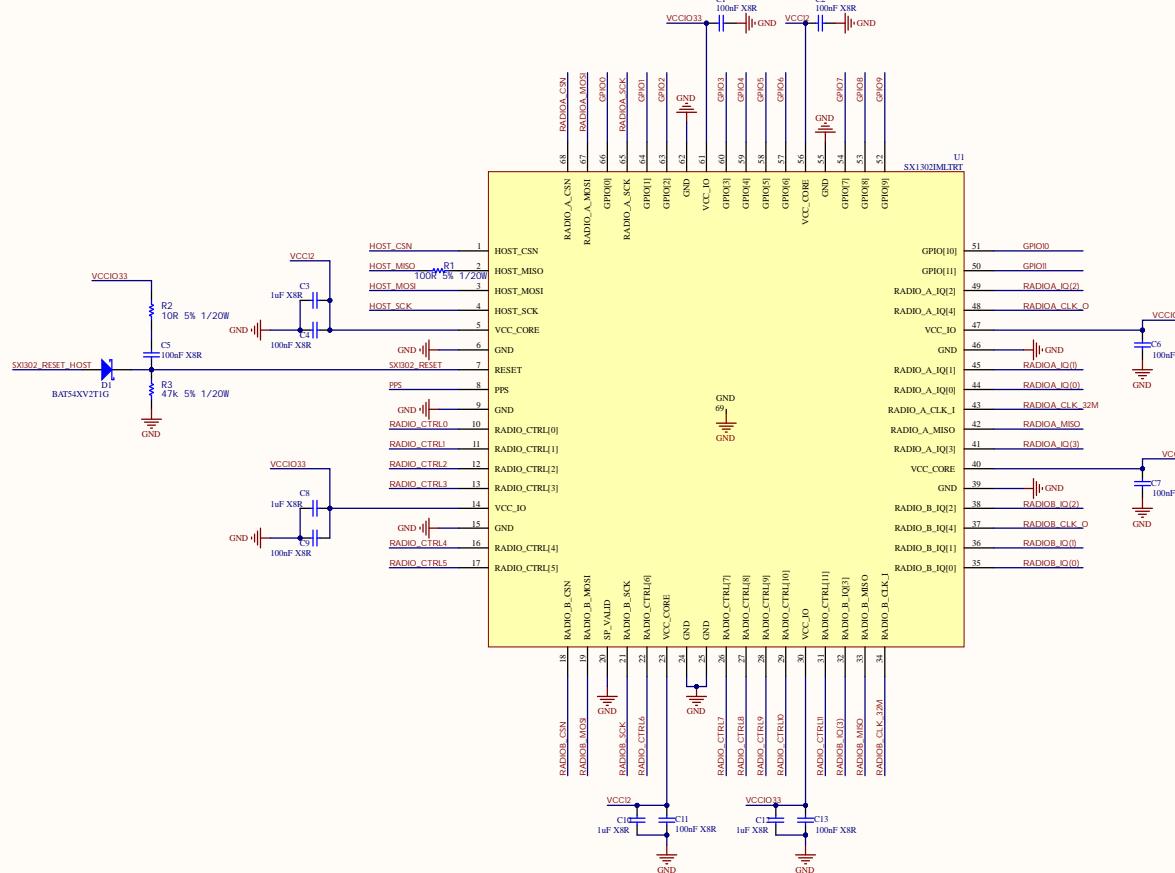
SPI BUS



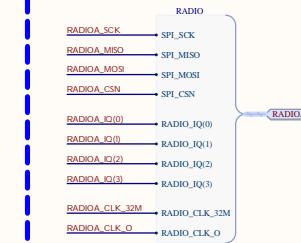
32MHz CLOCK



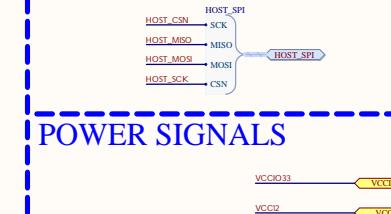
SX1302 CONCENTRATOR DESIGN



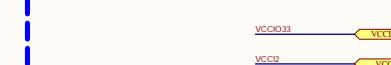
RADIO PORTS



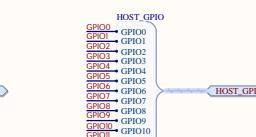
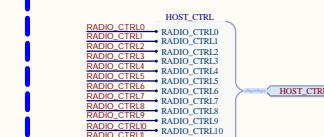
CONTROL SIGNALS



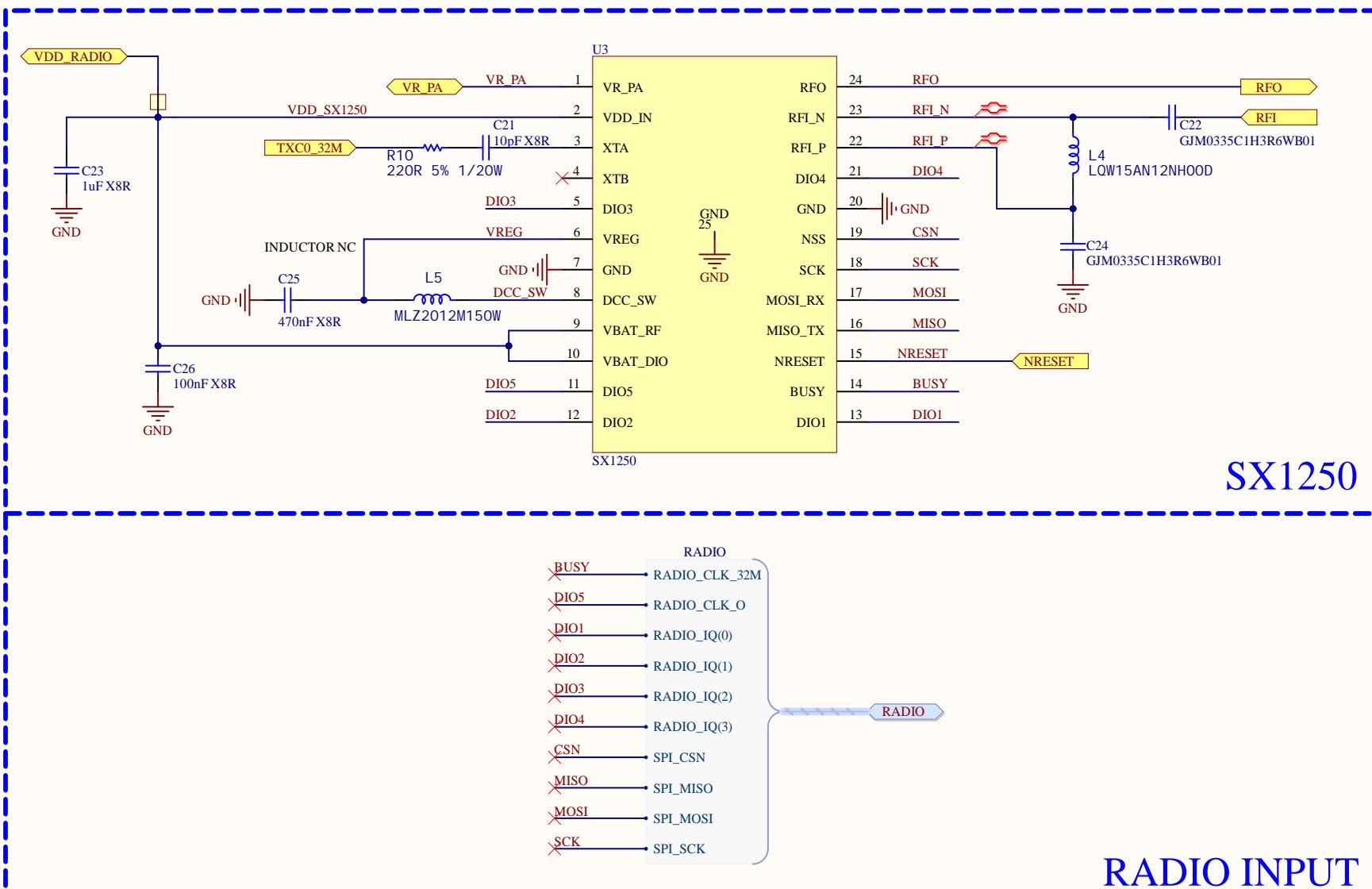
POWER SIGNALS



I/O PORTS

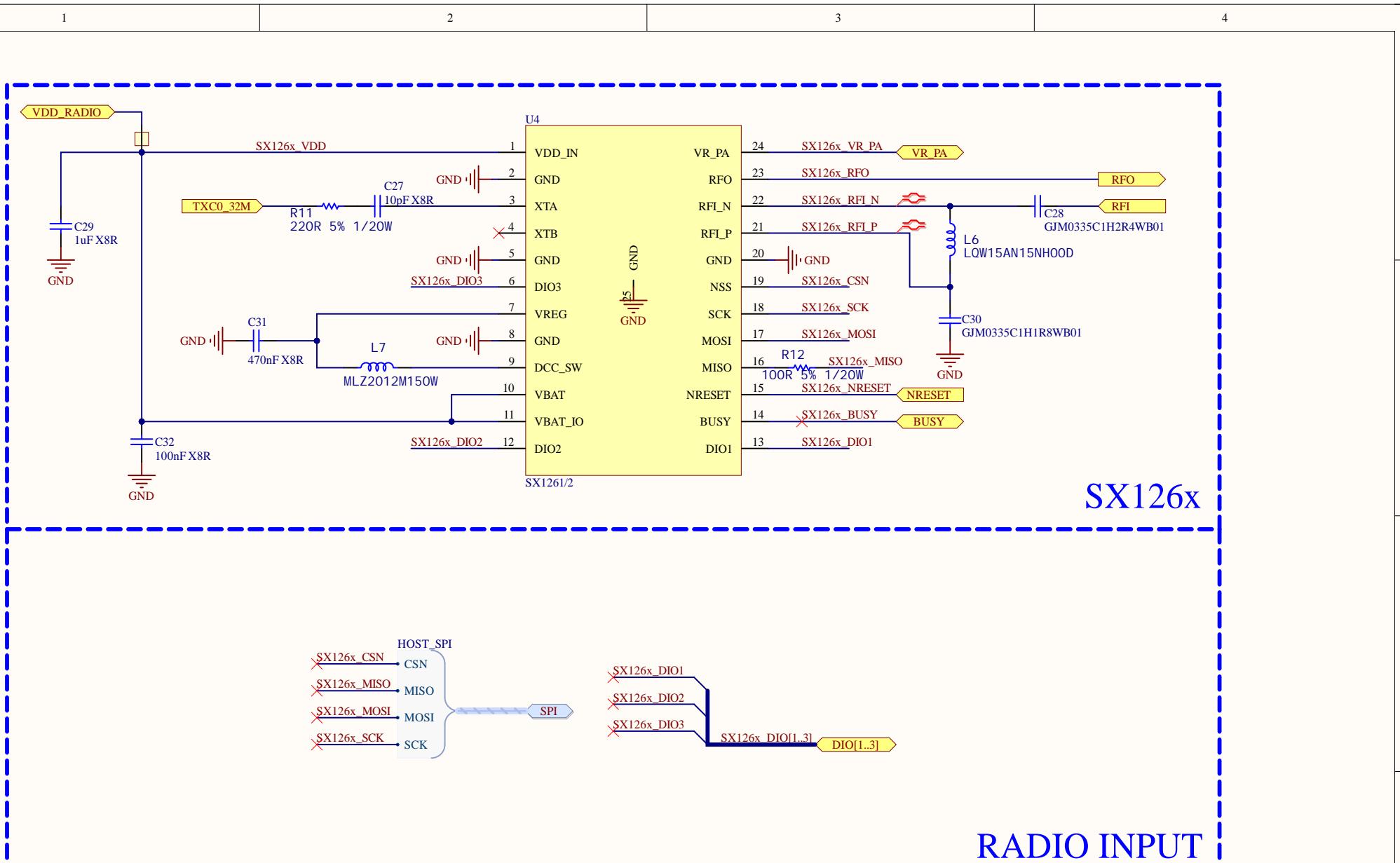


1 2 3 4

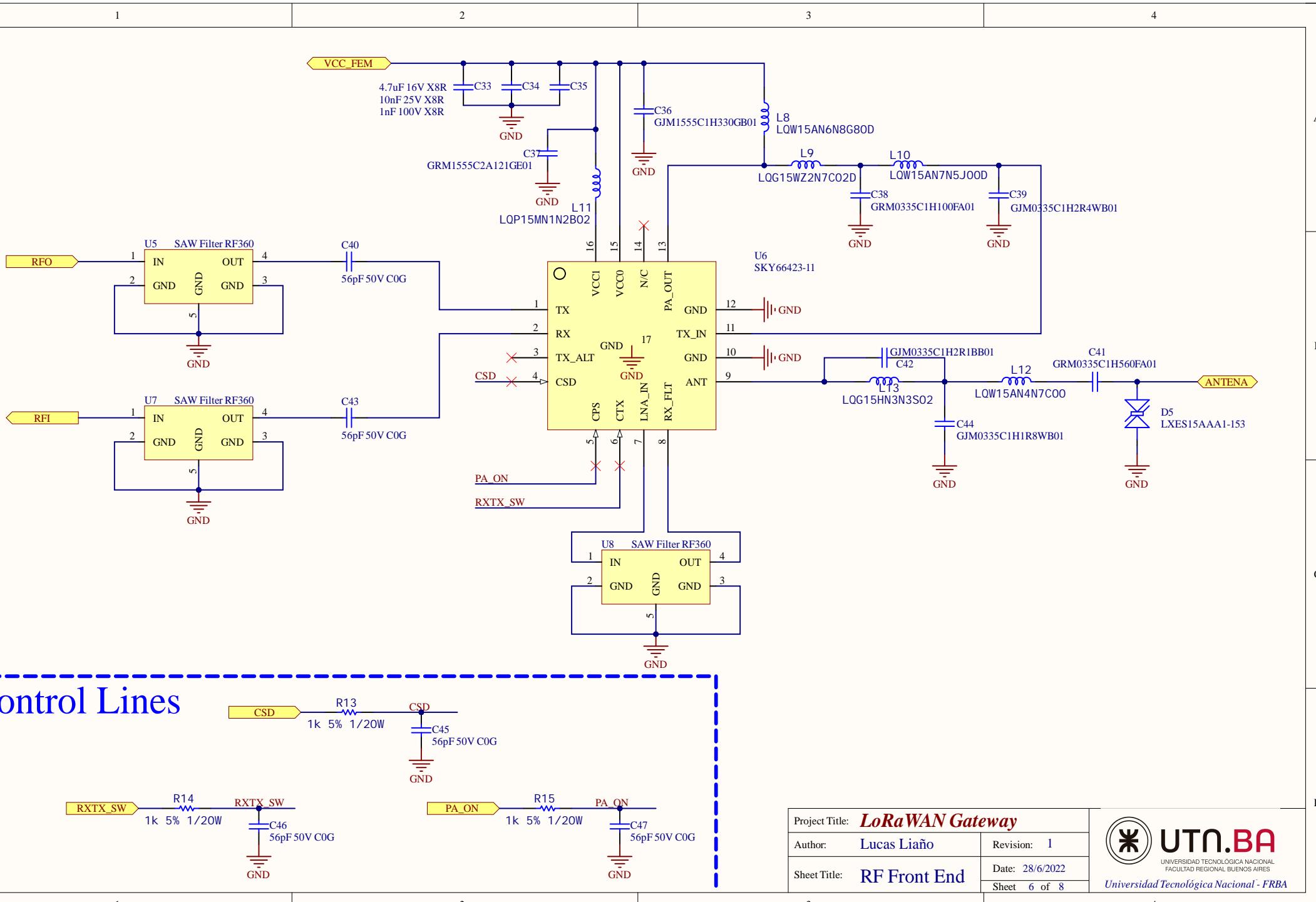


Project Title:	<i>LoRaWAN Gateway</i>	
Author:	Lucas Liaño	Revision: 1
Sheet Title:	SX1250	Date: 28/6/2022
	Sheet 4 of 8	

1 2 3 4



Project Title:	<i>LoRaWAN Gateway</i>	
Author:	Lucas Liaño	Revision: 1
Sheet Title:	SX126x	Date: 28/6/2022
	Sheet 5 of 8	



1

2

3

4

A

A

B

B

C

C

Control Lines

D

D

1

2

3

4

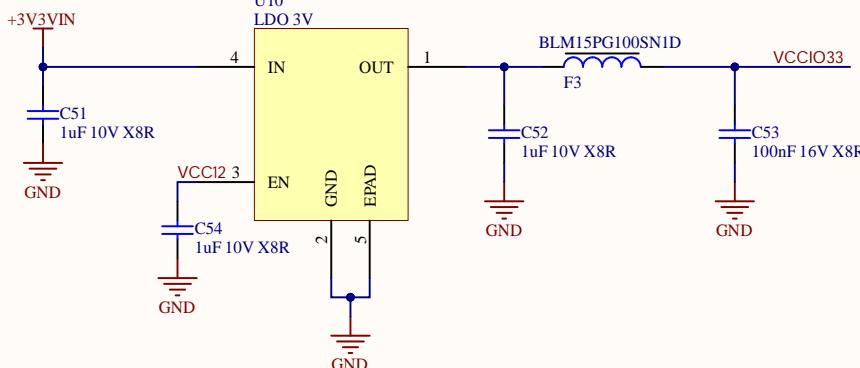
1

2

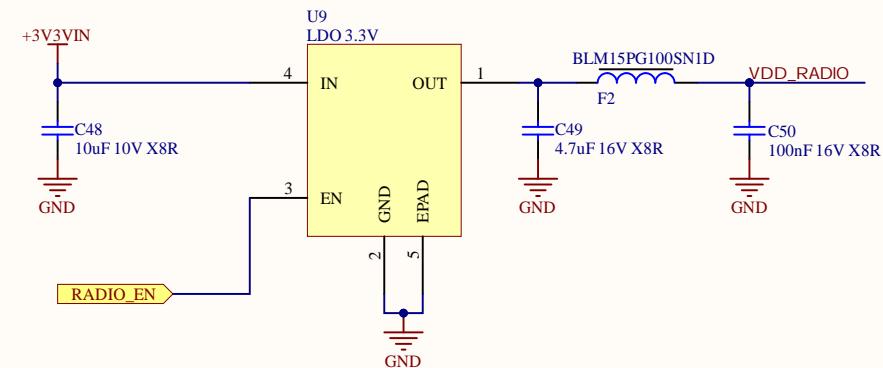
3

4

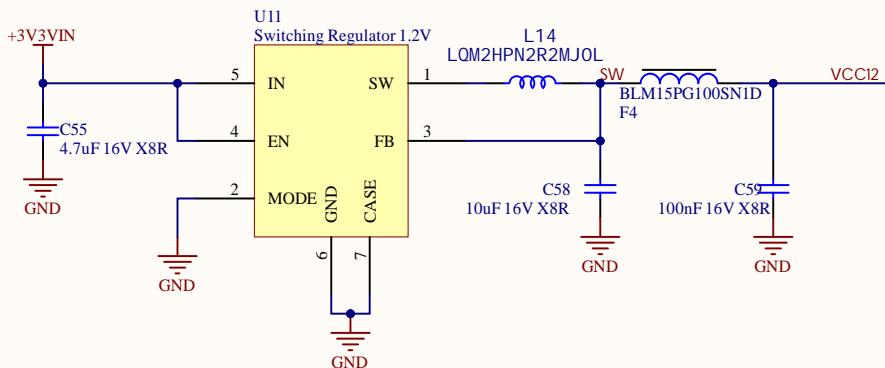
A



B



C



D



VCCIO33

VDD_RADIO

VCC12

VCC_FEM

POWER PORTS

Project Title: *LoRaWAN Gateway*

Author: Lucas Liaño

Revision: 1

Sheet Title: Power banks

Date: 28/6/2022

A

A

B

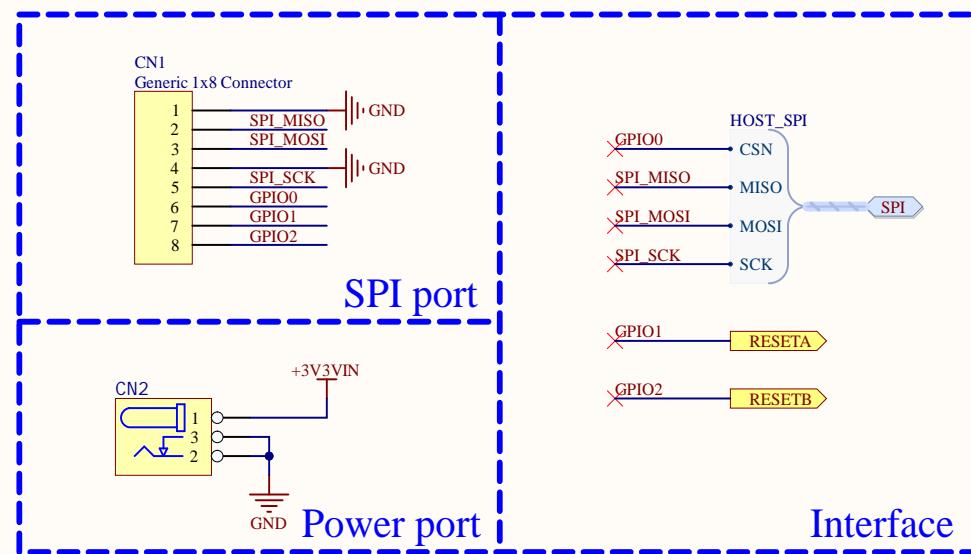
B

C

C

D

D

Project Title: **LoRaWAN Gateway**

Author: Lucas Lliaño Revision: 1

Sheet Title: Test Connector Date: 28/6/2022

Sheet 8 of 8