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SE Host Services API

Version 1.103.0 SERVICES Library 0.50.3



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SERVICES Version	SES Version	NOTES



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Irc fatc

Service Group	Notes
Maintenance	
System	
Management	
Application / Pin	
mux management	
Power	



Security /Crypto		
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Boot		
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Clock		



Update	



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Name	Date modified	Type Size
a32_startup	7/11/2023 4:15 PM	File folder
build	7/7/2023 7:51 AM	File folder
drivers	7/7/2023 7:51 AM	File folder
example	7/11/2023 4:15 PM	File folder
include	7/11/2023 6:01 AM	File folder
☐ lib	7/7/2023 7:51 AM	File folder
RTE	7/11/2023 4:15 PM	File folder
services_lib	7/11/2023 4:15 PM	File folder
💰 A32_MRAM	7/11/2023 4:15 PM	Windows Script C
	7/11/2023 4:15 PM	Windows Script C
CMakeLists	7/11/2023 4:15 PM	Text Document
CMakePresets	7/11/2023 4:15 PM	JSON File
gcc_A32_MRAM	7/11/2023 4:15 PM	LD File
gcc_A32_TCM	7/11/2023 4:15 PM	LD File
<pre>gcc_M55_HE_MRAM</pre>	7/11/2023 4:15 PM	LD File
<pre>gcc_M55_HE_TCM</pre>	7/11/2023 4:15 PM	LD File
<pre>gcc_M55_HP_MRAM</pre>	7/11/2023 4:15 PM	LD File
<pre>gcc_M55_HP_TCM</pre>	7/11/2023 4:15 PM	LD File
License	7/7/2023 7:51 AM	Text Document
	7/11/2023 4:15 PM	Windows Script C
	7/11/2023 4:15 PM	Windows Script C
	7/11/2023 4:15 PM	Windows Script C
	7/11/2023 4:15 PM	Windows Script C
Makefile_linux	7/7/2023 7:51 AM	File
■ README	7/11/2023 4:15 PM	Markdown Source
toolchain-armclang	7/11/2023 4:15 PM	CMake Source File
toolchain-gnu	7/11/2023 4:15 PM	CMake Source File

Name	Purpose	Notes



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[SES] es0 ppu_isr=0x80
[SES] es0 ppu_aisr=0x2

[SES] es0 PPU PPU_PWRP=0x100 PPU_PWSR=0x108
[SES] es0 ppu_isr=0x80
[SES] es0 ppu_aisr=0x2

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Flag	Meaning

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r fft c crl fottbccrlarfft



\$ cmake .. -G "Unix Makefiles" -DENSEMBLE_CORE=M55_HE -DCMAKE_TOOLCHAIN_FILE=toolchain-armclang.cmake -DINSTALL_DIR=../junk
-- [INFO] Version=9
-- [INFO] installation override, using ../junk
-- The C compiler identification is ARMClang 6.18.2
-- The CXX compiler identification is ARMClang 6.18.2
-- The ASM compiler identification is ARMClang 6.18.2



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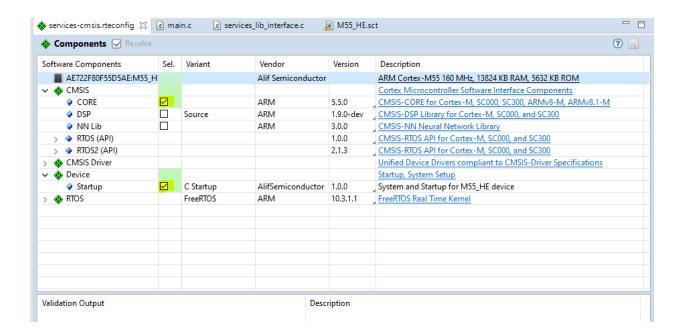
```
[SES] CM0+ frequency is 100 MHz
[SES] os Kernel U10.4.2
[SES] Ain Task - looping forever...
[SRN] RKC-- SID= 0x0CE, Receiver ID=4, Address=0x9083FF88
[ITY] SEDICES version 0.6.
[ITY] ** TEST heartbeat
[ITY] ** TEST heartbeat
[ITY] ** TEST padcontrol
[ITY] ** TEST padcontrol
[ITY] ** TEST crypto IRNG 64 error_code=SERVICES Req_SUCCESS
[ITY] ** TEST crypto IRNG 22 error_code=SERVICES Req_SUCCESS
[ITY] ** TEST crypto TRNG 22 error_code=SERVICES Req_SUCCESS
[ITY] ** TEST crypto TRNG 22 error_code=SERVICES Req_SUCCESS
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[ITY] ** TEST crypto IRNG 25 error_code=SERVICES Req_SUCCESS
[ITY] ** TEST TOC via name HP error_code=SERVICES Req_SUCCESS
[ITY] ** TEST TOC via name HP error_code=SERVICES Req_SUCCESS
[ITY] ** TEST TOC via cpuid error_code=SERVICES Req_SUCCESS
[ITY] ** TEST TOC
```



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Software Components	Sel.	Variant	Vendor	Version	Description	
# AE722F80F55D5AE:M55_H			Alif Semiconductor		ARM Cortex-M55 160 MHz, 13824 KB RAM, 5632 KB ROM	
> & BSP			7 III Sellincolladetol		The second secon	
> do CMSIS					Cortex Microcontroller Software Interface Components	
> & CMSIS Driver					Unified Device Drivers compliant to CMSIS-Driver Specifications	
✓ ♦ Device					Startup, System Setup	
> OSPI XIP						
→ SE Services						
MHU Driver			AlifSemiconductor	0.63.0	Message Handling Unit driver for Alif Soc	
SE RunTime services	\square	Lib	AlifSemiconductor	0.63.0	SE runtime Services library for RTSS cores	
> SOC Peripherals						
Startup		C Startup	AlifSemiconductor	1.0.0	System and Startup for M55_HE device	
> 💠 FreeRTOS						
> 💠 RTOS		AzureRTOS	AlifSemiconductor	0.2.1	Alif Semiconductor port of AzureRTOS for its M55 device	

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Irc fatc fft

Component	Description

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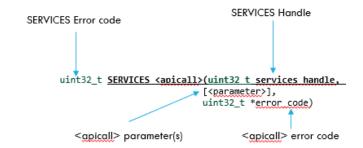


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ALIF
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```
typedef struct
{
  service header t header;
  volatile uint32 t send <param>;
                                      /*!< Send parameter */
 volatile uint32_t resp_<param>;
                                       /*!< Return parameter */
 volatile uint32 t resp error code;
                                       /*!< Call error code */
} example service t;
void SERVICES example call(services req t *service)
  example service t *p svc =
    (example service t *)service->pkt buffer address; /* services request */
  uint32 t error code;
 uint32 t local result;
 error code = function call(p svc->send <param>, &local result);
                        = local result;
 p_svc->resp_<param>
  p svc->resp error code = error code;
 SERVICES send response code(service, SERVICES REQ SUCCESS);
```

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Shared memory buffer

Common header

Request fields Response fields Request fields Response fields

Service-specific structure

Common header format

Error Code: resp

Flags: req

Padding

typedef struct { uint16_t send_sid; uint16_t send_flags; uint16_t resp_error_code; // transport layer error code uint16_t none_padding; } service_header_t;

Service-specific structure example

```
typedef struct
{
    service_header_t header;
    uint8_t send port num;
    uint8_t send pin num;
    uint8_t send config_data;
    uint8_t resp_error_code; // service-specific
    error code
} pinmux_svc_t;
```

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Irc fatc fft Ir I Alc
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Irc fatt fft I fft

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Ircfatcfft clft fft



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A rc cr rla c fft

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These Services are not intended to be used directly by applications

SERVICES_cryptocell_mbedtls_hardware_poll	
Syntax:	
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SERVICES_cryptocell_mbedtls_aes_init	
Syntax:	
Description:	
SERVICES_cryptocell_mbedtls_aes_set_key	
Syntax:	



Description:
SERVICES_cryptocell_mbedtls_aes_crypt Syntax:
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Syntax:
Description:



SEMICONDUCTOR
SERVICES_cryptocell_mbedtls_sha_process
Syntax:
Description:
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SERVICES_cryptocell_mbedtls_sha_update
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Description:
SERVICES_cryptocell_mbedtls_sha_finish Syntax:
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SERVICES_cryptocell_mbedtls_ccm_gcm_set_key
Syntax:
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Description:
SERVICES_cryptocell_mbedtls_ccm_gcm_crypt
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SERVICES_cryptocell_mbedtls_chacha20_crypt
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SERVICES_cryptocell_mbedtls_chachapoly_crypt
Syntax:
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SERVICES_cryptocell_mbedtls_poly1305_crypt
Syntax:
Description:
SERVICES_cryptocell_mbedtls_cmac_init_setkey
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SEMICONDUCTOR
SERVICES_cryptocell_mbedtls_cmac_update
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SERVICES_cryptocell_mbedtls_cmac_finish
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SERVICES_cryptocell_mbedtls_cmac_reset
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Version	Change Log