# ELEC 433 Report

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#### 1 Introduction

### 2 LDPC codes

LDPC codes, or low-density parity-check codes, are a class of linear block codes that are characterized by sparse parity-check matrices. The sparsity of the parity-check matrix allows for efficient encoding and decoding algorithms. LDPC codes are also known to achieve near Shannon capacity performance when decoded using iterative message-passing algorithms.

### 2.1 Types of LDPC codes

There are two main types of LDPC codes: regular and irregular. Regular LDPC codes have a constant column weight and row weight, while irregular LDPC codes have varying column and row weights. Regular LDPC codes are easier to analyze and implement, but irregular LDPC codes can achieve better performance.

#### 2.1.1 Regular LDPC codes

Gallager developed the LDPC code as his Ph.D. thesis in 1962. The parity-check matrix of a regular LDPC code is defined by the following equation:

A regular LDPC code (j,k) has a weight of j for each column and a weight of k for each row.1 An example of a parity-check matrix for a regular LDPC code where j=3 and k=4 is shown below:

•	v v .											
	Γ1	1	1	1	0	0	0	0	0	0	0	0
	0	0	0	0	1	1	1	1	0	0	0	0
	0	0	0	0	0	0	0	0	1	1	1	1
	1	0	0	0	1	0	0	0	1			0
	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	1		0	0	1	0	0		1	0
	0	0	0		0	0	0	1	0	0	0	1
	1	1	0		0	0	1	1	0	0	0	0
	0	0	1	1	0	0	0	0	1	1	0	0
	0	0	0	0	1	1	0	0	0	0	1	1

So this is a (3,4) regular LDPC code where the column weight is 3 and the row weight is 4. The dimension of the code is n = 12 and n - k = 9.

The rate 
$$R = 1 - \frac{j}{k} = \frac{k}{n} = \frac{1}{4}$$

#### 2.1.2 Irregular LDPC codes

Irregular LDPC codes have varying column and row weights. However, they are distributed uniformly across the parity-check matrix. Irregular LDPC codes can achieve better performance than regular LDPC codes, but they are more difficult to analyze and implement.

#### 2.2 Tanner graph

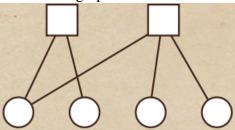
A Tanner graph is a bipartite graph that represents the parity-check matrix of an LDPC code. The Tanner graph consists of two sets of nodes: variable nodes and check nodes. The edges of the graph connect variable nodes to check nodes and vice versa.

The Tanner graph is used to visualize the structure of the LDPC code and to develop efficient decoding algorithms.

For example, for this matrix:

$$\begin{bmatrix} 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 1 \end{bmatrix}$$

The Tanner graph is shown below:



A cycle in a Tanner graph is a closed path that starts and ends at the same node.

A **girth** of a Tanner graph is the length of the shortest cycle in the graph. A larger girth is desirable because it leads to better error-correction performance.

#### 2.3 Generator matrix

Given a parity-check matrix H for an LDPC code, the generator matrix G can be constructed using the following steps:

- 1. Assume that the encoded code c of length n is composed of the parity bits b and the message bits m in a form c = [b, m].
- 2. Then if there are no errors in the code,  $cH^T=[b,m]H^T=0$ .
- 3. Divide H into  $H = [H_1, H_2]^T$  where  $H_1$  is a square matrix of size n k. Note that b is of length n k as well. So we can break the matrix multiplication into  $[b, m][H_1, H_2]^T = bH_1^T + mH_2^T = 0$ .

- 4. Since we are in a binary field,  $bH_1^T=mH_2^T \implies b=mH_2^T(H_1^T)^{-1}$ . Name the matrix  $H_2^T(H_1^T)^{-1}$  as A.
- 5. So the generator matrix G is  $[A, I_{n-k}]$ .

## 3 LDPC on FPGA

# 4 Hamming code as comparison

# 5 Conclusion

## 6 References

1. Brian Kurkoski, Introduction to Low-Density Parity Check Codes, https://www.jaist.ac.jp/~kurkoski/teaching/portfolio/uec\_s05/S05-LDPC%20Lecture%201.pdf