Han Lu

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EDUCATION

ACM Honors Class, Shanghai Jiao Tong University

China, Shanghai

Bachelor of Computer Science at Zhiyuan College

Sep 2017 – Present

Zhiyuan Collage is for training outstanding students in the basic sciences ACM Honors Class is an elite CS program for top 5% talented students.

o GPA:3.79/4.3 Score: 88.73/100 Ranking: 10/46

RESEARCH EXPERIENCE

Research Internship

Iune 2019 - Present

- Machine Vision and Intelligence Group, SJTU
 - Advised by Cewu Lu and finsh the course cs231n of stanford online
 - Do some works about Human-Object Interactions(HOI)
 - Modify and utilize 3D construction models like SMPL and SMPL-X
 - Make contribution to the paper Detailed 2D-3D Joint Representation for Human-Object Interaction for CVPR 2020

Multi-task Driven Network for Indoor Layering Prediction with Human-in-loop

course project

o I and my team members implemented a human-in-loop Multi-task Driven Network to give layering of images' spatial structure. The paper is accepted by Computer and Technology Applications(ICCTA) 2020.

Fine-grained image categorization

course project

o I and my team members did research in Fine-grained image categorization and gave a presentation on the subject. The Graph Convolutional Network(GCN) is also used by us to do some pilot experiments.

AWARDS AND COMPETITIONS

Zhiyuan Honorary Scholarship

2018&2019

top 5% of 17,000 students in SJTU

2019 Mathematical Contest In Modeling

2019

Meritorious winner

The 33nd China National Olympiad in Informatics(NOI)

2016

Sliver Award

The 10th Asia-Pacific Informatics Olympiad (APIO)

2016

Gold Award

PROJECTS

MX-Compiler

(Score 95/100) course project

A simple complier of MX* language(a simplified language like Java) with NSAM assembly output. I
implemented several optimization and the performance was close to GCC -O1 in certain test sets.

Tiny Tensorflow

(Score 97/100) course project

A simplified Tensorflow for CNN network with auto diff and back propagation. Its backend is built by c++
in order to improve its performence.

RISCV CPU course project

o Designed and implemented a RISC-V CPU with 5-stage pipeline implemented in Verilog HDL.

OTHER EXPERIENCE

Teaching Assistant 2018&2019

- CS152: C++ Programming (B)
 - Give lecture about some basic algorithms and data structures.
 - Prepare the last and hardest algorithm problems for four online examination

Invited Topic Presenter

2018&2019

- o CS358: Data Structures: Give algorithms and data structures lectures
- o Mathematical Contest In Modeling training camp in SJTU for 200+ students
- o China University modeling competition training camp in SJTU for 200+ students

Publications

 Detailed 2D-3D Joint Representation for Human-Object Interaction Yong-Lu Li, Xinpeng Liu, Han Lu, Shiyi Wang, Junqi Liu, Jiefeng Li, Cewu Lu CVPR 2020.