RESOURCE-EFFICIENT SIMULATION OF NOISY QUANTUM CIRCUITS AND APPLICATION TO NETWORK-ENABLED QRAM OPTIMIZATION

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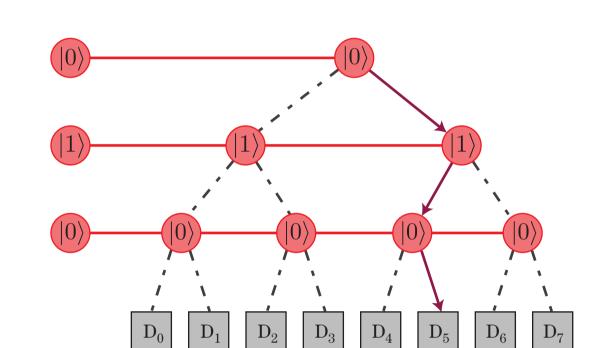
QRAM

A quantum random access memory (QRAM) [1] is an essential computational primitive for many quantum algorithms. The ability to perform a QRAM query in log(N) time steps, where N is the number of memory cells, implies polynomial speed-ups for applications such as quantum machine learning, matrix inversion, quantum imaging, and quantum searching. However, finding a suitable architecture that can be realized in the near-future remains an active research subject in the theoretical and experimental domains.

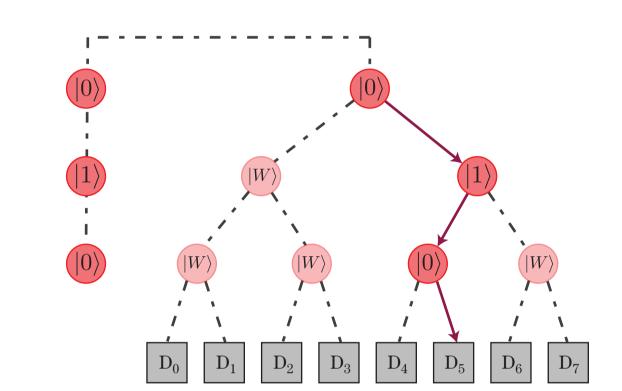
[DEF]:
$$|\psi_{\rm in}\rangle = |\psi_{\rm in}'\rangle |\emptyset\rangle_b \overset{\rm QRAM}{\longrightarrow} |\psi_{\rm out}\rangle = \sum_{j=1}^N \alpha_j |j\rangle_a |D_j\rangle_b$$

There are multiple architectures known in the literature to implement a QRAM, among them:

FANOUT MODEL:



BUCKET-BRIGADE MODEL:



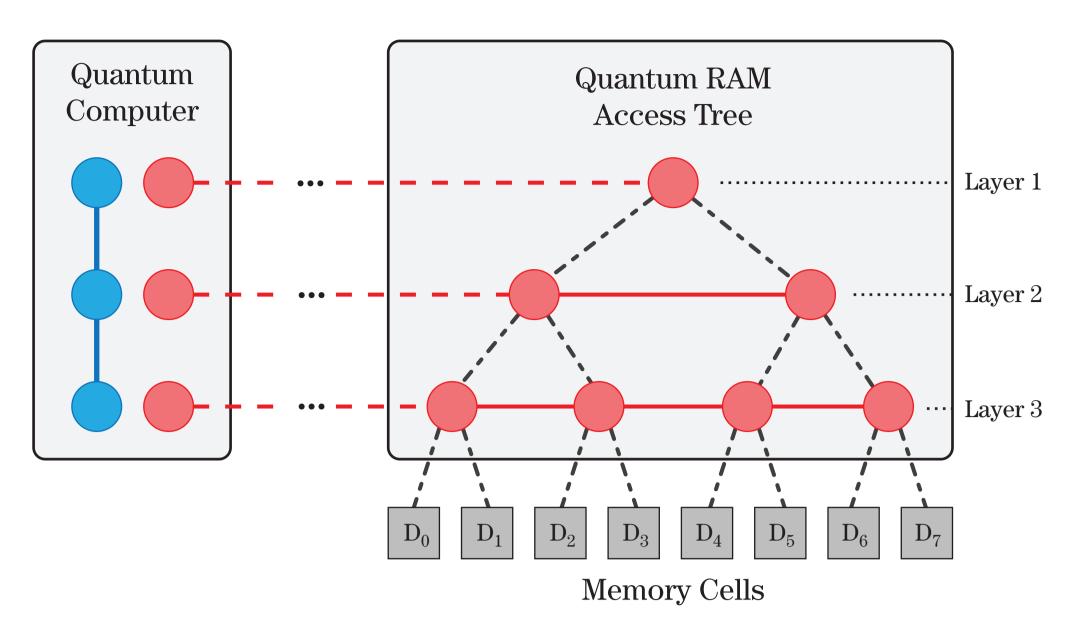
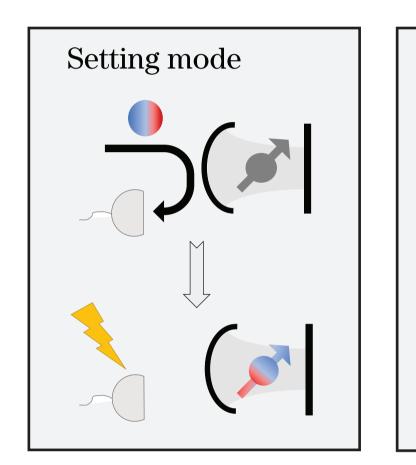


Figure 1. A quantum RAM in the form of a binary tree comprises GHZ states for each routing layer. The left-most node of each layer i is entangled with an ancillary qubit in a remote quantum computer, which hosts the query address qubits (blue). A Bell state measurement in the quantum computer then teleports the address state onto the access tree.



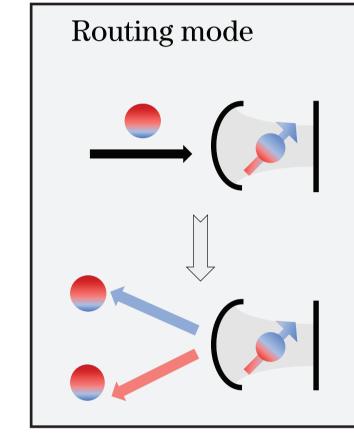
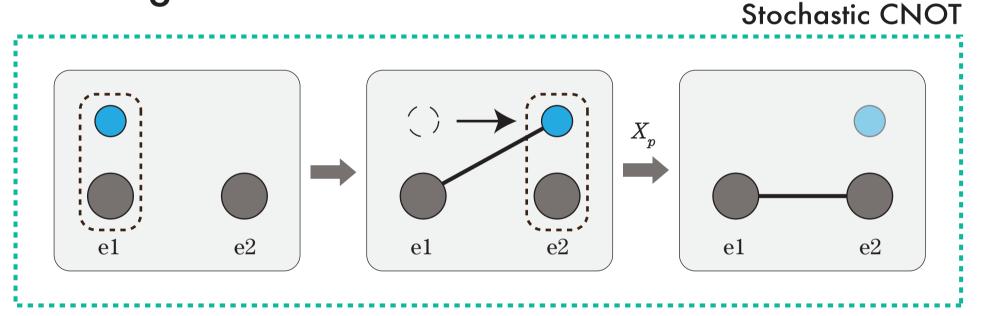
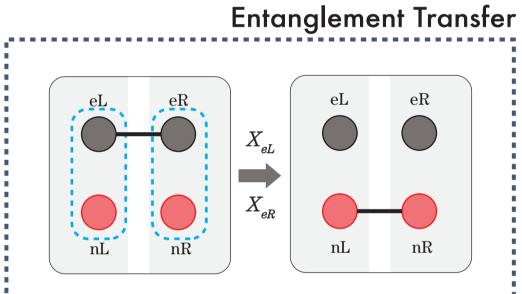


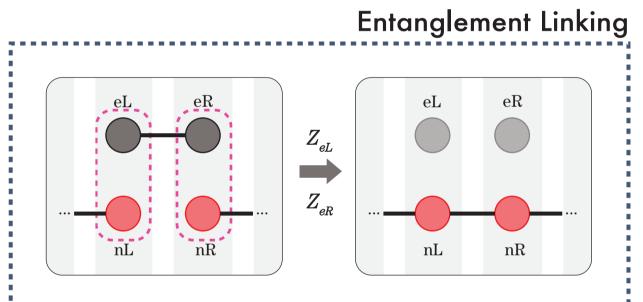
Figure 2. (a) detecting a cavity-reflected photon to herald successful mapping of a photonic address register qubit onto a stationary spin qubit (b) reflecting a subsequent photon to perform state-dependent quantum routing in a quantum switch.

Teleportation-based Protocols

Building Blocks:

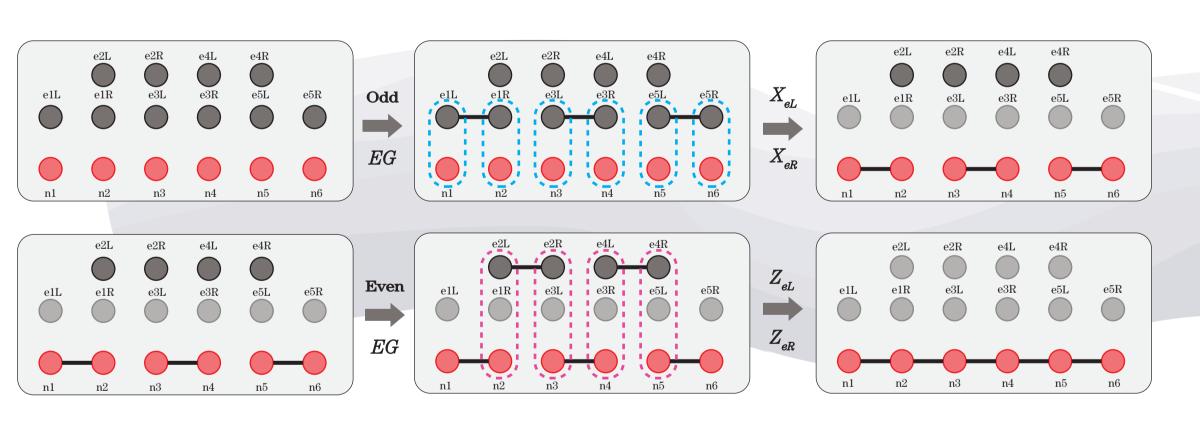


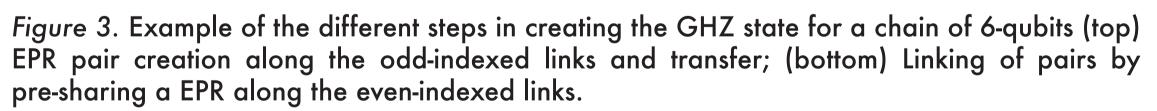




Noise Sources: Dephasing + Damping **CNOTs Errors** T1T2 Noise Model on memories Depolarising Noise on qubits Nuclear CNOT Electronic CNOT **Decoherence Free** Stochastic with efficiency n

Deterministic Protocol (TD-QRAM) [2]





Fidelity Scaling of TD-QRAM:

 $1 - 10^{-3}$ $\eta = 0.5$

 $1 - 10^{-2}$ $\eta = 0.8$

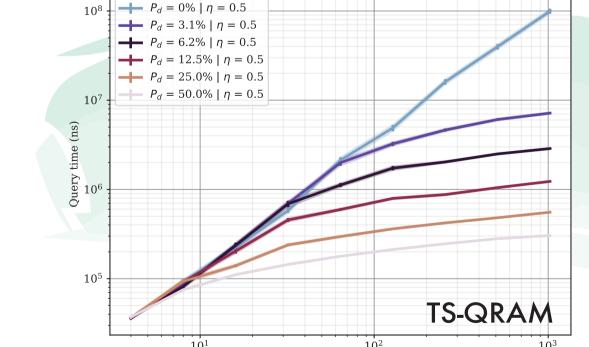
 $\eta = 0.7$

 $\eta = 0.9$

 $\eta = 0.5; T_2 = \infty$

$\eta = 0.7$ $\eta = 0.8$ TD-QRAM Number of Memory Qubits

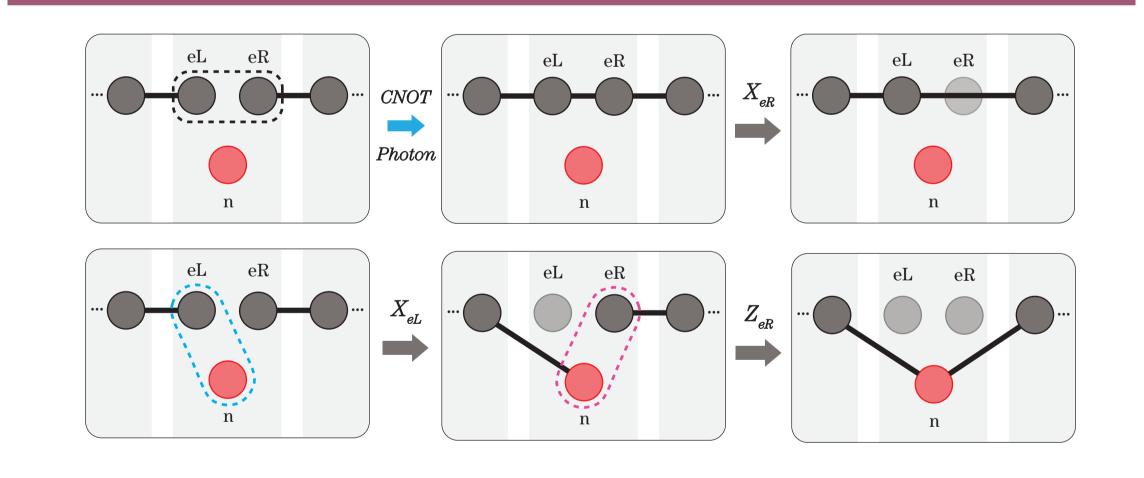
Query Time Scaling:



Number of Qubit

Deterministic CNOT errors rapidly render the QRAM access fidelity useless!

Stochastic Protocol (TS-QRAM)



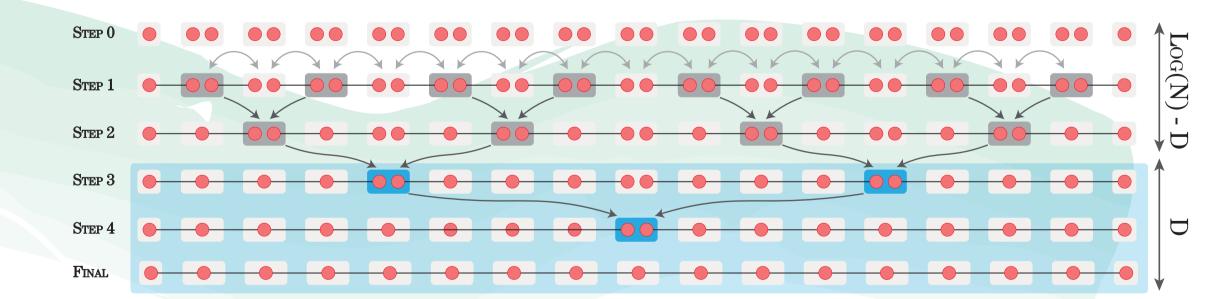


Figure 4. (top) Stochastic link of two different GHZ states; (middle) Deterministic link of two different GHZ states; (bottom) Order for the linking of pairs, where the last layers perform deterministic linking, instead of stochastic, to guarantee the correct query time scaling.

Simulation on NetSquid [3]

 $\epsilon_{CNOT} = 10^{-2}$

 $\epsilon_{CNOT} = 10^{-3}$

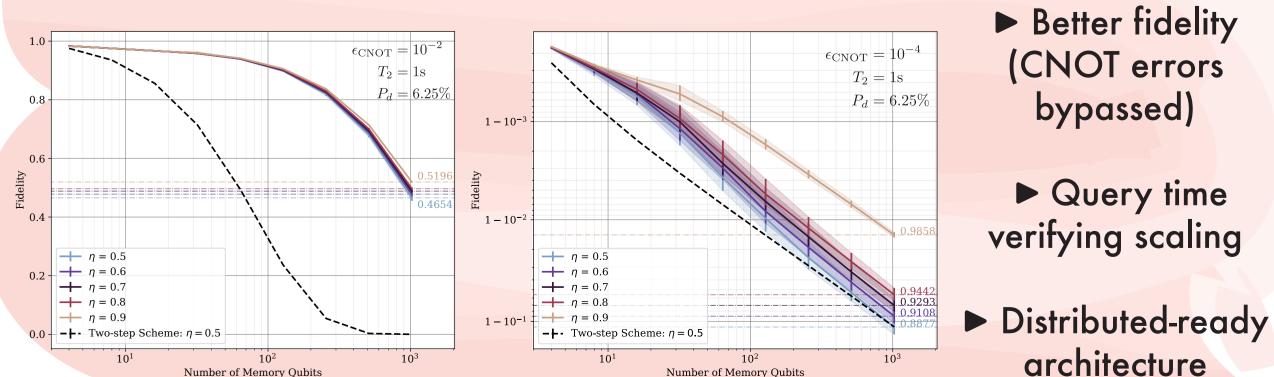
+ $\epsilon_{CNOT} = 10^{-4}$ $\epsilon_{CNOT} = 10^{-5}$

 $0.0 + \epsilon_{CNOT} = 0$

We have used NetSquid to simulate the protocol in a noiseless discrete-time event simulation, resorting to the stabilizer formalism in polynomial time. While doing so, we also retrieve all the information necessary to reconstitute all the noise from qubits spent decohering in memories, together with the information about the channels of decoherence that would have been applied in real noisy simulations, both for waiting times and gate errors. With this information at hand, we have access to the time-evolved state of the QRAM tree at all steps of the protocol. To reconstruct the density matrix, we find the analytical expressions for each of the building blocks. This is what allows to postpone the noise calculations to the end of the simulation, without losing the effects of the natural stochastic behavior of the protocol.

Number of Memory Qubits

Fidelity Scaling of TS-QRAM:



References:

Our work: arXiv:2210.13494

[1] V. Giovannetti, S. Lloyd, and L. Maccone, Physical Review Letters 100, 160501 (2008)

[2] K. C. Chen, W. Dai, C. Errando-Herranz, S. Lloyd, and D. Englund, PRX Quantum 2, 030319 (2021 [3] T. Coopmans, et al., Communication Physics 4, 164 (2021)

Acknowledgments:

▶ Better fidelity

(CNOT errors

bypassed)

► Query time

verifying scaling

architecture