1.2 Detail Register Description

1.2.1 obq0\_0\_baddr\_I (0x5000\_0000)

Table 2

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  | baddr |  |  |  |  |  |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 31:0 | R/W | obq0\_0\_baddr\_I (baddr) - obq0\_0\_fifo | 32'h00 |

1.2.2 obq0\_0\_baddr\_h (0x5000\_0004)

Table 3

|  |  |
| --- | --- |
|  | baddr |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 7:0 | R/W | obq0\_0\_baddr\_h (baddr) - obq0\_0\_fifo | 32'h00 |
| 31:8 | NA | Reserved | 0x0 |

1.2.3 obq1\_0\_baddr\_I (0x5000\_0008)

Table 4

|  |
| --- |
| baddr |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 31:0 | R/W | obq1\_0\_baddr\_I (baddr) – obq1\_0\_fifo | 32'h00 |

1.2.4 obq1\_0\_baddr\_h (0x5000\_000C)

Table 5

|  |  |
| --- | --- |
|  | baddr |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 7:0 | R/W | obq0\_0\_baddr\_h (baddr) – obq1\_0\_fifo | 8'h00 |
| 31:8 | NA | Reserved | 0x0 |

1.2.5 obq2\_0\_baddr\_I (0x5000\_0010)

Table 6

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  | baddr |  |  |  |  |  |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 31:0 | R/W | obq2\_0\_baddr\_I (baddr) - obq2\_0\_fifo | 32'h00 |

1.2.6 obq1\_0\_baddr\_h (0x5000\_0014)

Table 7

|  |  |
| --- | --- |
|  | baddr |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 7:0 | R/W | obq2\_0\_baddr\_h (baddr) – obq2\_0\_fifo | 8'h00 |
| 31:8 | NA | Reserved | 0x0 |

1.2.7 obq3\_0\_baddr\_I (0x5000\_0018)

Table 8

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  | baddr |  |  |  |  |  |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 31:0 | R/W | obq3\_0\_baddr\_I (baddr) - obq3\_0\_fifo | 32'h00 |

1.2.8 obq3\_0\_baddr\_h (0x5000\_001C)

Table 9

|  |  |
| --- | --- |
|  | baddr |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 7:0 | R/W | obq3\_0\_baddr\_h (baddr) – obq3\_0\_fifo | 8'h00 |
| 31:8 | NA | Reserved | 0x0 |

1.2.9 obq4\_0\_baddr\_I (0x5000\_0020)

Table 10

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  |  |  |  | baddr |  |  |  |  |  |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 31:0 | R/W | obq4\_0\_baddr\_I (baddr) - obq4\_0\_fifo | 32'h00 |

1.2.8 obq4\_0\_baddr\_h (0x5000\_0024)

Table 11

|  |  |
| --- | --- |
|  | baddr |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 7:0 | R/W | obq4\_0\_baddr\_h (baddr) – obq4\_0\_fifo | 8'h00 |
| 31:8 | NA | Reserved | 0x0 |

1.2.536 obq9\_1\_status (0x5000\_0DAC)

Table 537

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
|  | alem\_th | alful\_th |  | alempty | alfull | empty | full |

|  |  |  |  |
| --- | --- | --- | --- |
| Bits | RW | Description | Default |
| 0 | RO | obq9\_1\_full (full) | 1'b0 |
| 1 | RO | obq9\_1\_empty (empty) | 1'b0 |
| 2 | RO | obq9\_1\_alfull (alfull) | 1'b0 |
| 3 | RO | obq9\_1\_alempty (alempty) | 1'b0 |
| 7:4 | NA | Reserved rsvd\_0 | 0x0 |
| 15:8 | R/W | obq9\_1\_alful\_th (alful\_th) – obq9\_1\_fifo | 8’h6 |
| 23:16 | R/W | obq9\_1\_alem\_th (alem\_th) – obq9\_1\_fifo | 8’h6 |
| 31:24 | NA | Reserved | 0x0 |