1.1 Detail Register Description

1.1.1 obq0\_0\_baddr (0x5E00\_0000)

Table 1

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **baddr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_0\_baddr (baddr) -  **obq0\_0 fifo 起始基地址32位** | **32'h00** |

1.1.2 obq0\_0\_depth (0x5E00\_0004)

Table 2

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **depth** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_0\_depth (depth) -  **obq0\_0 fifo的深度** | **16'h40** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.3 obq0\_0\_tailer (0x5E00\_0008)

Table 3

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **tailer** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_0\_tailer (tailer) -  **obq0\_0的写指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.4 obq0\_0\_hed\_addr (0x5E00\_000C)

Table 4

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **addr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_0\_hed\_addr (addr) -  **obq0\_0 fifo header指针地址32位** | **32'h00** |

1.1.5 obq0\_0\_header (0x5E00\_0010)

Table 5

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **header** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **RO** | obq0\_0\_header (header) -  **obq0\_0的读指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.6 obq0\_0\_status (0x5E00\_0014)

obq0\_0 fifo的空满状态

Table 6

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | | 2 | | 1 | | 0 | |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | | | | | | | | | | | | | | **alempty** | | **alfull** | | **empty** | | **full** |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **0** | **RO** | obq0\_0\_full (full) - | **1'b0** |
| **1** | **RO** | obq0\_0\_empty (empty) - | **1'b0** |
| **2** | **RO** | obq0\_0\_alfull (alfull) - | **1'b0** |
| **3** | **RO** | obq0\_0\_alempty (alempty) - | **1'b0** |
| **31:4** | **NA** | **Reserved** | **0x0** |

1.1.7 obq0\_0\_cnt (0x5E00\_0018)

Table 7

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **num** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_0\_cnt (num) -  **obq0\_0fifo内部还剩多少个entry没取** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.8 obq0\_0\_th (0x5E00\_001C)

Table 8

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **alem\_th** | | | | | | | | **alful\_th** | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **7:0** | **R/W** | obq0\_0\_alful\_th (alful\_th) -  **obq0\_0 fifo 将满阈值** | **8'h6** |
| **15:8** | **R/W** | obq0\_0\_alem\_th (alem\_th) -  **obq0\_0 fifo 将空阈值** | **8'h6** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.9 obq0\_1\_baddr (0x5E00\_0020)

Table 9

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **baddr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_1\_baddr (baddr) -  **obq0\_1 fifo 起始基地址32位** | **32'h00** |

1.1.10 obq0\_1\_depth (0x5E00\_0024)

Table 10

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **depth** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_1\_depth (depth) -  **obq0\_1 fifo的深度** | **16'h40** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.11 obq0\_1\_tailer (0x5E00\_0028)

Table 11

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **tailer** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_1\_tailer (tailer) -  **obq0\_1的写指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.12 obq0\_1\_hed\_addr (0x5E00\_002C)

Table 12

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **addr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_1\_hed\_addr (addr) -  **obq0\_1 fifo header指针地址32位** | **32'h00** |

1.1.13 obq0\_1\_header (0x5E00\_0030)

Table 13

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **header** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **RO** | obq0\_1\_header (header) -  **obq0\_1的读指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1 Detail Register Description

1.1.1 obq0\_0\_baddr (0x5E00\_0000)

Table 1

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **baddr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_0\_baddr (baddr) -  **obq0\_0 fifo 起始基地址32位** | **32'h00** |

1.1.2 obq0\_0\_depth (0x5E00\_0004)

Table 2

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **depth** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_0\_depth (depth) -  **obq0\_0 fifo的深度** | **16'h40** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.3 obq0\_0\_tailer (0x5E00\_0008)

Table 3

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **tailer** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_0\_tailer (tailer) -  **obq0\_0的写指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.4 obq0\_0\_hed\_addr (0x5E00\_000C)

Table 4

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **addr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_0\_hed\_addr (addr) -  **obq0\_0 fifo header指针地址32位** | **32'h00** |

1.1.5 obq0\_0\_header (0x5E00\_0010)

Table 5

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **header** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **RO** | obq0\_0\_header (header) -  **obq0\_0的读指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.6 obq0\_0\_status (0x5E00\_0014)

obq0\_0 fifo的空满状态

Table 6

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | | 2 | | 1 | | 0 | |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | | | | | | | | | | | | | | **alempty** | | **alfull** | | **empty** | | **full** |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **0** | **RO** | obq0\_0\_full (full) - | **1'b0** |
| **1** | **RO** | obq0\_0\_empty (empty) - | **1'b0** |
| **2** | **RO** | obq0\_0\_alfull (alfull) - | **1'b0** |
| **3** | **RO** | obq0\_0\_alempty (alempty) - | **1'b0** |
| **31:4** | **NA** | **Reserved** | **0x0** |

1.1.7 obq0\_0\_cnt (0x5E00\_0018)

Table 7

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **num** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_0\_cnt (num) -  **obq0\_0fifo内部还剩多少个entry没取** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.8 obq0\_0\_th (0x5E00\_001C)

Table 8

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **alem\_th** | | | | | | | | **alful\_th** | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **7:0** | **R/W** | obq0\_0\_alful\_th (alful\_th) -  **obq0\_0 fifo 将满阈值** | **8'h6** |
| **15:8** | **R/W** | obq0\_0\_alem\_th (alem\_th) -  **obq0\_0 fifo 将空阈值** | **8'h6** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.9 obq0\_1\_baddr (0x5E00\_0020)

Table 9

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **baddr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_1\_baddr (baddr) -  **obq0\_1 fifo 起始基地址32位** | **32'h00** |

1.1.10 obq0\_1\_depth (0x5E00\_0024)

Table 10

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **depth** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_1\_depth (depth) -  **obq0\_1 fifo的深度** | **16'h40** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.11 obq0\_1\_tailer (0x5E00\_0028)

Table 11

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **tailer** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **R/W** | obq0\_1\_tailer (tailer) -  **obq0\_1的写指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |

1.1.12 obq0\_1\_hed\_addr (0x5E00\_002C)

Table 12

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **addr** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **31:0** | **R/W** | obq0\_1\_hed\_addr (addr) -  **obq0\_1 fifo header指针地址32位** | **32'h00** |

1.1.13 obq0\_1\_header (0x5E00\_0030)

Table 13

| 31 | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 | 16 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | | | | | | | | | | | | | | | | **header** | | | | | | | | | | | | | | | |

| **Bits** | **RW** | **Description** | **Default** |
| --- | --- | --- | --- |
| **15:0** | **RO** | obq0\_1\_header (header) -  **obq0\_1的读指针** | **16'h0** |
| **31:16** | **NA** | **Reserved** | **0x0** |