

		0	1	2	3	4	5	6	7	8	9	10	11
SR LD		0	0	0	0	0	0	0	0	1	1	0	0
RF LD		1	0	1	0	0	1	1	1	0	1	0	0
IR LD		0	0	0	1	0	0	0	0	0	0	0	0
MAR LD		0	1	0	0	0	0	0	0	0	0	1	0
MDR LD		0	0	0	0	0	0	0	0	0	0	0	0
R/W		0	0	1	1	0	0	0	0	0	0	0	1
MOV		0	0	1	1	0	0	0	0	0	0	0	1
DT 1	x	x		1	1	x	x	x	x	x	x		0
DT 0	x	x		0	0	x	x	x	x	x	x		0
MA 1		0	1	1	0	0	0	0	0	0	0	0	0
MA 0		0	0	0	0	0	0	0	0	0	0	0	0
MB 1		1	0	0	0	0	0	0	0	0	0	0	0
MB 0		1	0	0	0	0	0	1	1	0	0	1	0
MC1		0	0	0	0	0	0	0	0	0	0	0	0
MC0		1	0	1	0	0	0	0	0	0	0	0	0
MD		1	1	1	0	0	0	0	0	0	0	1	0
ME		0	0	0	0	0	0	0	0	0	0	0	0
OP4		0	1	1	0	0	0	0	0	0	0	0	0
OP3		1	0	0	0	0	0	0	0	0	0	0	0
OP2		1	0	0	0	0	0	0	0	0	0	1	0
OP1		0	0	0	0	0	0	0	0	0	0	0	0
OP0		1	0	1	0	0	0	0	0	0	0	0	0
N2		0	0	0	1	1	0	0	0	0	0	0	0
N1		1	1	1	0	0	0	0	0	0	0	1	1
N0		1	1	1	1	0	1	1	1	1	1	1	1
Inv	x	x	x		1	0	x	x	x	x	x	x	
S1	x	x	x		0	0	x	x	x	x	x	x	
S0	x	x	x		0	1	x	x	x	x	x	x	
CR(decimal)	x	x	x		3	1	x	x	x	x	x	x	
	Reset State	Load PC into Me	Increment PC	Wait for MOC	Check Condition	Add/Sub(R-R)	Add/Sub(Shift)	Add/Sub(Immediate)	CMP (R-R)	MOV(R-R)	Calc. & Load Ad	Begin Byte Read	
						The operation performed depends on the Inst. Register						Load Byte w/ or w/out i	

12	13	14	15	16	17	18	19	20	21	22	23	24	25
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0 x		x		1	1 x	x		0	0 x	x		1	1 x
0 x		x		0	0 x	x		0	0 x	x		0	0 x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	1	1	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	1	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x		x	x		1 x	x	x		1 x	x	x		1 x
0 x		x	x		0 x	x	x		0 x	x	x		0 x
0 x		x	x		0 x	x	x		0 x	x	x		0 x
12 x		x	x		16 x	x	x		20 x	x	x		24 x
Wait for MOC	Load Byte into T	Calc. & Load Ad	Begin Word Rea	Wait for MOC	Load Word into T	Calc. & Load Ad	Begin Byte Read	Wait for MOC	Load Byte into T	Calc. & Load Ad	Begin Word Rea	Wait for MOC	Load Word into T
immediate offset added	Load Word w/ or w/out immediate offset added					Load Byte w/ Immediate Offset Subtracted					Load Word w/ immediate offset Subtracted		

26	27	28	29	30	31	32	33	34	35	36	37	38	39
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	1	1	0	0	1	1	0	0	1	1	0	0	1
x	0	0	x	x	0	0	x	x	1	1	x	x	1
x	0	0	x	x	0	0	x	x	0	0	x	x	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	1	0	0	0	1	1	0	0	1	0	0
0	0	0	0	1	0	0	0	0	0	0	0	1	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	1	0	0	1	1	0	0	1	1	0	0	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	1	x	x	x	1	x	x	x	1	x	x	
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	28	x	x	x	32	x	x	x	36	x	x	
Target Register													
Load Byte Shift By Register adding				Load Byte Shift By Register Subtracting				Load Word Shift By Register adding				Load Word Shift By I	

40	41	42	43	44	45	46	47	48	49	50	51	52	53
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
1 x	x		0	0 x	x		1	1 x	x		0	0 x	
0 x	x		0	0 x	x		0	0 x	x		0	0 x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	1	1	0	0	1	1	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x	x	x		1 x	x	x		1 x	x	x		1 x	
0 x	x	x		0 x	x	x		0 x	x	x		0 x	
0 x	x	x		0 x	x	x		0 x	x	x		0 x	
40 x	x	x		44 x	x	x		48 x	x	x		52 x	
		Calc. & Load Address	Begin Byte Read	Wait for MOC	Load Byte into Target Register	Calc. & Load Address	Begin Word Read	Wait for MOC	Load Word into Target Register	Calc. & Load Address	Begin Byte Read	Wait for MOC	Load Byte into Target Register
Register Subtracting		Load Byte w/ immediate offset added pre-indexed				Load Word w/ immediate offset added pre-indexed				Load Byte w/ Immediate Offset Subtracted pre-indexed			

	54	55	56	57	58	59	60	61	62	63	64	65	66	67
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	0	0	0	1	1	0	0	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	0	1	0	0	0	0
	0	0	1	0	0	0	1	0	0	0	0	1	0	0
	0	1	1	0	0	1	1	0	0	0	1	1	0	0
	0	1	1	0	0	1	1	0	0	0	1	1	0	0
x		1	1	x		0	0	x		x		0	0	x
x		0	0	x		0	0	x		x		0	0	x
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	0	1	0
	1	0	0	0	1	0	0	0	1	1	0	0	0	1
	1	0	0	0	0	0	0	0	1	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	1	0	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	1	0	0	0	0	1	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	0	1	0
	0	0	0	1	0	0	0	1	1	0	0	0	1	0
	1	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	1	0	0	0	1	0	0	0	0	1	0
	0	0	1	0	0	0	1	0	0	0	0	1	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	0	0	1	1	0	1	0	1	1	0	1	0
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x		1	x		x	1	x		x		1	x	
x	x		0	x		x	0	x		x		0	x	
x	x		0	x		x	0	x		x		0	x	
x	x		56	x		x	60	x		x		65	x	
Calc. & Load Add	Begin Word Read	Wait for MOC	Load Word into Target Register											
Load Word w/ immediate offset Subtracted pre-indexed				Load byte and Add immediate offset post-indexed						Load byte and subtract immediate offset post-indexed				

[illegible]

82	83	84	85	86	87	88	89	90	91	92	93	94	95
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	1	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	1	1	0	0	1	1	0	0	1	1	0	0	1
x	1	1	x	x	0	0	x	x	1	1	x	x	0
x	0	0	x	x	0	0	x	x	0	0	x	x	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	1	0	0	1	1	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	1	0
1	0	0	1	0	0	0	0	1	0	0	0	1	0
0	0	0	0	0	1	0	0	0	1	0	0	0	0
0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	1	0	0	1	1	0	0	1	1	0	0	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	1	x	x	x	1	x	x	x	1	x	x	x
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	84	x	x	x	88	x	x	x	92	x	x	x
Calc. & Load Address Begin Word Read Wait for MOC				Load Word into Target Register				Calc. & Load Address Begin Byte Read Wait for MOC				Load Byte into Target Register	
Load Word w/ register offset added pre-indexed				Load Byte w/ Register Offset Subtracted pre-indexed				Load Word w/ register offset Subtracted pre-indexed				Load byte and /	

96	97	98	99	100	101	102	103	104	105	106	107	108	109
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	1	1	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
1	0	0	0	0	1	0	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	0	1	1	0	0	0
1	0	0	0	1	1	0	0	0	1	1	0	0	0
0 x	x	x		0	0 x	x	x		1	1 x	x	x	
0 x	x	x		0	0 x	x	x		0	0 x	x	x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	1	1	1	0	0	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	1	1	0	0	0	1	0	0	0	0	1	1	0
0	0	0	0	0	0	0	1	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	1	1	0	1	0	1	1	0	1	0	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x	x	x	x		1 x	x	x	x		1 x	x	x	
0 x	x	x	x		0 x	x	x	x		0 x	x	x	
0 x	x	x	x		0 x	x	x	x		0 x	x	x	
96 x	x	x	x		101 x	x	x	x		106 x	x	x	

Add register offset post-indexed

Load byte and subtract register offset post-indexed

Load Word and add register offset post-indexed

Lc

110	111	112	113	114	115	116	117	118	119	120	121	122	123
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	1	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	0	1	1	0	0	1	1	0	0
1	1 x	x		0	0	0	0	1	1	1	1	0	0
0	0 x	x		0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	1	1	0	0	1	1	0	0	1	1
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	1	0	0	0	1	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	0	1	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	1	0	0	0	1	0	0
1	0	1	0	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	1 x	x	x	x	x		0 x	x	x		0 x	x	
x	0 x	x	x	x	x		0 x	x	x		0 x	x	
x	0 x	x	x	x	x		0 x	x	x		0 x	x	
x	111 x	x	x	x	x		117 x	x	x		121 x	x	

Load Word and subtract register offset post-indexed

Store byte w/ or w/out immediate offset added

Store word w/ or w/out immediate offset added

Store byte w/ immedi

124	125	126	127	128	129	130	131	132	133	134	135	136	137
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	1	1	0	0	1	1
0	0	1	1	1	1	0	0	0	0	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	0	x	x	x	0	x	x	x	0	x	x	x	0
x	0	x	x	x	0	x	x	x	0	x	x	x	0
x	0	x	x	x	0	x	x	x	0	x	x	x	0
x	125	x	x	x	129	x	x	x	133	x	x	x	137
iate offset subtracted		Store word w/ immediate offset subtracted				Store byte w/ register offset added				Store word w/ register offset added			

138	139	140	141	142	143	144	145	146	147	148	149	150	151
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	0	1	1	1	1	0	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	1	1	0	0	1	1
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	1	0
1	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	x	0	x	x	x	0	x	x	x	0	x	x
x	x	x	0	x	x	x	0	x	x	x	0	x	x
x	x	x	0	x	x	x	0	x	x	x	0	x	x
x	x	x	141	x	x	x	145	x	x	x	149	x	x
Store byte w/ register offset subtracted				Store word w/ register offset subtracted				Store byte w/ immediate offset added pre-indexed				Store word w/ immediate	

152	153	154	155	156	157	158	159	160	161	162	163	164	165
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	1	1	0	0	1	1
1	1	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	1	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	1	1	1	1	1	1	1	1	1	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	0	x	x	x	0	x	x	x	0	x	x	x	1
x	0	x	x	x	0	x	x	x	0	x	x	x	0
x	0	x	x	x	0	x	x	x	0	x	x	x	0
x	153	x	x	x	157	x	x	x	161	x	x	x	165
offset added pre-indexed		Store byte w/ immediate offset subtracted pre-indexed				Store word w/ immediate offset subtracted pre-indexed				Store byte w/ immediate offset added post-indexed			

166	167	168	169	170	171	172	173	174	175	176	177	178	179
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	1	0	0	0	1	1	0	0	0	1
0	1	1	1	1	1	0	0	0	0	0	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
1	0	0	0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	1	0	0	1	1	1	0	0	1	1	1	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	1	1	0	0	0	1	1	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	1	1	1	0	0	1	1	1	0	0	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	x	x	1	x	x	x	1	x	x	x	x	
x	x	x	x	0	x	x	x	0	x	x	x	x	
x	x	x	x	0	x	x	x	0	x	x	x	x	
x	x	x	x	170	x	x	x	175	x	x	x	x	
	Store word w/ immediate offset added post-indexed					Store byte w/ immediate offset subtracted post-indexed					Store word w/ immediate offset subtr		

180	181	182	183	184	185	186	187	188	189	190	191	192	193
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	1	1	0	0	1	1
1	1	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	1	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x	x	x	x		0 x	x	x		0 x	x	x		0
0 x	x	x	x		0 x	x	x		0 x	x	x		0
0 x	x	x	x		0 x	x	x		0 x	x	x		0
180 x	x	x	x		185 x	x	x		189 x	x	x		193
racted post-indexed		Store byte w/ register offset added pre-indexed				Store word w/ register offset added pre-indexed				Store byte w/ register offset subtracted pre-indexed			

194	195	196	197	198	199	200	201	202	203	204	205	206	207
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	1	0	0	0	0
0	1	0	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	0	0	0	1	1	0
1	1	1	1	0	0	0	0	0	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	1	1	1	0	0	1
0	1	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	0	1	1	0	0	0	1	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	1	0	0	0	0	1
1	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	0	1	0
1	1	1	1	1	1	1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	x	0	x	x	x	1	x	x	x	x	1	x
x	x	x	0	x	x	x	0	x	x	x	x	0	x
x	x	x	0	x	x	x	0	x	x	x	x	0	x
x	x	x	197	x	x	x	201	x	x	x	x	206	x
Store word w/ register offset subtracted pre-indexed				Store byte w/ register offset added post-indexed				Store word w/ register offset added post-indexed					

208	209	210	211	212	213	214	215	216	217	218	219	220	221
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	0	1	0	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	0	0	0	1	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	0	1	1	0	0	0	1	1	0	0	1	1	0
0	0	0	0	0	1	1	1	1	1	x	0	0	x
0	0	0	0	0	0	0	0	0	0	x	0	0	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	1	0	0	1	1	0	0	1
0	1	0	0	0	0	1	0	0	0	0	0	0	0
1	1	0	0	0	1	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	0	1	0	0	0	0	1	1	0	0	0
0	0	0	0	0	0	0	0	0	0	1	0	0	1
0	0	0	1	0	0	0	0	1	0	0	0	1	0
1	1	1	0	0	1	1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	0	1
x	x	x	1	x	x	x	1	x		x	x	1	x
x	x	x	0	x	x	x	0	x		x	x	0	x
x	x	x	0	x	x	x	0	x		x	x	0	x
x	x	x	211	x	x	x	216	x		x	x	220	x
Store byte w/ register offset subtracted post-indexed					Store word w/ register offset subtracted post-indexed					Load Imm offset signed byte added			

222	223	224	225	226	227	228	229	230	231	232	233	234	235
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	1	1	0	0	1	1	0	0	1	1	0	0	1
x	0	0	x	x	0	0	x	x	0	0	x	x	1
x	0	0	x	x	1	1	x	x	1	1	x	x	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	1	1	0	0	1	0	0	0	1	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	1	1	0	1	1	1	0	1	1	1	0	1	1
1	1	0	1	1	1	0	1	1	1	0	1	1	1
x	x	1	x	x	x	1	x	x	x	1	x	x	x
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	224	x	x	x	228	x	x	x	232	x	x	x
												Calc.	read
Load Imm offset signed byte substract				Load Imm offset signed halfword add				Load Imm offset signed halfword substract					

236	237	238	239	240	241	242	243	244	245	246	247	248	249
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	0	0	0	1	0	0	0
1	0	1	0	0	0	1	0	1	0	0	1	0	0
1	0	1	0	0	1	1	0	1	0	0	0	0	0
1	0	1	0	0	1	1	0	1	0	0	0	1	1
1 x		1 x	x		1	1 x		1 x		0	0	0	0
1 x		1 x	x		1	1 x		1 x		1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	1	1	0	0	1	0	1	1	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	1	0	0	1	0	1	1	0	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	1	0	0	0	1	0	0	0
0	1	0	1	0	0	0	1	0	1	1	0	0	0
1	0	1	0	0	0	0	1	0	1	0	0	0	1
1	1	1	0	1	1	1	1	1	0	1	1	1	1
0	1	0	1	1	1	0	1	0	1	1	1	1	1
1 x		1 x		x x		1 x		1 x		x	x	x	0
0 x		0 x		x x		0 x		0 x		x	x	x	0
0 x		0 x		x x		0 x		0 x		x	x	x	0
236 x		238 x		x x		242 x		244 x		x	x	x	249
wair for MOC	Load into register		Calc.	read	wair for MOC	Load into register							
Load Imm offset doubleword add				Load Imm offset doubleword substract						Store imm offset signed halfword add			

250	251	252	253	254	255	256	257	258	259	260	261	262	263
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	1	1	0	0	1	1
0	0	0	0	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	0	0	1	1	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	1	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	1	1	1	1	1	1	0	1	1	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	x	0	x	x	x	1	x	0	x	x	x	1
x	x	x	0	x	x	x	0	x	0	x	x	x	0
x	x	x	0	x	x	x	0	x	0	x	x	x	0
x	x	x	253	x	x	x	257	x	259	x	x	x	263
Store imm offset signed halfword substract				Store imm offset s doubleword add						Store imm offset doubleword subs			

264	265	266	267	268	269	270	271	272	273	274	275	276	277
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	1	1	0	0	1	1	0	0	1	1	0
1	1	0	1	1	0	0	1	1	0	0	1	1	0
1	1	x	0	0	x	x	0	0	x	x	0	0	x
1	1	x	0	0	x	x	0	0	x	x	1	1	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	1	0	0	0	1	1	0	0	1
0	1	0	0	1	0	0	0	1	0	0	0	1	0
1	1	1	1	1	1	0	1	1	1	0	1	1	0
1	1	1	1	1	0	1	1	1	0	1	1	1	0
x	0	x	x	1	x	x	x	1	x	x	x	1	x
x	0	x	x	0	x	x	x	0	x	x	x	0	x
x	0	x	x	0	x	x	x	0	x	x	x	0	x
x	265	x	x	268	x	x	x	272	x	x	x	276	x
		Load signed Byte w/ immediate offset added pre-indexed				Load signed Byte w/ immediate offset substract pre-indexed				Load signed halfword w/ immediate offset added pre-indexed			

278	279	280	281	282	283	284	285	286	287	288	289	290	291
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	1	0	0	0	1	0
0	1	1	0	0	1	1	0	1	0	0	1	1	0
0	1	1	0	0	1	1	0	1	0	0	1	1	0
x	0	0	x	x	1	1	1	1	1	x	1	1	1
x	1	1	x	x	1	1	1	1	1	x	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	0	0	0	0	1	0	0	0	0	1	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	0	0	0	1	1	0	0	0	0	1	0	0	0
0	0	0	1	1	1	0	1	0	1	0	0	0	1
0	0	1	0	0	0	1	0	1	0	0	0	1	0
1	1	1	0	1	1	1	0	1	0	1	1	1	1
1	1	0	1	1	1	0	1	0	1	1	1	0	1
x	x	1	x	x	x	1	x	1	x	x	x	1	x
x	x	0	x	x	x	0	x	0	x	x	x	0	x
x	x	0	x	x	x	0	x	0	x	x	x	0	x
x	x	280	x	x	x	284	x	286	x	x	x	290	x
										Calc.	Read	Wait for MOC	Load Byte into R
Load signed halfword w/ immediate offset substract pre-indexed				Load doubleword w/ immediate offset added pre-indexed						Load doubleword w/ immediate offset substract pre-indexed			

292	293	294	295	296	297	298	299	300	301	302	303	304	305
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	1	1	0	0	1	1
1	1	0	0	0	0	0	0	0	0	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	0	0	0	0	0	1	0	0	0
1	0	0	0	0	1	0	0	0	1	0	0	0	1
1	0	1	1	1	1	1	1	1	1	1	1	1	0
0	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x		x	x	x	0	x	x	x	0	x	x	x	1
0 x		x	x	x	0	x	x	x	0	x	x	x	0
0 x		x	x	x	0	x	x	x	0	x	x	x	0
292 x		x	x	x	297	x	x	x	301	x	x	x	305
register													
		Store signed halfword w/ immediate offset added pre-indexed				Store signed halfword w/ immediate offset subtract pre-indexed				Store doubleword w/ immediate offset added			

306	307	308	309	310	311	312	313	314	315	316	317	318	319
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	1	0	0	0	0	1
0	0	0	1	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	1	1	0	0	0
1	1	0	0	1	1	1	1	0	1	1	0	0	0
1	1	1	1	1	1	1	1	x	0	0	x	x	
1	1	1	1	1	1	1	1	x	0	0	x	x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0	1	0	0	1	1	1
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	1	0	0	0	1	0	1	0	0	1	0	0	0
1	1	1	1	1	1	0	1	1	1	1	1	0	1
1	1	1	1	1	1	1	1	1	1	0	1	1	1
x	0	x	x	x	1	x	0	x	x	1	x	x	x
x	0	x	x	x	0	x	0	x	x	0	x	x	x
x	0	x	x	x	0	x	0	x	x	0	x	x	x
x	307	x	x	x	311	x	313	x	x	316	x	x	x

pre-indexed

Store doubleword w/ immediate offset subtracted pre-indexed

Load signed byte and Add immediate offset post-indexed

Load si

320	321	322	323	324	325	326	327	328	329	330	331	332	333
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
0	0	x	x	x	0	0	x	x	x	0	0	x	x
0	0	x	x	x	1	1	x	x	x	1	1	x	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	1	1	1	0	0	1	1	0	0	0	1
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	1	0	0	1	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	1	0	0	0	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	1	0	0	0	0	1	1	0	0	0	1	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	1	1	0	1	1	1	1	0	1	1	1	0
1	0	1	1	1	1	1	0	1	1	1	1	0	1
x	1	x	x	x	x	1	x	x	x	x	x	1	x
x	0	x	x	x	x	0	x	x	x	x	x	0	x
x	0	x	x	x	x	0	x	x	x	x	x	0	x
x	321	x	x	x	x	326	x	x	x	x	331	x	x

gned byte and substract immediate offset post-indexed

Load signed halfword and Add immediate offset post-indexed

Load signed halfword and substract immediate offset post-indexed

334	335	336	337	338	339	340	341	342	343	344	345	346	347
0	0	0	1	0	1	0	0	0	0	1	0	1	0
0	0	0	1	0	1	1	0	0	0	1	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0	0	0	0
0	0	1	0	1	0	0	0	0	1	0	1	0	0
0	1	1	0	1	0	0	0	1	1	0	1	0	0
0	1	1	0	1	0	0	0	1	1	0	1	0	0
x	1	1	1	1	1	1 x	x	1	1 x	1 x	1 x	x	
x	1	1	1	1	1	1 x	x	1	1 x	1 x	1 x	x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	1	0	0	0	0	1	0	1	0
1	0	0	0	0	0	0	1	1	0	0	0	0	1
0	0	0	0	0	0	0	1	0	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	1	1	1	0	0	1	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0	0	0	0
0	0	0	1	0	1	1	0	0	0	1	0	1	1
0	0	0	1	0	1	0	0	0	0	1	0	1	0
0	0	0	0	0	0	1	0	0	0	0	0	0	1
0	0	0	1	0	1	1	0	0	0	1	0	1	0
0	0	0	1	0	1	0	0	0	1	0	1	0	0
1	1	1	1	1	1	1	0	1	1	1	1	1	0
1	1	0	1	0	1	1	1	1	1	1	0	1	1
x	x	1	x	1	x	x	x	x	1	x	1	x	x
x	x	0	x	0	x	x	x	x	0	x	0	x	x
x	x	0	x	0	x	x	x	x	0	x	0	x	x
x	x	336	x	338	x	x	x	x	343	x	345	x	x
Load doubleword and Add immediate offset post-indexed							Load doubleword and subtract immediate offset post-indexed						

348	349	350	351	352	353	354	355	356	357	358	359	360	361
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	0	1	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	1	0	0	1	1	1	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
1	1	1	0	0	1	1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	x	1	x	x	x	x	1	x	x	x	x	1
x	x	x	0	x	x	x	x	0	x	x	x	x	x
x	x	x	0	x	x	x	x	0	x	x	x	x	x
x	x	x	351	x	x	x	x	356	x	x	x	x	361
Store signed halfword w/ immediate offset added post-indexed					Store signed halfword w/ immediate offset subtracted post-indexed					Store doubleword w/ immediate offset			

362	363	364	365	366	367	368	369	370	371	372	373	374	375
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	1	0	0	0	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	1	0	0	0
1	0	0	0	1	0	1	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	1	0	0	0	0	1	0	1	1	0	1	1	0
1	1	1	1	1	1	1	1	1	1	1 x	0	0 x	
1	1	1	1	1	1	1	1	1	1	1 x	0	0 x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	1	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	1	1	0	0	0	0	0	1	1	0	0	0
0	0	1	0	0	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	1	1	1	0	1	0	0	1	1	0	0	1
1	0	0	0	0	1	0	1	0	0	0	0	0	0
1	0	0	1	1	1	0	1	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	1	0	0	0	0	0	0	1	1	0	0	0
0	0	1	0	0	0	0	0	0	0	1	0	0	1
0	1	0	0	0	0	1	0	1	0	0	0	1	0
1	0	0	1	1	1	0	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	0	1
x	1	x	x	x	x	1	x	1	x	x x		1 x	
x	x	x	x	x	x	x	x	x	x	x x		0 x	
x	x	x	x	x	x	x	x	x	x	x x		0 x	
x	363	x	x	x	x	368	x	370	x	x x		374 x	
t added post-indexed										Store doubleword w/ immediate offset substract post-indexed			
										Load Register offset signed byte added			

376	377	378	379	380	381	382	383	384	385	386	387	388	389
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	1	1	0	0	1	1	0	0	1	1	0	0	1
x	0	0	x	x	0	0	x	x	0	0	x	x	1
x	0	0	x	x	1	1	x	x	1	1	x	x	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	0	1	0	0	0	1	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	1	1	0	0	1	0	0	0	1	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	1	1	0	1	1	1	0	1	1	1	0	1	1
1	1	0	1	1	1	0	1	1	1	0	1	1	1
x	x	1	x	x	x	1	x	x	x	1	x	x	x
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	0	x	x	x	0	x	x	x	0	x	x	x
x	x	378	x	x	x	382	x	x	x	386	x	x	x
											Calc.	read	
Load Register offset signed byte substract				Load Register offset signed halfword add				Load Register offset signed halfword substract					L

390	391	392	393	394	395	396	397	398	399	400	401	402	403
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	0	1	0	0	0
1	0	1	0	0	0	0	1	0	1	0	1	0	0
1	0	1	0	0	0	1	0	1	0	0	0	0	0
1	0	1	0	0	0	1	0	1	0	0	0	1	1
1 x		1 x	x		1	1 x		1 x		0	0	0	0
1 x		1 x	x		1	1 x		1 x		1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	1	1	0	0	1	0	1	1	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	1	0	0	1	0	1	1	0	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	1	0	0	0	1	0	1	1	0	0	0
1	0	1	0	0	0	1	0	1	0	0	0	0	1
1	1	1	0	1	1	1	1	1	0	1	1	1	1
0	1	0	1	1	1	0	1	0	1	1	1	1	1
1 x		1 x		x x		1 x		1 x		x	x	x	0
0 x		0 x		x x		0 x		0 x		x	x	x	0
0 x		0 x		x x		0 x		0 x		x	x	x	0
390 x		392 x		x x		396 x		398 x		x	x	x	403
wair for MOC	Load into register			Calc.	read	wair for MOC	Load into register						
Load Register offset doubleword add					Load Register offset doubleword substract					Store Register offset signed halfword add			

404	405	406	407	408	409	410	411	412	413	414	415	416	417
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	1	1	0	0	1	1
0	0	0	0	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	0	0	1	1	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	1	0	0	1	0	0	0	1
1	1	1	1	1	1	1	0	1	1	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	x	0	x	x	x	1	x	0	x	x	x	1
x	x	x	0	x	x	x	0	x	0	x	x	x	0
x	x	x	0	x	x	x	0	x	0	x	x	x	0
x	x	x	407	x	x	x	411	x	413	x	x	x	417
Store Register offset signed halfword substract				Store Register offset s doubleword add						Store Register offset doubleword subtr			

418	419	420	421	422	423	424	425	426	427	428	429	430	431
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	1	1	0	0	1	1	0	0	1	1	0
1	1	0	1	1	0	0	1	1	0	0	1	1	0
1	1	x	0	0	x	x	0	0	x	x	0	0	x
1	1	x	0	0	x	x	0	0	x	x	1	1	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	1	0	1
0	0	1	0	0	1	0	0	0	1	0	0	0	1
0	1	0	0	1	0	0	0	1	0	0	0	1	0
1	1	1	1	1	0	1	1	1	0	1	1	1	0
1	1	1	1	1	0	1	1	0	1	1	1	0	1
x	0	x	x	1	x	x	x	1	x	x	x	1	x
x	0	x	x	0	x	x	x	0	x	x	x	0	x
x	0	x	x	0	x	x	x	0	x	x	x	0	x
x	419	x	x	422	x	x	x	426	x	x	x	430	x
ract		Load signed Byte w/ Register offset added pre-indexed				Load signed Byte w/ Register offset subtract pre-indexed				Load signed halfword w/ Register offset added pre-indexed			

432	433	434	435	436	437	438	439	440	441	442	443	444	445
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	1	0	0	0	1	0
0	1	1	0	0	1	1	0	1	0	0	1	1	0
0	1	1	0	0	1	1	0	1	0	0	1	1	0
x	0	0	x	x	1	1	1	1	1	x	1	1	1
x	1	1	x	x	1	1	1	1	1	x	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	0	0	0	1	1	0	0	0	0	1	0	0	0
0	0	0	1	1	0	0	1	0	1	0	0	0	1
0	0	1	0	0	0	1	0	1	0	0	0	1	0
1	1	1	0	1	1	1	0	1	0	1	1	1	1
1	1	0	1	1	1	0	1	0	1	1	1	0	1
x	x	1	x	x	x	1	x	1	x	x	x	1	x
x	x	0	x	x	x	0	x	0	x	x	x	0	x
x	x	0	x	x	x	0	x	0	x	x	x	0	x
x	x	434	x	x	x	438	x	440	x	x	x	444	x
										Calc.	Read	Wait for MOC	Load Byte into R
Load signed halfword w/ Register offset subtract pre-indexed				Load doubleword w/ Register offset added pre-indexed						Load doubleword w/ Register offset subtract pre-indexed			

446	447	448	449	450	451	452	453	454	455	456	457	458	459
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	1	0	0	1	0	0	1	1
1	1	0	0	0	0	0	0	0	0	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	1	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	0	0	0	0	0	1	0	0	0
1	0	0	0	0	0	1	0	0	0	0	0	0	1
1	0	1	1	1	1	1	1	1	1	1	1	1	0
0	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x		x	x	x	0	x	x	x	0	x	x	x	1
0 x		x	x	x	0	x	x	x	0	x	x	x	0
0 x		x	x	x	0	x	x	x	0	x	x	x	0
446 x		x	x	x	451	x	x	x	455	x	x	x	459
register													
		Store signed halfword w/ Register offset added pre-indexed				Store signed halfword w/ Register offset substract pre-indexed				Store doubleword w/ Register offset added pre-indexed			

460	461	462	463	464	465	466	467	468	469	470	471	472	473
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	1	0	0	0	0	1
0	0	0	1	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	1	1	0	0	0
1	1	0	0	1	1	1	1	0	1	1	0	0	0
1	1	1	1	1	1	1	1	x	0	0	x	x	
1	1	1	1	1	1	1	1	x	0	0	x	x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0	1	0	0	1	1	1
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	1	0	0	0	1	0	1	0	0	1	0	0	0
1	1	1	1	1	1	0	1	1	1	1	1	0	1
1	1	1	1	1	1	1	1	1	1	0	1	1	1
x	0	x	x	x	1	x	x	x	x	1	x	x	x
x	0	x	x	x	0	x	x	x	x	0	x	x	x
x	0	x	x	x	0	x	x	x	x	0	x	x	x
x	461	x	x	x	465	x	467	x	x	470	x	x	x

pre-indexed

Store doubleword w/ Register offset subtracted pre-indexed

Load signed byte and Add Register offset post-indexed

Load s

474	475	476	477	478	479	480	481	482	483	484	485	486	487
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
0	0	x	x	x	0	0	x	x	x	0	0	x	x
0	0	x	x	x	1	1	x	x	x	1	1	x	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	1	0	0	1	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	1	0	0	0	0
0	0	1	1	0	0	0	1	1	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	1	0	0	0	0	1	1	0	0	0	1	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	1	0	1	1	1	1	0	1	1	1	1	0
1	0	1	1	1	1	0	1	1	1	1	0	1	1
x	1	x	x	x	x	1	x	x	x	x	1	x	x
x	0	x	x	x	x	0	x	x	x	x	0	x	x
x	0	x	x	x	x	0	x	x	x	x	0	x	x
x	475	x	x	x	x	480	x	x	x	x	485	x	x

signed byte and subtract Register offset post-indexed

Load signed halfword and Add Register offset post-indexed

Load signed halfword and subtract Register offset post-indexed

488	489	490	491	492	493	494	495	496	497	498	499	500	501
0	0	0	1	0	1	0	0	0	0	1	0	1	0
0	0	0	1	0	1	1	0	0	0	1	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0	0	0	0
0	0	1	0	1	0	0	0	0	1	0	1	0	0
0	1	1	0	1	0	0	0	1	1	0	1	0	0
0	1	1	0	1	0	0	0	1	1	0	1	0	0
x	1	1	1	1	1	1 x	x	1	1 x		1 x		x
x	1	1	1	1	1	1 x	x	1	1 x		1 x		x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	1	0	0	0	0	1	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	1	0	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	1	1	1	0	0	1	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0	0	0	0
0	0	0	1	0	1	1	0	0	0	1	0	1	1
0	0	0	1	0	1	0	0	0	0	1	0	1	0
0	0	0	0	0	0	1	0	0	0	0	0	0	1
0	0	0	1	0	1	1	0	0	0	1	0	1	0
0	0	1	0	1	0	0	0	0	1	0	1	0	0
1	1	1	1	1	1	1	1	1	1	1	1	1	0
1	1	0	1	0	1	1	1	1	0	1	0	1	1
x	x	1	x	1	x	x	x	x	1	x	1	x	x
x	x	0	x	0	x	x	x	x	0	x	0	x	x
x	x	0	x	0	x	x	x	x	0	x	0	x	x
x	x	490	x	492	x	x	x	x	497	x	499	x	x
Load doubleword and Add Register offset post-indexed							Load doubleword and substract Register offset post-indexed						

502	503	504	505	506	507	508	509	510	511	512	513	514	515
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	1	0	0	1	1	1	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
1	1	1	0	0	1	1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	x	x	1	x	x	x	x	1	x	x	x	x	1
x	x	x	0	x	x	x	x	0	x	x	x	x	x
x	x	x	0	x	x	x	x	0	x	x	x	x	x
x	x	x	505	x	x	x	x	510	x	x	x	x	515
Store signed halfword w/ Register offset added post-indexed					Store signed halfword w/ Register offset subtracted post-indexed					Store doubleword w/ Register offset :			

516	517	518	519	520	521	522	523	524	525	526	527	528	529
0	0	0	0	0	0	0	0	0	0	1	1	1	1
0	0	1	0	0	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	1	1	0	0	0	0	0
1	1	1	1	1	1	1	1	1	1	x	x	x	x
1	1	1	1	1	1	1	1	1	1	x	x	x	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	1	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	1	1	0	1
0	0	1	0	0	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	1	1	1	0	1	0	0	1	0	0	0	0
1	0	0	0	0	1	0	1	0	0	0	0	0	0
1	0	0	1	1	1	0	1	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	1	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	1	0	0	0	0	0
1	0	0	1	1	1	1	0	1	0	0	0	0	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
x	1	x	x	x	x	1	x	1	x	x	x	x	x
x	x	x	x	x	x	x	x	x	x	x	x	x	x
x	x	x	x	x	x	x	x	x	x	x	x	x	x
x	517	x	x	x	x	522	x	524	x	x	x	x	x
										CMP(shift)	CMP(Immediate)	TST(R-R)	TST(shift)
added post-indexed			Store doubleword w/ Register offset subtract post-indexed										

[illegible]

543	544	545	546	547	548	549	550	551	552
1	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
0	0	0	1	0	0	0	1	0	0
0	0	1	1	0	0	1	1	0	0
0	0	1	1	0	0	1	1	0	0
x	x	x	x x	x	x	x	x x		x
x	x	x	x x	x	x	x	x x		x
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	1	0
1	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	1	0	0	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0
0	1	0	0	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	0	0	0	0
0	0	0	1	1	0	0	1	1	0
0	1	1	0	1	1	1	0	1	1
1	1	1	1	1	1	1	1	1	1
x	x	x	1	0	x	x	1	0	x
x	x	x	0	1	x	x	0	1	x
x	x	x	0	0	x	x	0	0	x
x	x	x	546	544	x	x	550	548	x
Add/Sub(Immediate)S									
on the Inst. Register	Multiple Load w/ IA(increment after)				Multiple Load w/ IB(increment before)				

553	554	555	556	557	558	559	560	561	562
0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0
1	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	1	1
0	1	1	0	0	1	1	0	1	1
x	x	x	x	x	x	x	x	x	x
x	x	x	x	x	x	x	x	x	x
0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	0	0
1	0	0	0	1	0	0	0	0	0
1	0	1	1	1	0	1	0	0	0
0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	1	0	0
0	0	1	1	0	0	0	0	0	0
0	1	1	0	0	1	1	0	0	1
1	1	1	1	1	1	1	1	1	0
1	0	1	1	1	0	1	1	1	1
x	x	1	x	x	x	1	x	x	1
x	x	0	x	x	x	0	x	x	0
x	x	0	x	x	x	0	x	x	0
x	x	552	x	x	x	556	x	x	562
Multiple Store w/ IA(Increment After)			Multiple Store w/ IB(Increment before)			Multiple Load w/ DA(decrement after)			

563	564	565	566	567	568	569	570	571	572
0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
0	0	0	1	0	0	1	0	0	0
0	0	1	1	0	0	0	0	0	0
0	0	1	1	0	0	0	1	1	0
x	x	x	x x		x	x	x	x	1
x	x	x	x x		x	x	x	x	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	0	0	0
1	0	0	0	1	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	1	0	0	1
0	0	0	0	0	0	1	0	0	0
1	0	0	0	1	0	1	0	1	0
0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
1	0	0	0	0	0	0	0	1	0
1	0	0	1	1	0	0	1	1	0
1	1	1	0	1	1	1	1	1	1
1	1	1	1	1	1	1	0	1	1
0	x	x	1	0	x	x	x	1	x
1	x	x	0	1	x	x	x	0	x
0	x	x	0	0	x	x	x	0	x
560	x	x	566	564	x	x	x	568	x
Multiple Load w/ DB(decrement before)					Multiple Store w/ DA(decrement After)				

573	574	575	576
0	0	0	0
0	0	1	1
0	0	0	0
0	0	0	0
1	0	0	0
0	0	0	0
0	1	1	0
1	1	1	x
0	0	0	x
0	0	0	1
1	0	0	0
0	0	0	0
0	0	0	1
0	0	0	0
0	0	0	1
0	0	0	0
0	0	0	1
1	0	0	1
1	0	0	0
1	0	1	0
0	0	0	0
0	0	0	1
0	0	0	0
0	0	0	0
0	1	1	0
1	1	1	0
1	0	1	1
x	x	1 x	
x	x	0 x	
x	x	0 x	
x	x	572	1
Multiple Store w/ DB(decrement before)			