		0	1	2 3	4	5	6	7	,	8 9	10	11
SR LD		0	0	0	0	0	0	C)	1 1	0	0
RF LD		1	0	1 0	0	1	1	1		0 1	0	0
IR LD		0	0	0 1	0	0	0	C)	0 0	0	0
MAR LD		0	1	0	0	0	0	C)	0 0	1	0
MDR LD		0	0	0 0	0	0	0	C)	0 0	0	0
R/W		0	0	1 1	0	0	0	C)	0 0	0	1
MOV		0	0	1 1	0	0	0	C)	0 0	0	1
DT 1	x	x		1 1	x	x	x	x	х	x	х	0
DT 0	x	x	(0 0	x	x	x	x	х	x	х	0
MA 1		0	1	1 0	0	0	0	C)	0 0	0	0
MA 0		0	0	0 0	0	0	0	C)	0 0	0	0
MB 1		1	0	0 0	0	0	0	C)	0 0	0	0
MB 0		1	0	0 0	0	0	1	1		0 0	1	0
MC1		0	0	0 0	0	0	0	C)	0 0	0	0
MC0		1	0	1 0	0	0	0	C)	0 0	0	0
MD		1	1	1 0	0	0	0	C)	0 0	1	0
ME		0	0	0 0	0	0	0	C)	0 0	0	0
OP4		0	1	1 0	0	0	0	C)	0 0	0	0
OP3		1	0	0 0	0	0	0	C)	0 0	0	0
OP2		1	0	0 0	0	0	0	C)	0 0	1	0
OP1		0	0	0 0	0	0	0	C)	0 0	0	0
OP0		1	0	1 0	0	0	0	C)	0 0	0	0
N2		0	0	0 1	1	0	0	C)	0 0	0	0
N1		1	1	1 0	0	0	0	C)	0 0	1	1
N0		1	1	1 1	0		1	1		1 1	1	1
Inv	X	X	X	1	0	X	X	X	X	Х	X	X
S1	x	x	х	0	0	x	x	Х	x	x	х	x
S0	X	х	X	0		x	x	Х	x	x	x	х
CR(decimal)	X	X	X	3	1	х	X	X	х	X	X	x
	Reset State	Load PC into M	e Increment PC	Wait for MOC	Check Condition			Add/Sub(Immediate)	CMP (R-R)	MOV(R-R)		Begin Byte Read
						The operation pe	erformed depends	on the Inst. Register			Load	Byte w/ or w/out ii

12	13	14	15	16	17	18	19	20	21	22	23	24	25
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0	х	х	1	1	х	x	0	0	x	x	1	1	x
0	х	x	0	0	х	х	0	0	x	x	0	0	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	1	1	0	0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	1	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1	X	x x		1	Х	x	X	1	Х	x x		1	Х
0	x	x x		0	x	x	х	0	x	x x		0	x
0		x x		0		x	X	0		x x		0	
12	X	x x		16	X	X	X	20	X	x x		24	Х
		Calc. & Load Add Be				Calc. & Load Add				Calc. & Load Add B			Load Word into T
mmediate offset a	dded	Load Wo	ord w/ or w/out ir	nmediate offset a	ıdded	Load	d Byte w/ Immedia	ate Offset Subtrac	eted	Load	Word w/ immedia	te offset Subtrac	eted

26	27	28	29	30	31	32	33	34	35	36	37	38	39
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	1	1	0	0	1	1	0	0	1	1	0	0	1
x	0	0	x	х	0	0	x	x	1	1 x		x	1
x	0	0	x	х	0	0	x :	x	0	0 x		x	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	1	0	0	0	1	1	0	0	1	0	0
0	0	0	0	1	0	0	0	0	0	0	0	1	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	1	0	0	1	1	0	0	1	1	0	0	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	X			X	Х	1		X	X	1 x		X	X
X	X			Х	X	0		x	X	0 x		X	х
X	X				X	0			x	0 x			X
X	X	28	X	X	Х	32	X	X	X	36 x		X	X
arget Register				-	10 . 01.6				114/ 101/6	D : 1 :::		_	
	Load Byte Shift B	y Register adding		Lo	ad Byte Shift By Re	gister Subtractin	g	Lo	ad Word Shift B	y Register adding		Lo	ad Word Shift By I

40	41	42	43	44	45	46	47	48	49	50	51	52	53
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	0	0	0	1	0	0	0	1	0	0	0	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
1	0	0	1	1	0	0	1	1	0	0	1	1	0
1 x	x	(0	0 x		x	1	1 x	x		0	0 x	
0 x	x	(0	0 x		x	0	0 x	x		0	0 x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
0	1	1	0	0	1	1	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x	X			1 x		X X		1 x	X	X		1 x	
0 x	X			0 x		X X		0 x	X	X		0 x	
0 x	X			0 x		X X		0 x	X	X		0 x	
40 x	X	X		44 x		X X		48 x	X	X		52 x	
		Calo, & Load Add Bosin	Puta Dood Mait	for MOC Load D	to into T	Cala & Load Add Bosin M	Vord Dog M	/oit for MOC	/ord into I Colo 9	2 Load Adr Bosis	Puto Dood Wait t	for MOC	nd Duto into Tr
Register Subtracting	(Calc. & Load Add Begin			/te into 18	Calc. & Load Add Begin V				Load Add Begin			ad Byte into Ta
register subtracting		Load Byte W/ II	iiiiediate offset	added pre-indexed		Load word w/ in	imediate off	fset added pre-indexed		Load Byte w/ Imr	nediate Offset St	ibiracted pre-in	uexeu

	54	55	56	57	58	59	60	61	62	63	64	65	66	67
	0	0	0	0	0	0	0	0	0	0	0	0	00	07
	1	0	0	1	0	0	0	1	1	0	0	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	0	1	0	0	0	0
	0	0	1	0	0	0	1	0	0	0	0	1	0	0
	0	1	1	0	0	1	1	0	0	0	1	1	0	0
	0	1	1	0	0	1	1	0	0	0	1	1	0	0
x		1	1 :			0	0 x	х	х		0	0 x	x	
x		0	0			0	0 x	х	x		0	0 x	x	
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	0	1	0
	1	0	0	0	1	0	0	0	1	1	0	0	0	1
	1	0	0	0	0	0	0	0	1	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	1	0	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	1	0	0	0	0	1	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	0	1	0
	0	0	0	1	0	0	0	1	1	0	0	0	1	0
	1	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	1	0	0	0	1	0	0	0	0	1	0
	0	0	1	0	0	0	1	0	0	0	0	1	0	0
	1	1	0	0	1	1	0	1	0	1	1	0	1	0
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	×	(1 :	x x	Х		1 x	X	X	X		1 x	X	
X	×	(0		х		0 x	х	X	x		0 x	x	
X	Х	(0		X		0 x	x	X	x		0 x	X	
X	×	(56	x x	х		60 x	Х	Х	X		65 x	X	
		Begin Word Rea Wait for I		Load Word into Target Re										
Load	Word	w/ immediate offset Subtr	acted pre	e-indexed	Loa	ad byte and Add imr	nediate offset p	oost-indexed		Load byte	and subtract ir	nmediate offset p	ost-indexed	

	68	69	70	71	72	73	74	75	76	77	78	79	80	81
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	1	0	0	0	1	1	1	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	1	0	0	0	0	1	0	0	0
	0	0	1	0	0	0	0	1	0	0	0	0	1	0
	0	1	1	0	0	0	1	1	0	0	0	1	1	0
	0	1	1	0	0	0	1	1	0	0	0	1	1	0
x		1	1 x	x		x	1	1 x	x	(x	0	0	x
x		0	0 x	x		x	0	0 x	x	(x	0	0	x
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	0	1	0	0	0	0	1
	1	0	0	0	1	1	0	0	0	1	0	0	0	0
	0	0	0	0	1	0	0	0	0	1	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	1	0	0	1	1	1	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	1	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	0	1	0	0	0	0	1
	0	0	0	1	1	0	0	0	1	0	1	0	0	1
	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	0	0	0	1	0	0	0	0	1	0	0	0	0	1
	0	0	1	0	0	0	0	1	0	0	0	0	1	0
	1	1	0	1	0	1	1	0	1	0	1	1	0	0
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	X		1 x	X		X X		1 x	X		X			X
X	X		0 x	X		X X		0 x	X		X			X
X	X		0 x	X		X X		0 x	X		X >			X
X	Х		70 x	X	2	X X		75 x	X		X	(80	X
											Calc. & Load Add	Regin Ryta Dood	Wait for MOC	Load Byte into Te
	Loa	d Word and add imn	mediate offset no	st_indeved		Load W	ord and subt	ract immediate offse	at nost_indev		Calc. & Load Add B			Load Byte into Ta
	Loa	u vvoiu ailu auu iilii	neulate oliset po	SI-IIIUEXEU		LUAU W	oru ariu Subi	iaci iiiiiieulate olist	t post-maexe	- u	Load B	yte w/ register of	fset added pre-in	uexeu

	82	83	84	85	86	87	88	89	90	91	92	93	94	95
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	0	0	1	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	0	1	1	0	0	1	1	0	0	1	1	0	0	1
	0	1	1	0	0	1	1	0	0	1	1	0	0	1
x		1	1	x	X	0	0	x	x	1	1 x		х	0
x		0	0	х	X	0	0	x	x	0	0 x		х	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	0	0	1	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	1	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	0	0	1	0	0	0	1	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	1	1	0	0	1	1	0	0	1	1	0	0	1	1
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	X		1	Х	X	X	1	X	x	X	1 x		X	X
X	x		0		X	x			х	x	0 x		х	x
X	X		0		x	x		x	x	Х	0 x		x	х
X	X		84	X	Х	X	88	x	x	X	92 x		х	X
Calc. & L		Word Rea Wait fo				Begin Byte Read				Begin Word Rea			Target Register	
	Load Word w	v/ register offset ac	dded pre-ind	dexed	Load Byte	e w/ Register Offse	et Subtracted pre	e-indexed	Load Wo	ord w/ register offse	et Subtracted pre-ir	ndexed		Load byte and A

96	97	98	99	100	101	102	103	104	105	106	107	108	109
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	1	1	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
1	0	0	0	0	1	0	0	0	0	1	0	0	0
1	0	0	0	1	1	0	0	0	1	1	0	0	0
1	0	0	0	1	1	0	0	0	1	1	0	0	0
0 >	x x		x	0	0 x	X	· · ·	(1	1 x		x	x
0 >	x x		x	0	0 x	х	· · ·	(0	0 x		x	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	1	1	1	0	0	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	1	1	0	0	0	1	0	0	0	0	1	1	0
0	0	0	0	0	0	0	1	0	0	0	0	0	0
0	1	0		0	0	1	0	0	0	0	1	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	1	1	0	1	0	1	1	0	1	0	1
1 >	, I	I	I V	I	1 x	I	, ,	,	I	1 x	I	I	X
			X X			X			X				
0 >			X X		0 x 0 x	X			X	0 x 0 x			x x
96 >			x x x		101 x	x			x x	106 x			X
90)	X		^ X		101 X	X	. ,		^	100 X		^	^
Add register offse	t post-indexed		Loa	d byte and sub	otract register offset p	oost-indexed		L	oad Word and	add register offset	post-indexed		Lo

110	111	112	113	114	115	116	117	118	119	120	121	122	123	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	1	1	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	1	0	0	0	1	0	0	0	1	0	
0	1	0	0	0	1	0	0	0	1	0	0	0	1	
1	1	0	0	0	0	0	0	0	0	0	0	0	0	
1	1	0	0	0	0	1	1	0	0	1	1	0	0	
1	1	х	x	0	0	0	0	1	1	1	1	0	0	
0	0	х	x	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	0	1	0	0	0	1	0	0	0	1	
0	0	1	0	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	1	0	0	0	1	0	0	0	1	0	
0	0	0	1	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	1	1	1	1	0	0	1	1	0	0	1	1	
0	0	0	0	0	1	0	0	0	1	0	0	0	1	
0	0	0	0	0	1	0	0	0	1	0	0	0	1	
0	0	1	0	0	0	0	0	0	0	0	0	0	0	
0	0	1	0	1	0	0	0	1	0	0	0	0	0	
0	0	0	1	0	0	0	0	0	0	0	0	1	0	
0	0	1	0	0	0	0	0	0	0	0	0	0	0	
0	1	0	0	0	0	0	1	0	0	0	1	0	0	
1	0	1	0	1	1	1	1	1	1	1	1	1	1	
1	1	1	1	1	1	1	1	1	1	1	1	1	1	
x	1	х	x	X	x	x	0	x	x x		0	x x		
x	0	x	x	x	x	x	0	х	x		0	x x	<u>.</u>	
x	0	X	x	X	x	х	0	х	x x		0	x x		
x	111	х	x	Х	x	x	117	х	x x		121	x x		
													byte w/ immed	
ad Word and su	0 0 0 0 1 0 0 0 1 1 0 0 0 0 0 0 0 0 0 0													

124	125	126	127	128	129	130	131	132	133	134	135	136	137
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	1	1	0	0	1	1
0	0	1	1	1	1	0	0	0	0	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	0 >		X X		0 :		X	(0		2	X	0
X	ν 0		x x		0		X X		0			x	0
X	Ο >		x x		0 :		X		0			X	0
X	125 >	<	x x		129	X	X X	(133	x x	2	X	137
			1 /								. , .		
iate offset subtrac	ted	Store	e word w/ immedia	te offset subtra	cted		Store byte w/ regis	ter offset added		Sto	ore word w/ regi	ster offset added	

	138	139	140	141	142	143	144	145	146	147	148	149	150	151
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	1	0	0	0	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	1	0	0	0	1	0	0	0	1	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	1	0	0	1	1	0	0	1	1	0	0
	0	0	0	0	1	1	1	1	0	0	0	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	1	0	0	0	1	0	0	0	1	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	1	0	0	0	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	0	0	1	1	0	0	1	1	0	0	1	1
	0	1	0	0	0	1	0	0	0	1	0	0	0	1
	0	1	0	0	0	1	0	0	0	1	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	1	0	0	0	1	0
	1	0	0	0	1	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	х	X		0	X	x x		0	X	X	Х	0		X
X	х	X		0		x x		0		X	х			х
X	Х	X		0		x x		0		Х	Х			X
X	Х	X		141	X	x x		145	X	Х	Х	149	X	X
													_	
	Stor	e byte w/ register offs	set subtracte	ed	Sto	ore word w/ registe	r offset subtract	ed	Store by	te w/ immediate	offset added pre-	indexed	Store wo	ord w/ immediate

152	2 1	53	154	155	156	157	158	159	160	161	162	163	164	165
(0	0	0	0	0	0	0	0	0	0	0	0	0	0
(0	0	1	0	0	0	1	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
=	1	1	0	0	1	1	0	0	1	1	0	0	1	1
=	1	1	0	0	0	0	1	1	1	1	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
(0	0	0	0	0	0	0	0	0	0	0	0	0	0
(0	0	1	0	0	0	1	0	0	0	1	0	0	0
(0	0	0	0	0	0	0	0	0	0	0	0	0	0
(0	0	0	0	0	0	0	0	0	0	0	0	0	0
(0	0	1	1	0	0	1	1	0	0	1	1	0	0
(0	0	0	1	0	0	0	1	0	0	0	1	0	0
(0	0	0	1	0	0	0	1	0	0	1	1	0	0
(0	0	0	0	0	0	0	0	0	0	0	0	0	0
(0	0	0	0	0	0	0	0	0	0	0	0	0	0
(0	0	1	0	0	0	1	0	0	0	0	0	0	0
(0	0	0	0	0	0	0	0	0	0	0	0	0	0
(0	1	0	0	0	1	0	0	0	1	0	0	0	1
•	1	1	1	1	1	1	1	1	1	1	1	1	1	0
•	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Х		0 x		X		0		x	Х		x x		X	1
X		0 x		X		0		x	X		x x		х	0
X		0 x		X		0			Х		x x		X	0
X	1	53 x	X	X		157	X	X	X	161	x x		X	165
							G							
offset added pro	e-ındexed		Store byte w/ immedia	ite offse	t subtracted pre-inde	red	Store word	w/ immediate of	fset subtracted p	re-indexed	St	tore byte w/ im	mediate offset add	led post-indexed

	166	167	168	169	170	171	172	173	174	175	176	177	178	179
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	1	0	0	0	0	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	1	0	0	0	0	1	0	0	0	0	1	0	0
	0	0	1	0	0	0	0	1	0	0	0	0	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	1	0	0	0	1	1	0	0	0	1
	0	1	1	1	1	1	0	0	0	0	0	1	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	0	0	0	0	1	0	0	0	0	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	0	0	0	1	1	0	0	0	1	1	0	0
	1	0	0	0	0	1	0	0	0	0	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	1	0	0	1	1	1	0	0	1	1	1	0
	0	0	1	0	0	0	0	1	0	0	0	0	1	0
	0	1	1	0	0	0	1	1	0	0	0	1	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	1	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	1	0	0	0	0	1	0	0	0	0
	0	1	1	1	0	0	1	1	1	0	0	1	1	1
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	X	X	X		1 x	Х	X)	(1 x	Х	X	Х	
X	X	x	x		0 x	x)	(0 x	x	X	X	
X	Х	X	x		0 x	Х)		0 x	х	X	X	
X	Х	X	X		170 x	Х	X	>	(175 x	Х	X	X	
							<u> </u>						1 /	
		Store w	vord w/ immedia	te offset added p	ost-indexed		Store by	yte w/ immed	liate offset subtracte	ed post-indexed		Store w	ord w/ immedia	ate offset subti

180	181	182	183	184	185	186	187	188	189	190	191	192	193
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	1	1	0	0	1	1
1	1	0	0	0	0	1	1	1	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	1	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	1	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
1 x	Х)	(x	0 >	C	Х	Х	0	x x		Х	0
0 x	x)	(x	0	(x	x	0	x x		x	0
0 x	х	>	(x	0		Х	Х	0			X	0
180 x	х)		x	185 >	(X	x	189	x x		Х	193
			,			-	. ,				,		
racted post-indexed		Store by	te w/ register off	fset added pre-ind	dexed	Store w	ord w/ register of	ffset added pre-ir	ndexed	Store byte	w/ register offs	set subtracted pre-	indexed

	194	195	196	197	198	199	200	201	202	203	204	205	206	207
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	0	0	0	1	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	0	1	0	0	0	0
	0	1	0	0	0	1	0	0	0	0	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	1	0	0	1	1	0	0	0	1	1	0
	1	1	1	1	0	0	0	0	0	1	1	1	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	1	0	0	0	1	0	0	0	0	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	1	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	0	0	1	1	0	0	1	1	1	0	0	1
	0	1	0	0	0	1	0	0	0	0	1	0	0	0
	0	1	0	0	1	1	0	0	0	1	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	1	0	0	0	0	1
	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	0	1	0
	1	1	1	1	1	1	1	0	0	1	1	1	0	0
	1	1	1	1	1	1	1	1	1	1	1	1	1	1
X)	X		0		X	<	1 >		X	X	(1	
X	У	X X		0		X >	(0 >		Х	x	(0	
X		X X		0		X		0 >		X	X >		0	
X)	X		197	x	X	(201	(X	X	<	206	X
	C 1	1 /												
	Store word	d w/ register offset	subtracted pre	-indexed		Store byte w/ reg	gister offset adde	ed post-indexed			Store word w/ re	gister offset adde	ed post-indexed	

	208	209	210	211	212	213	214	215	216	217	218	219	220	221
	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	0	1	0	0	0	0	1	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	1	0	0	0	0	1	0	0	0
	0	1	0	0	0	0	1	0	0	0	0	0	1	0
	0	0	0	0	0	0	0	0	0	0	0	1	1	0
	0	0	1	1	0	0	0	1	1	0	0	1	1	0
	0	0	0	0	0	1	1	1	1	1	х	0	0	(
	0	0	0	0	0	0	0	0	0	0	х	0	0	(
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	1	0	0	0	0	1	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	1	0	0	0
	0	0	0	0	1	0	0	0	0	1	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	0	0	1	1	1	0	0	1	1	0	0	1
	0	1	0	0	0	0	1	0	0	0	0	0	0	0
	1	1	0	0	0	1	1	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	1	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	0	0	0	0	1	0	0	0	0	1	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	1	0	0	1
	0	0	0	1	0	0	0	0	1	0	0	0	1	0
	1	1	1	0	0	1	1	1	0	0	1	1	1	0
	1	1	1	1	1	1	1	1	1	1	1	1	0	1
X	X	X		1 x		x x		X	1		x 2		1 >	
X	X	X		0 x		x x		X	0		x 2		0 >	
X	X	X		0 x		x x		X	0		x 2		0 >	
X	Х	X		211 x		x x		X	216	X	x 2	X	220 >	(
		- h				2.				1		d l	and the desired	
	Store	e byte w/ register of	rset subtract	ea post-indexed		Sto	ore word w/ regi	ster offset subtrac	ted post-indexed	I	L	oad Imm offset sig	gned byte added	

	222	223	224	225	226	227	228	229	230	231	232	233	234	235
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	0	1	1	0	0	1	1	0	0	1	1	0	0	1
	0	1	1	0	0	1	1	0	0	1	1	0	0	1
x		0	0 x	x		0	0 x	>	(0	0 x	X		1
X		0	0 x	x		1	1 x	>	(1	1 x	X		1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	0	0	1	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	0	0	1	1	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	0	0	1	1	0	0	1	0	0	0	1	1	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	1	1	1	0	1	1	1	0	1	1	1	0	1	1
	1	1	0	1	1	1	0	1	1	1	0	1	1	1
X	X		1 x		x X		1 x		x X		1 x		X Z	
X	X		0 x		x X		0 x		x X		0 x		x 2	
X	X		0 x		x X		0 x		x X		0 x		x Z	
X	X		224 X		хХ		228 X		x X		232 X		x Z	
												Cal	C.	read
	Load Imm	offset signed b	oyte substract		Load Im	m offset signe	d halfword add		Load Imm	offset signed ha	alfword substract			

236	237	238	239	240	241	242	243	244	245	246	247	248	249
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	0	1	0	0	0
1	0	1	0	0	0	1	0	1	0	0	1	0	0
1	0	1	0	0	1	1	0	1	0	0	0	0	0
1	0	1	0	0	1	1	0	1	0	0	0	1	1
1	x	1 x	x		1	1 x		1 x		0	0	0	0
1	x	1 x	x		1	1 x		1 x		1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	1	1	0	0	1	0	1	1	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	1	0	0	1	0	1	1	0	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	1	0	0	0	1	0	1	1	0	0	0
1	0	1	0	0	0	1	0	1	0	0	0	0	1
1	1	1	0	1	1	1	1	1	0	1	1	1	1
0		0	1	1	1	0	1	0	1	1	1	1	1
1	X	1 x		хх		1 x		1 x		X	X	Х	0
0	x	0 x		x X		0 x		0 x		X	x	x	0
	X	0 x		x X		0 x		0 x		x	х	х	0
236		238 X		x X		242 X		244 X		Х	x	x	249
wair for MOC	Load into registe	er	Ca	ilc. read	d	wair for MOC Load	d into register	-					
Load Imm offset	doubleword add				Loa	ad Imm offset doublew	ord substract			Store in	nm offset signed	halfword add	

250	251	252	253	254	255	256	257	258	259	260	261	262	263
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	1	1	0	0	1	1
0	0	0	0	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	0	0	1	1	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	1	1	1	1	1	1	0	1	1	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	X	X	0	X	Х	X	1	X	0	X	X	X	1
Х	x	х	0	х	Х	х	0	х	0	х	х	Х	0
X	X	X	0	Х	Х	х	0	Х	0	X	X	Х	0
X	X	X	253	X	Х	X	257	X	259	X	X	X	263
Store imm	n offset signed ha	lfword substract		Store in	mm offset s do	oubleword add					Store	imm offset dou	ubleword subs

264	265	266	267	268	269	270	271	272	273	274	275	276	277
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	1	0	0	0	1	0
0	0	0	1	1	0	0	1	1	0	0	1	1	0
1	1	0	1	1	0	0	1	1	0	0	1	1	0
1	1	x	0	0 x		x	0	0 x		X	0	0	x
1	1	x	0	0 x		x	0	0 x		X	1	1	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	1	1	0	0	1	1	0	0	1
0	0	0	0	0	1	0	0	0	1	0	0	0	1
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	1	0	0	1	0	0	0	1	1	0	0	1
0	1	0	0	1	0	0	0	1	0	0	0	1	0
1	1	1	1	1	0	1	1	1	0	1	1	1	0
1	1	1	1	0	1	1	1	0	1	1	1	0	1
X	0	X	X	1	Х	X	X	1	X	Х	X	1	X
x	0	Х	X	0	Х	Х	x		Х	Х	x	0	x
Х	0	Х	X	0	Х	X	X		Х	Х	X	0	X
X	265	X	X	268	Х	X	X	272	Х	X	X	276	X
		Load signed By	/te w/ immedia	ate offset added p	re-indexed	Load signed By	yte w/ immediat	e offset substract p	re-indexed	Load signed hal	tword w/ immed	diate offset adde	d pre-indexed

	278	279	280	281	282	283	284	285	286	287	288	289	290	291
	0	0	0	1	0	0	0	1	0	1	0	0	0	1
	1	0	0	1	1	0	0	1	0	1	1	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	0	0	1	0	0	0
	0	0	1	0	0	0	1	0	1	0	0	0	1	0
	0	1	1	0	0	1	1	0	1	0	0	1	1	0
	0	1	1	0	0	1	1	0	1	0	0	1	1	0
X		0	0	X	X	1	1	1	1	1	x	1	1	1
X		1	1	X	X	1	1	1	1	1	x	1	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	1	0	0	0	1
	1	0	0	0	1	0	0	0	0	0	1	0	0	0
	1	0	0	0	1	0	0	0	0	0	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	0	1	1	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	0	1	1	0	0	1
	0	0	0	1	0	0	0	1	0	1	0	0	0	1
	1	0	0	0	1	0	0	0	0	0	1	0	0	0
	0	0	0	1	1	0	0	1	0	1	0	0	0	1
	0	0	1	0	0	0	1	0	1	0	0	0	1	0
	1	1	1	0	1	1	1	0	1	0	1	1	1	1
	1	1	0	1	1	1	0	1	0	1	1	1	0	1
	X	Х	1	X	X	X	1	X	1	Х	X	Х	1	X
	Х	х	0	х	X	X	0	х	0	х	х	x	0	X
	Х	Х	0	х	Х	Х	0	х	0	Х	Х	х		
	Х	х	280	X	X	х	284	Х	286	Х	х	X	290	Х
												Read	Wait for MOC	Load Byte into Re
Load signe	d half\	word w/ immediate	e offset substr	act pre-indexed		Load doublev	vord w/ immedia	ate offset added p	ore-indexed		Load doublew	ord w/ immedia	te offset substra	ct pre-indexed

292	293	294	295	296	297	298	299	300	301	302	303	304	305
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
1	0	0	1	0	0	0	1	0	0	0	1	0	0
1	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	1	1	0	0	1	1
1	1	0	0	0	0	0	0	0	0	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	1	1	0	0	1	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	0	0	1	0	0	0	1	0	0	0	1	0	0
0	1	1	0	0	0	1	0	0	0	1	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	1	0	0	0	1	0	0	0
0	1	1	0	0	0	0	0	0	0	1	0	0	0
1	0	0	0	0	1	0	0	0	1	0	0	0	1
1	0	1	1	1	1	1	1	1	1	1	1	1	0
0	1	1	1	1	1	1	1	1	1	1	1	1	1
1	X	Х	X	X	0	X	X	X	0	X	X	X	1
0	x	х	х	x	0	x	x	х	0	х	x	x	0
0		х	х	x		x	х	Х		х	х	x	0
292	x	х	х	X	297	Х	X	Х	301	х	х	X	305
egister													
		Store signed ha	alfword w/ immed	diate offset adde	ed pre-indexed	Store signed hal	fword w/ immedia	ate offset subst	ract pre-indexed		Store doublev	vord w/ immedia	ate offset added

306	307	308	309	310	311	312	313	314	315	316	317	318	319
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	1	0	0	0	0	1
0	0	0	1	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	1	1	0	0	0
1	1	0	0	1	1	1	1	0	1	1	0	0	0
1	1	1	1	1	1	1	1	х	0	0	x	x	x
1	1	1	1	1	1	1	1	х	0	0	x	x	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0	1	0	0	1	1	1
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0		0	0		0	0	1	1	0
0	1	0	0	0		0	1	0	0	1	0	0	0
1	1	1	1	1	0	1	1	1	1	1	1	0	1
1	1	1	1	1	1	1	1	1	1	0	1	1	1
X	0	X	Х	Х		X	0	Х	Х	1	Х	Х	X
X	0	X	X	X		Х	0		х		X		X
X	0	X		Х		X		Х	Х		х		X
X	307	X	X	X	311	X	313	Х	Х	316	Х	х	X
			Otana da della	ad/ !:aa :!!		d : !			alama al lecete	al A alal imerica1! (1 1 :
pre-indexed			Store doublewor	a w/ immediate	e offset subtracte	a pre-indexed		Load	signed byte and	d Add immediate	ortset post-ind	exea	Load si

320	321	322	323	324	325	326	327	328	329	330	331	332	333
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
0	0 x	x	x		0	0	x		X	0	0	x	K
0	0 x	x	x		1	1	x		X	1	1	x	K
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	1	1	0	0	0	1	1	0	0	0	1
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	1	0	0	1	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	1	0	0	0	0	1	1	0	0	0	1	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	1	0	1	1	1	1	0	1	1	1	1	0
1	0	1	1	1	1	0	1	1	1	1	0	1	1
X	1	X	X	Х	X	1	X	х	Х	X	1	X	X
X	0	X	X	Х	X	0	X	x	X	X	0	X	X
X	0	X	X	Х	X	0	X	X	X		0	X	X
X	321	x	X	X	Х	326	X	Х	х	X	331	X	X
		. , , .										P 4 65 4 4	
gned byte and su	bstract immediate off	set post-indexe	ed	Load sig	ned halfword and	Add immedia	te offset post-inde	exed	Load sign	ed halfword and	substract imme	diate offset post-	indexed

	334	335	336	337	338	339	340	341	342	343	344	345	346	347
	0	0	0	1	0	1	0	0	0	0	1	0	1	0
	0	0	0	1	0	1	1	0	0	0	1	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	0	0	1	0	0	0	0	0	0
	0	0	1	0	1	0	0	0	0	1	0	1	0	0
	0	1	1	0	1	0	0	0	1	1	0	1	0	0
	0	1	1	0	1	0	0	0	1	1	0	1	0	0
X		1	1	1	1	1 x		x	1	1	х	1	x	×
X		1	1	1	1	1 x		x	1	1	x	1	x	x
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	1	0	0	0	0	1	0	1	0
	1	0	0	0	0	0	1	1	0	0	0	0	0	1
	0	0	0	0	0	0	1	0	0	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	0	1	1	1	0	0	1	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	0	0	1	0	0	0	0	0	0
	0	0	0	1	0	1	1	0	0	0	1	0	1	1
	0	0	0	1	0	1	0	0	0	0	1	0	1	0
	0	0	0	0	0	0	1	0	0	0	0	0	0	1
	0	0	0	1	0	1	1	0	0	0	1	0	1	0
	0	0	1	0	1	0	0	0	0	1	0	1	0	0
	1	1	1	1	1	1	0	1	1	1	1	1	1	0
	1	1	0	1	0	1	1	1	1	0	1	0	1	1
	X	х	1	х	1	X	Х	X	X	1	X	1	X	X
	Х	х	0	х	0	X	х	x	x	0	x	0	X	x
	Х	Х	0		0	Х	Х	X	X	0			X	X
	X	X	336	х	338	X	Х	X	X	343	X	345	X	X
					_									
	Load	doubleword and	Add immediate	e offset post-inde	xed			Load do	ubleword and s	ubstract immed	ate offset post-ir	ndexed		

348	349	350	351	352	353	354	355	356	357	358	359	360	361
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	0	1	0	0	0	0	1	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	0	1	1	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	1	0	0	1	1	1	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
1	1	1	0	0	1	1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	Х	X	1	X	X	X	X	1	X	X	X	X	1
X	X	X	0	Х	X	X	X	0	X	Х	X	X	Х
X	X	X	0	Х	X	X	X	0	X	X	X	Х	Х
X	Х	X	351	Х	X	X	X	356	X	X	X	X	361
Store signer	d halfword w/ imn	nediate offset add	ded nost-indexed		Store signed	halfword w/immo	diate offset subs	tracted post-index	ad		Store dou	bleword w/ im	mediate offso
Store signer	a nanword w/ IIIII	inculate offset du	aca post-inaexeu		Julie signed	nanword w/ mille	aiate onset subs	tracted post-muex	Cu		Store dod	ibiewolu w/ IIII	iniculate onse

362	363	364	365	366	367	368	369	370	371	372	373	374	375
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	1	0	0	0	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	1	0	0	0
1	0	0	0	1	0	1	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	1	0	0	0	1	0	1	1	0	0	1	1	0
1	1	1	1	1	1	1	1	1	1 x		0	0 x	
1	1	1	1	1	1	1	1	1	1 x		0	0 x	
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	1	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	1	1	0	0	0	0	0	1	1	0	0	0
0	0	1	0	0	0	0	0	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	1	1	1	0	1	0	0	1	1	0	0	1
1	0	0	0	1	0	1	0	0	0	0	0	0	0
1	0	0	1	1	0	1	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	1
0	0	1	0	0	0	0	0	0	1	1	0	0	0
0	0	1	0	0	0	0	0	0	0	1	0	0	1
0	1	0	0	0	0	1	0	1	0	0	0	1	0
1	0	0	1	1	1	0	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	0	1
X	1	х	X	X	Х	1	x	1	X	x X		1 x	
x	x	х	x	X	x	x	X	x	x	x X		0 x	
X	X	х	x	X	X	x	X	x	X	χX		0 x	
x	363	х	X	X	x	368	x	370	x	x X		374 X	
t added post-indexe	ed			Store do	oubleword w/ immed	liate offset subs	stract post-indexed			Load Regi	ster offset signe	d byte added	

	376	377	378	379	380	381	382	383	384	385	386	387	388	389
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	0	1	1	0	0	1	1	0	0	1	1	0	0	1
	0	1	1	0	0	1	1	0	0	1	1	0	0	1
X		0	0 x	x		0	0 x		х	0	0 x	х		1
X		0	0 x	x		1	1 x		х	1	1 x	X		1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	0	0	1	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	1	0	0	1	1	0	0	1	1	0
	0	0	0	1	0	0	0	1	0	0	0	1	0	0
	1	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	0	0	1	1	0	0	1	0	0	0	1	1	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	1	1	1	0	1	1	1	0	1	1	1	0	1	1
	1	1	0	1	1	1	0	1	1	1	0	1	1	1
X	X		1 x		x X		1 x		x X		1 x		x Z	
X	X		0 x		x X		0 x		x X		0 x		x Z	
X	X		0 x		хх		0 x		x X		0 x		x Z	
X	X		378 X		хх		382 X		x X		386 X		x Z	
												Calc	.	read
	Load Regist	er offset signe	d byte substract		Load Regi	ster offset sign	ed halfword add		Load Registe	er offset signed h	alfword substract			L

390	391	392	393	394	395	396	397	398	399	400	401	402	403
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	0	1	0	0	0
1	0	1	0	0	0	1	0	1	0	0	1	0	0
1	0	1	0	0	1	1	0	1	0	0	0	0	0
1	0	1	0	0	1	1	0	1	0	0	0	1	1
1 x		1 x	x		1	1 x		1 x		0	0	0	0
1 x		1 x	x		1	1 x		1 x		1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	1	1	0	0	1	0	1	1	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	1	0	1	1	0	0	1	0	1	1	0	0	0
0	1	0	1	0	0	0	1	0	1	0	0	0	0
0	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	1	0	0	0	1	0	1	1	0	0	0
1	0	1	0	0	0	1	0	1	0	0	0	0	1
1	1	1	0	1	1	1	1	1	0	1	1	1	1
0	1	0	1	1	1	0	1	0	1	1	1	1	1
1 x		1 x		хх		1 x		1 x		Х	Х	Х	0
0 x		0 x		x X		0 x		0 x		X	x	x	0
0 x		0 x		x X		0 x		0 x		x	x	Х	0
390 X		392 x		x X		396 X		398 x		x	x	Х	403
wair for MOC Load i	into register		Calc.	read	,	wair for MOC Load	into register						
oad Register offset doub	leword add				Load	Register offset doublev	word substrac	t		Store Reg	ister offset signe	d halfword add	

404	405	406	407	408	409	410	411	412	413	414	415	416	417
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	1	1	1	1	0	0	1	1
0	0	0	0	1	1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1	1	1	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	0	0	0	0	1	1	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
0	1	0	0	0	1	0	0	0	0	0	1	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	1	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	1	1	1	1	1	1	0	1	1	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1	1	1
X	X	X	0	X	X	X	1	X	0	X	X	Х	1
Х	x	X	0	Х	Х	x	0	х	0	x	x	Х	0
X	X	X	0	X	Х	Х	0	X	0	X	X	Х	0
X	X	X	407	X	X	X	411	X	413	X	X	X	417
Store Regist	ter offset signed l	nalfword substract		Store Reg	gister offset s	doubleword add					Store Reg	ister offset dou	bleword subtr

	418	419	420	421	422	423	424	425	426	427	428	429	430	431
	0	0	0	0	0	1	0	0	0	1	0	0	0	1
	0	0	1	0	0	1	1	0	0	1	1	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	0	0	0	0	1	0	0	0	1	0	0	0	1	0
	0	0	0	1	1	0	0	1	1	0	0	1	1	0
	1	1	0	1	1	0	0	1	1	0	0	1	1	0
	1	1 x	(0	0	x	x	0	0	X	x	0	0	x
	1	1 x	(0	0	x	x	0	0	x	x	1	1	x
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	1	0	0	0	1	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	0	0	1	1	0	0	1	1	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	1	0	0	1	1	0	0	1	1	0	0	1
	0	0	0	0	0	1	0	0	0	1	0	0	0	1
	0	0	1	0	0	0	1	0	0	0	1	0	0	0
	0	0	1	0	0	1	0	0	0	1	1	0	0	1
	0	1	0	0	1	0	0	0	1	0	0	0	1	0
	1	1	1	1	1	0	1	1	1	0	1	1	1	0
	1	1	1	1	0	1	1	1	0	1	1	1	0	1
	X	0	Х	X	1	Х	X	X	1	Х	Х	Х	1	X
	х	0	х		0	х	x	х		×	х	Х		х
	X	0	Х	X	0	х	X	X		Х	Х	Х		Х
	X	419	X	X	422	х	X	X	426	Х	Х	Х	430	X
act			Load signed	Byte w/ Registe	er offset added p	re-indexed	Load signed	Byte w/ Register	offset substract	pre-indexed	Load signed ha	alfword w/ Reg	ister offset added	I pre-indexed

432	433	434	435	436	437	438	439	440	441	442	443	444	445
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	1	0	0	0	1	0	1	0	0	0	1	0
0	1	1	0	0	1	1	0	1	0	0	1	1	0
0	1	1	0	0	1	1	0	1	0	0	1	1	0
X	0	0	х	X	1	1	1	1	1	X	1	1	1
x	1	1	х	х	1	1	1	1	1	x	1	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	1	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	0	0	1	0	1	1	0	0	1
0	0	0	1	0	0	0	1	0	1	0	0	0	1
1	0	0	0	1	0	0	0	0	0	1	0	0	0
0	0	0	1	1	0	0	1	0	1	0	0	0	1
0	0	1	0	0	0	1	0	1	0	0	0	1	0
1	1	1	0	1	1	1	0	1	0	1	1	1	1
1	1	0	1	1	1	0	1	0	1	1	1	•	1
X	X	1	Х	Х	X	1	X	1	Х	X	Х		X
X	X	0	X	X	Х	0	X	0	X	Х			
X	X	0	х	Х	Х	0	Х	0	Х	х			
X	X	434	Х	Х	X	438	Х	440	Х	х	Х	444	X
	If we and work Dominis	6 5 1	-4							Calc.	Read	Wait for MOC	Load Byte into Ro
Load signed hal	lfword w/ Register	offset substra	ict pre-indexed		Load doubley	wora w/ Registe	er offset added p	re-indexed		Load double	word w/ Registe	er offset substrac	t pre-indexed

446	6 4	47	448	449	450	451	452	453	454	455	456	457	458	459
(0	1	0	0	0	0	0	0	0	0	0	0	0	0
(ס	1	1	0	0	0	1	0	0	0	1	0	0	0
(ס	0	0	0	0	0	0	0	0	0	0	0	0	0
(ס	0	1	0	0	0	1	0	0	0	1	0	0	0
•	1	0	0	1	0	0	0	1	0	0	0	1	0	0
•	1	0	0	0	0	0	0	0	0	0	0	0	0	0
•	1	0	0	0	1	1	0	0	1	1	0	0	1	1
•	1	1	0	0	0	0	0	0	0	0	1	1	1	1
•	1	1	1	1	1	1	1	1	1	1	1	1	1	1
(ס	0	0	0	0	0	0	0	0	0	0	0	0	0
(ס	0	0	1	0	0	0	1	0	0	0	1	0	0
(ס	1	0	0	0	0	0	0	0	0	0	0	0	0
(ס	0	0	0	0	0	0	0	0	0	0	0	0	0
(ס	0	0	0	0	0	0	0	0	0	0	0	0	0
(ס	0	0	0	0	0	0	0	0	0	0	0	0	0
(ס	1	1	1	0	0	1	1	0	0	1	1	0	0
(ס	0	0	1	0	0	0	1	0	0	0	1	0	0
(ס	0	0	1	0	0	0	1	0	0	0	1	0	0
(ס	1	1	0	0	0	1	0	0	0	1	0	0	0
(ס	1	0	0	0	0	0	0	0	0	0	0	0	0
(ס	0	1	0	0	0	1	0	0	0	1	0	0	0
(ס	1	1	0	0	0	0	0	0	0	1	0	0	0
•	1	0	0	0	0	1	0	0	0	1	0	0	0	1
•	1	0	1	1	1	1	1	1	1	1	1	1	1	0
(0	1	1	1	1	1	1	1	1	1	1	1	1	1
•	1 x		x	х	x	0	x	x	х	0	х	х	x	1
(0 x		x	x	x	0	x	x	x	0	x	х	x	0
(0 x		x	x	X	0	X	x	x	0	х	Х	X	0
446	6 x		x	x	x	451	x	x	x	455	х	х	x	459
egister														
			Store signed h	nalfword w/ Regi	ster offset added	d pre-indexed	Store signed ha	alfword w/ Regis	ter offset substra	act pre-indexed		Store doubl	leword w/ Registe	er offset added p

460	461	462	463	464	465	466	467	468	469	470	471	472	473
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	1	0	0	0	0	1
0	0	0	1	0	0	0	0	0	0	1	0	0	0
0	0	0	0	0	0	0	0	0	1	1	0	0	0
1	1	0	0	1	1	1	1	0	1	1	0	0	0
1	1	1	1	1	1	1	1	x	0	0	x	x	x
1	1	1	1	1	1	1	1	x	0	0	х	x	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0	1	0	0	1	1	1
0	0	0	1	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	0	0	0	0	0	0	0	0	0	0	1	0	0
0	0	1	0	0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	1	1	0
0	1	0	0	0	1	0	1	0	0	1	0	0	0
1	1	1	1	1	0	1	1	1	1	1	1	0	1
1	1	1	1	1	1	1	1	1	1	0	1	1	1
X	0	X	X	X	1	X	X	X	X	1	X	х	X
X	0	х	Х	x	0	х	х	x	Х	0	х	х	Х
X	0	X	Х	X		х		х	х		х	х	X
X	461	X	X	Х	465	X	467	X	х	470	Х	х	X
ore-indexed			Store doublewo	ord w/ Register	offset subtracted	pre-indexed		Loa	d signed byte ar	nd Add Register	offset post-inde	xed	Load s

474	475	476	477	478	479	480	481	482	483	484	485	486	487
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
1	1	0	0	0	1	1	0	0	0	1	1	0	0
0	0 x	x	x		0	0 x	x		x	0	0	x	x
0	0 x	x	x		1	1 x	x		x	1	1	x	x
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	1	1	1	0	0	1	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	0	1	0	0	0	0
0	0	1	1	0	0	0	1	1	0	0	0	1	1
0	0	1	0	0	0	0	1	0	0	0	0	1	0
0	0	0	1	0	0	0	0	1	0	0	0	0	1
0	0	1	0	0	0	0	1	1	0	0	0	1	0
0	1	0	0	0	0	1	0	0	0	0	1	0	0
1	1	1	0	1	1	1	1	0	1	1	1	1	0
1	0	1	1	1	1	0	1	1	1	1	0	1	1
X	1	X	Х	X	X	1	X	Х	X	X	1	X	X
X	0	X	X	X	X	0	X	Х	X	X	0	X	X
X	0	X	X	X	Х	0	X	Х	Х	X	0	X	X
X	475	X	Х	X	X	480	X	Х	X	X	485	Х	X
Simple and hearth and the	b.atma.at D.a. viata a			1 1	in and balfered at	A al al D = -:!-1		J	1		l acchatus et D	-t	
signed byte and s	ubstract Register offs	set post-indexed		Load si	igned naitword and	Add Register	offset post-indexed	ם ا	Load sigi	ned halfword and	substract Regi	ster offset post-i	naexea

	488	489	490	491	492	493	494	495	496	497	498	499	500	501
	0	0	0	1	0	1	0	0	0	0	1	0	1	0
	0	0	0	1	0	1	1	0	0	0	1	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	0	0	1	0	0	0	0	0	0
	0	0	1	0	1	0	0	0	0	1	0	1	0	0
	0	1	1	0	1	0	0	0	1	1	0	1	0	0
	0	1	1	0	1	0	0	0	1	1	0	1	0	0
x		1	1	1	1	1 x	x		1	1 x		1 x	x	
x		1	1	1	1	1 x	x		1	1 x		1 x	x	
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	1	0	1	0	0	0	0	1	0	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	1	0	0	0	0	0	0	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	1	0	1	1	1	0	0	1	0	1	1
	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	0	0	0	0	0	0	1	0	0	0	0	0	0
	0	0	0	1	0	1	1	0	0	0	1	0	1	1
	0	0	0	1	0	1	0	0	0	0	1	0	1	0
	0	0	0	0	0	0	1	0	0	0	0	0	0	1
	0	0	0	1	0	1	1	0	0	0	1	0	1	0
	0	0	1	0	1	0	0	0	0	1	0	1	0	0
	1	1	1	1	1	1	0	1	1	1	1	1	1	0
	1	1	0	1	0	1	1	1	1	0	1	0	1	1
	х	х	1	x	1	Х	х	X	Х	1	x	1	х	X
	X	x	0	x	0	x	x	X	x	0	X	0	x	x
	X	x	0	x	0	х	x	X	x	0	x	0	x	X
	Х	х	490	x	492	Х	х	X	x	497	x	499	x	Х
	Load do	ubleword and Ad	ld Register offset	t post-indexed				Load d	oubleword and subs	stract Register offs	et post-indexed	t		

502	503	504	505	506	507	508	509	510	511	512	513	514	515	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	1	0	0	0	0	1	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
1	0	0	0	0	1	0	0	0	0	1	0	0	0	
0	1	0	0	0	0	1	0	0	0	0	1	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	1	1	0	0	0	1	1	0	0	0	1	1	
0	0	0	0	0	0	0	0	0	0	1	1	1	1	
1	1	1	1	1	1	1	1	1	1	1	1	1	1	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	1	0	0	0	0	1	0	0	0	0	1	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	1	0	0	0	0	1	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
1	1	0	0	1	1	1	0	0	1	1	1	0	0	
0	1	0	0	0	0	1	0	0	0	0	1	0	0	
1	1	0	0	0	1	1	0	0	0	1	1	0	0	
0	0	0	0	1	0	0	0	0	1	0	0	0	0	
0	0	0	0	0	0	0	0	0	0	0	0	0	0	
0	0	0	0	1	0	0	0	0	1	0	0	0	0	
0	0	0	0	1	0	0	0	0	0	0	0	0	0	
0	0	0	1	0	0	0	0	1	0	0	0	0	1	
1	1	1	0	0	1	1	1	0	0	1	1	1	0	
1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Х	X	X	1	X	Х	Х	X	1	X	X	х	X	1	
x	x	x	0	Х	x	x	Х	0	x	x	x	х	Х	
х	x	x	0	Х	х	х	X	0	x	x	х	Х	Х	
х	х	x	505	Х	х	x	Х	510	х	х	х	Х	515	
Store signe	ed halfword w/ Re	egister offset add	ed post-indexed		Store signe	tore signed halfword w/ Register offset substracted post-indexed				Store doubleword w/ Register offset a				

516	517	518	519	520	521	522	523	524	525	526	527	528	529
0	0	0	0	0	0	0	0	0	0	,	1	1	1
0	0	1	0	0	0	0	0	0	1	(C	0	0
0	0	0	0	0	0	0	0	0	0	(C	0	0
0	0	0	1	0	0	0	0	0	0	(C	0	0
1	0	0	0	1	0	1	0	0	0	(C	0	0
0	0	0	0	0	0	0	0	0	0	(C	0	0
0	1	0	0	0	1	0	1	1	0	(C	0	0
1	1	1	1	1	1	1	1	1	1	x	x	x	x
1	1	1	1	1	1	1	1	1	1	x	x	x	X
0	0	0	0	0	0	0	0	0	0	(C	0	0
1	0	0	0	1	0	1	0	0	0	(C	0	0
0	0	0	0	0	0	0	0	0	0	(C	0	0
0	0	0	0	0	0	0	0	0	0		1	0	1
0	0	1	0	0	0	0	0	0	1	(C	0	0
0	0	0	0	0	0	0	0	0	0	(C	0	0
1	0	1	1	1	0	1	0	0	1	(C	0	0
1	0	0	0	1	0	1	0	0	0	(C	0	0
1	0	0	1	1	0	1	0	0	0	(C	0	0
0	0	1	0	0	0	0	0	0	1	(C	0	0
0	0	0	0	0	0	0	0	0	0	(C	0	0
0	0	1	0	0	0	0	0	0	1	(C	0	0
0	0	1	0	0	0	0	0	0	0	(C	0	0
0	1	0	0	0	0	1	0	1	0	(C	0	0
1	0	0	1	1	1	0	1	0	0	(0	0	0
1	1	1	1	1	1	1	1	1	1		1	1	1
X	1	Х	X	X	X	1	X	1	X		X	X	X
X	X	Х	X	X	X	X	X	Х	X	x	X	X	х
X	X	Х	X	X	X	X	X	X	X	x	X	X	X
X	517	Х	X	X	X	522	X	524	X	X	X	X	Х
										CMP(shift)	CMP(Immediate	e) TST(R-R)	TST(shift)
added post-indexed	dded post-indexed Store doubleword w/ Register offset substract post-indexed												

530	53	532	533	534	535	536	537	538	539	540	541	542
1		1 1	1	1	1	1	0	0	0	0	1	1
0		0 0		0	0	0	1	1	0	0	1	1
0		0 0		0	0	0	0	0	0	0	0	0
0		0 0		0	0	0	0	0	0	0	0	0
0		0 0	C	0	0	0	0	0	0	0	0	0
0		0 0	0	0	0	0	0	0	0	0	0	0
0		0 0	C	0	0	0	0	0	0	0	0	0
x	x	x	x	x	x	x	x	x	x	x	x	x
x	x	x	x	х	x	x	x	x	x	x	х	x
0		0 0	C	0	0	0	0	1	0	0	0	0
0		0 0	O	0	0	0	0	0	0	0	0	0
0		0 0	0	0	0	0	0	0	0	0	0	0
1		0 1	1	0	1	1	1	1	1	1	0	1
0		0 0	0	0	0	0	0	1	0	0	0	0
0		0 0	0	0	0	0	1	1	0	0	0	0
0		0 0	0	0	0	0	1	1	0	0	0	0
0		0 0	0	0	0	0	0	0	0	0	0	0
0		0 0	0	0	0	0	0	1	0	0	0	0
0		0 0	0	0	0	0	0	0	0	0	0	0
0		0 0	0	0	0	0	1	0	0	0	0	0
0		0 0	0	0	0	0	0	0	0	0	0	0
0		0 0							0			
0		0 0						0	_			
0		0 0		0	0	0	0	1	0	0	0	0
1		1 1	1	1	1	1	1	0		1	1	1
X	X	X	X	X	X	X	X	X	X	X		X
X	X	X	X	X	X	X	X	X	X	X	X	X
X	X	X	X	X	X	X	X	X	X	X	X	X
X	X	X	X	X	X	X	X	576	X	X	X	X
TCT/Immadiata\	TEO(D D)	TEO(abiff)	TEO/Immodiate) CMN/P P)	CMN(abift)	CMN/Immodiate	DEO.	DAI	MOV//Immadiata\	MOV(Chiff)	Add/Sub/D D\C	Add/Cub/Cb:#\C
TST(Immediate)	IEQ(K-K)	TEQ(shift)	TEQ(Immediate	(K-K)	CMN(shift)	CMN(Immediate	DEW	BAL	MOV(Immediate)	wov(Silit)		Add/Sub(Shift)S
											The operation pe	erformed depends

543	544	545	546	547	548	549	550	551	552
1	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
0	0	0	1	0	0	0	1	0	0
0	0	1	1	0	0	1	1	0	0
0	0	1	1	0	0	1	1	0	0
x	х	х	x	X	X	x	x x		x
x	х	х	x	x	X	x	x x		x
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	0	0	0	1	0
1	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	1	0	0	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0
0	1	0	0	1	1	0	0	1	1
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	1	1	0	0	0	0
0	0	0	1	1	0	0	1	1	0
0	1	1	0	1	1	1	0	1	1
1	1	1	1	1	1	1	1	1	1
X	Х	Х	1	0		X	1	0	
X		X	0	1		x	0	1	
X		X	0	0		X	0	0	
X	Х	X	546	544	X	X	550	548	X
Add/Sub(Immediate)S									
on the Inst. Register	Multiple Load w/ IA(increment after)				Multiple Load w/ IB(increment before)				

	553	554	555	556	557	558	559	560	561	562	
	0	0	0	0	0	0	0	0	0	0	
	0	0	1	0	0	0	1	0	0	0	
	0	0	0	0	0	0	0	0	0	0	
	0	0	0	1	0	0	0	1	0	0	
	1	0	0	0	1	0	0	0	0	1	
	0	0	0	0	0	0	0	0	1	1	
	0	1	1	0	0	1	1	0	1	1	
	X	х	х	Х	x	x	x x		x	x	
	Х	х	х	х	х	x	x x		x	X	
	0	0	0	0	0	0	0	0	0	0	
	1	0	0	0	1	0	0	0	0	0	
	0	0	0	0	0	0	0	0	0	0	
	0	0	0	1	0	0	0	1	0	0	
	0	0	0	0	0	0	0	0	0	0	
	0	0	0	0	0	0	0	0	0	0	
	1	0	0	1	1	0	0	1	0	0	
	1	0	0	0	1	0	0	0	0	0	
	1	0	1	1	1	0	1	0	0	0	
	0	0	0	0	0	0	0	1	0	0	
	0	0	0	0	0	0	0	0	0	0	
	0	0	0	0	0	0	0	1	0	0	
	0	0	1	1	0	0	0	0	0	0	
	0	1	1	0	0	1	1	0	0	1	
	1	1	1	1	1	1	1	1	1	0	
	1	0	1	1	1	0		1	1	1	
X		Х		X	X	X	1 x		(1	
X		X	0		X	X	0 x			0	
X		X	0		X	X	0 x		(0	
X		Х	552	X	Х	X	556 x	>	(562	
8.6 1.1	diala Cha (1)	A / I			Maritim Co. / 17	\(\begin{align*} \text{Viscours at 1 f}					
Multiple Store w/ IA(Increment After)					Multiple Store w/ I		Multiple Load w/ DA(decrement after)				

563	564	565	566	567	568	569	570	571	572
0	0	0	0	0	0	0	0	0	0
1	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
0	0	0	1	0	0	1	0	0	0
0	0	1	1	0	0	0	0	0	0
0	0	1	1	0	0	0	1	1	0
x	x	X	X	x	X	Х	X	x	1
x	x	X	X	X	X	х	X	х	0
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	1	0	0	0
1	0	0	0	1	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0
0	0	0	0	0	0	0	0	0	0
1	1	0	0	1	1	1	0	0	1
0	0	0	0	0	0	1	0	0	0
1	0	0	0	1	0	1	0	1	0
0	1	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	1
1	0	0	0	0	0	0	0	1	0
1	0	0	1	1	0	0	1	1	0
1	1	1	0	1	1	1	1	1	1
1	1	1	1	1	1	1	0	1	1
0		Х	1	0		X X		1	
		X	0			x x		0	
		X	0			x x		0	
560	X	Х	566	564	X	X X		568	X
		Marking 1 / 22	/damananti f			National Company	(4		
		Multiple Load w/ DB	(decrement before)		Multiple Store w/ DA(decrement After)				

573	574	575	576
0	0	0	0
0	0	1	1
0	0	0	0
0	0	0	0
1	0	0	0
0	0	0	0
0	1	1	0
1	1	1	х
0	0	0	х
0	0	0	1
1	0	0	0
0	0	0	0
0	0	0	1
0	0	0	0
0	0	0	1
1	0	0	1
1	0	0	0
1	0	1	0
0	0	0	0
0	0	0	1
0	0	0	0
0	0	0	0
0	1	1	0
1	1	1	0
1	0	1	1
X	X	1	X
X	Х		X
X	X		X
X	X	572	1
Multiple Store w/ D			