Curso: "SIMATIC Step7 S7-300 Nivel 1"

Unidad 2

"Semana 2"

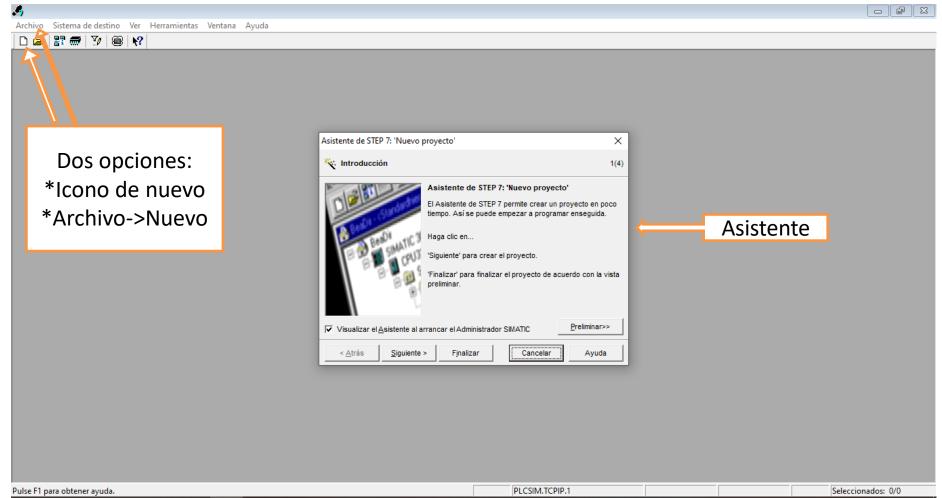
Instructor: D.Zarate Guillermo





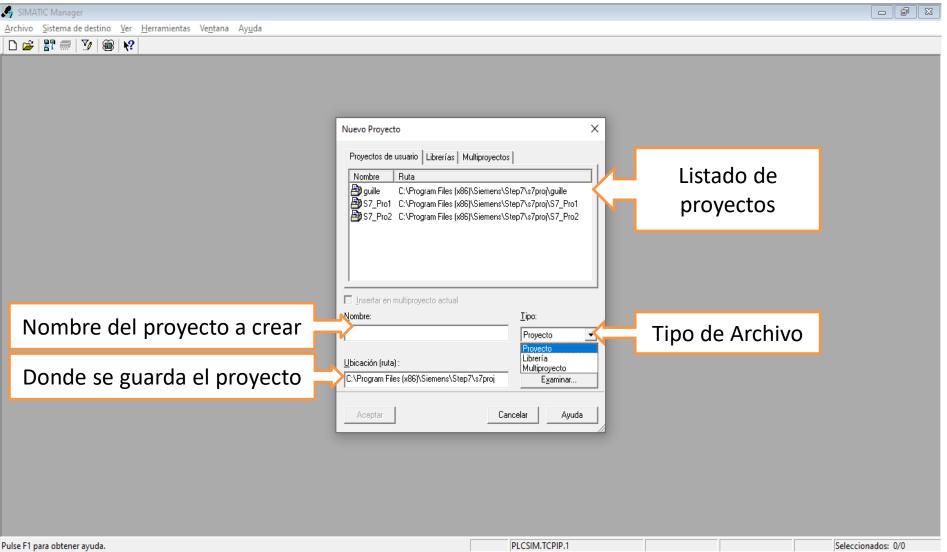
Comienzo de STEP7

Si se comienza nuevo proyecto, hay dos manera de hacerlo: una con el asistente y la otra manualmente.



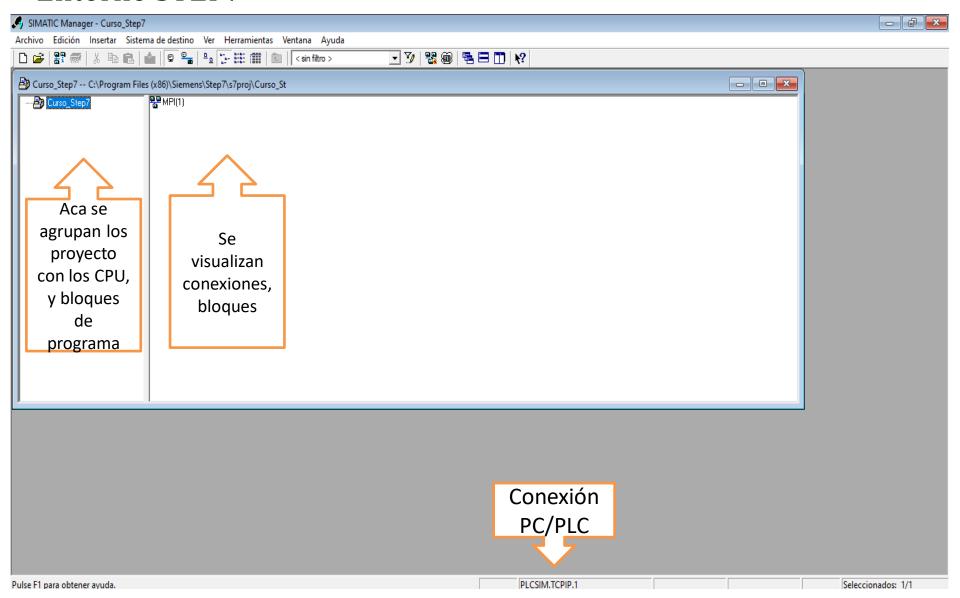


Nuevo proyecto en forma manual



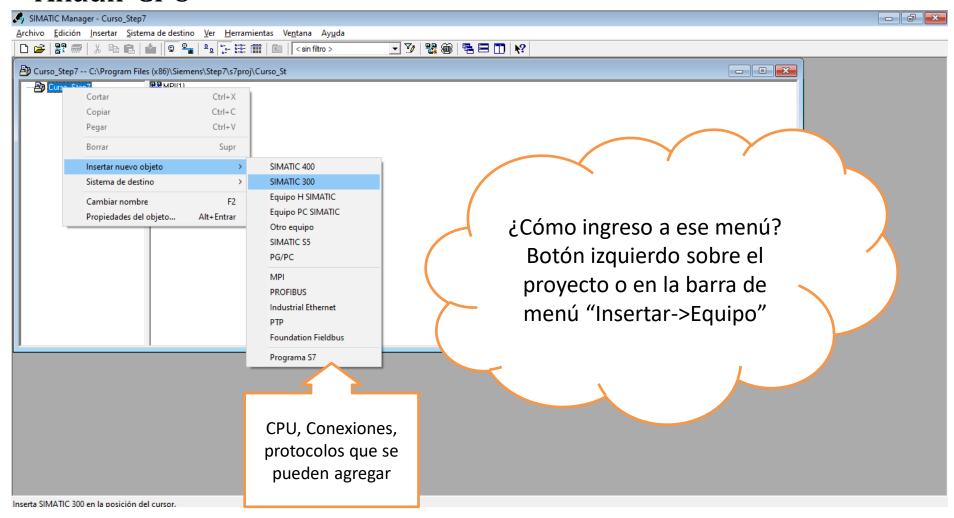


Entorno STEP7



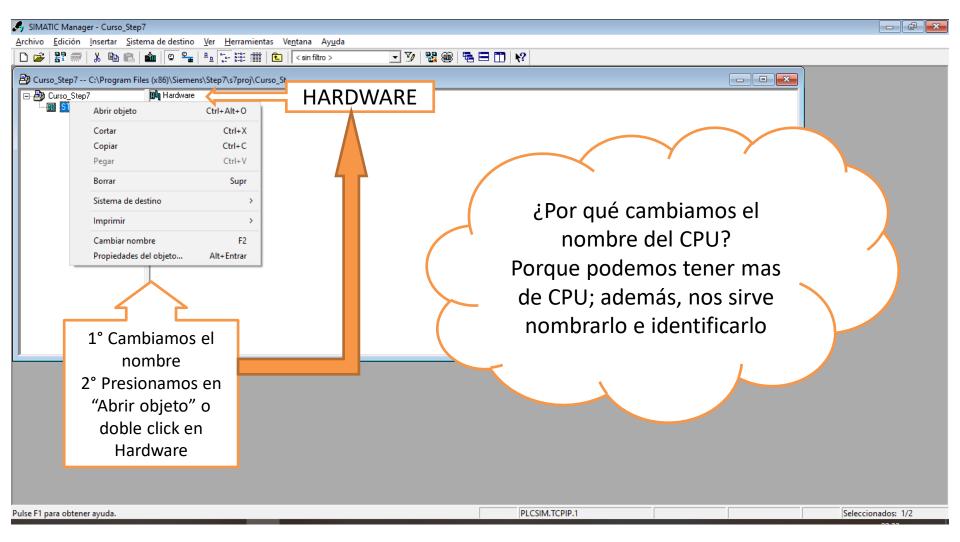


Añadir CPU

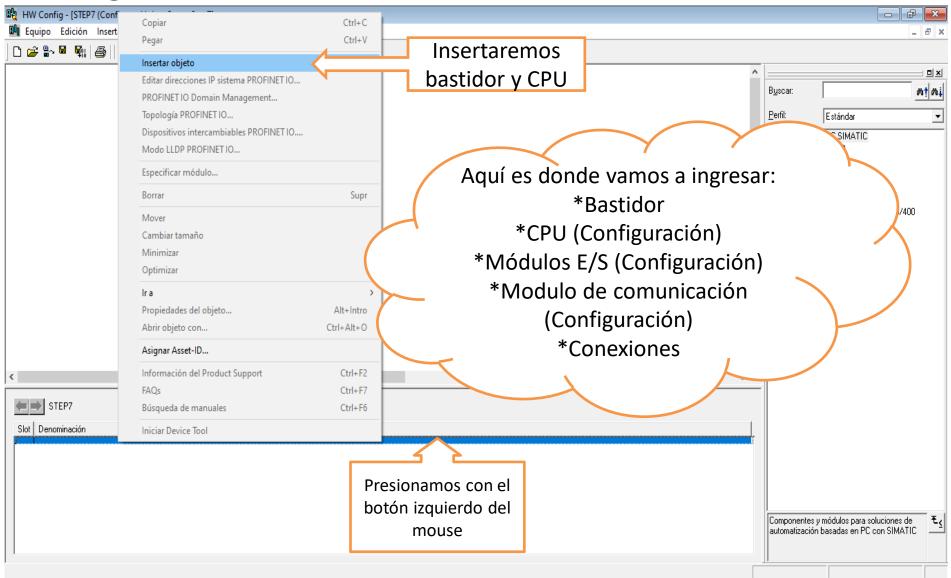




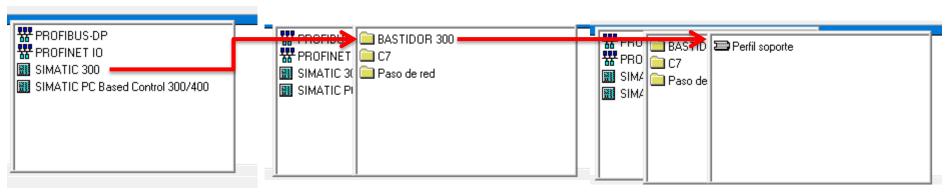
Añadir CPU



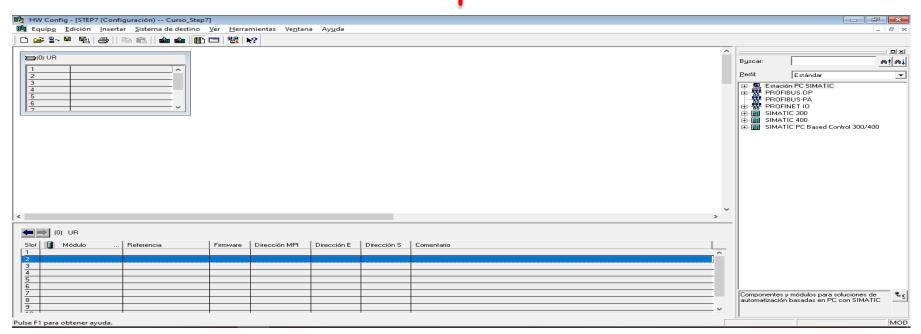




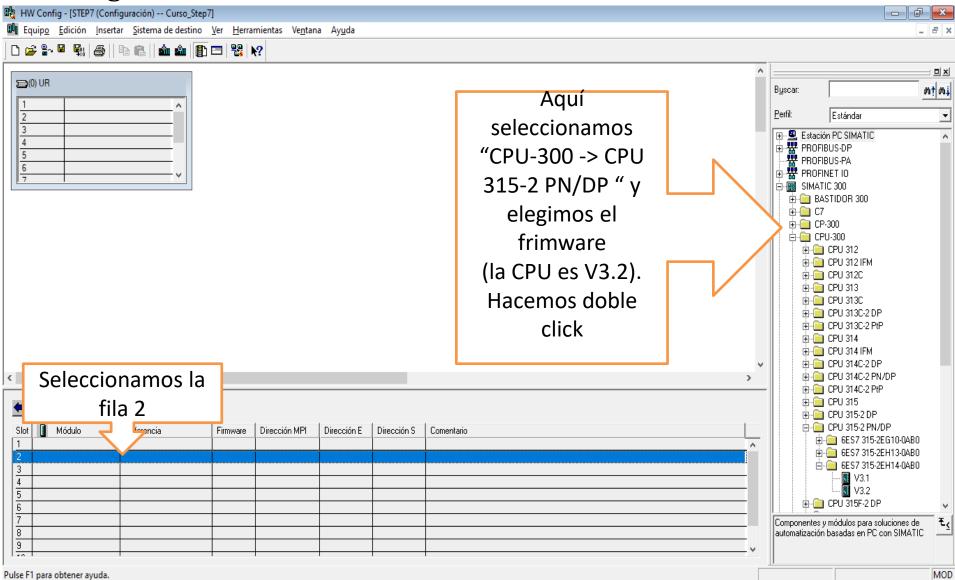




Al elegir el bastidor, ya podemos agregar el CPU

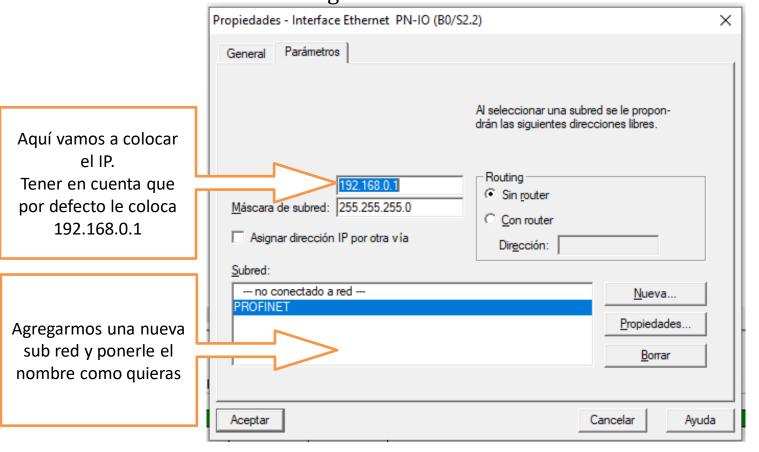




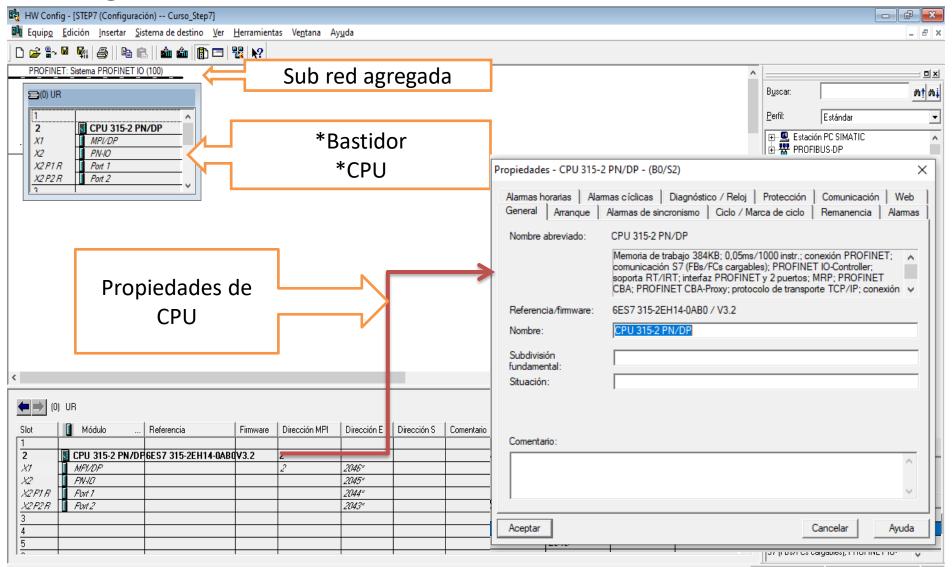




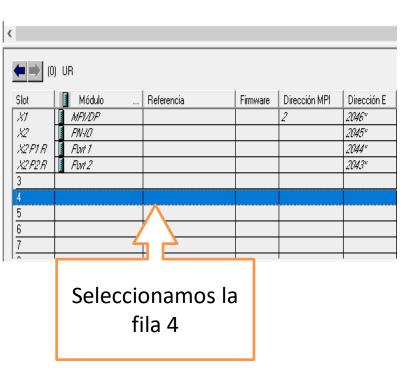
*Configuración Dirección IP *Configuración Subred

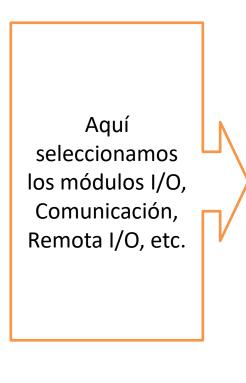


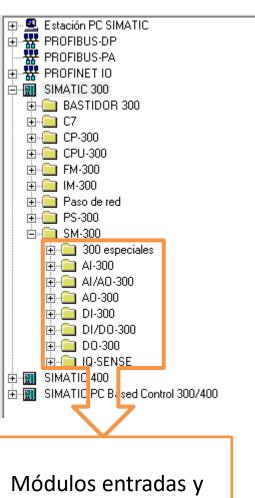








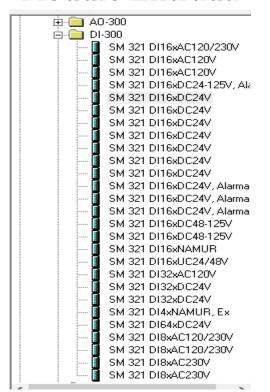




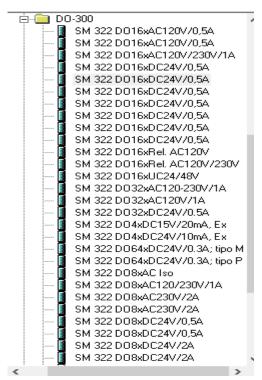
salidas



Configuración de Hardware Módulo Entrada



Módulo Salida



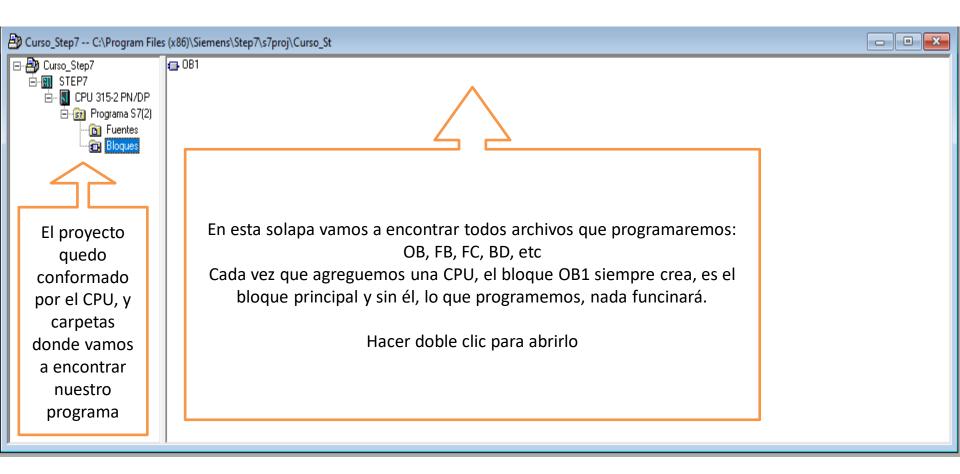
| X7 | | MFI/DF | | 2 | 2046" | | |
|-------|---|-----------------|---------------------|---|-------|----|---|
| X2 [| 1 | FN-10 | | | 2045" | | |
| X2F1R | 1 | Fort 1 | | | 2044" | | |
| X2F2R | | Port 2 | | | 2043" | | |
| 3 | | | | | | | |
| 4 | 1 | DI16xDC24V | 6ES7 321-1BH00-QAA0 | | 01 | | |
| 5 | 1 | D016xDC24V/0.5A | 6ES7 322-1BH01-0AA0 | | | 45 | < |
| 6 | | | | | | | ı |
| | | | | | | | |

GUARDAMOS Y SALIMOS

Se agregaron dos módulo, uno de entrada y otro de salida de 16 bit. También se podrán ver las direcciones

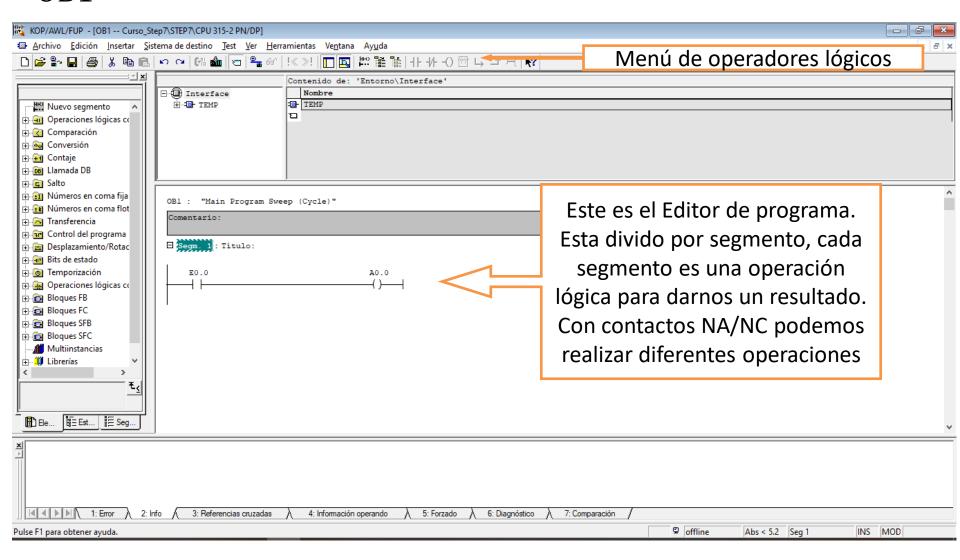


Proyecto creado



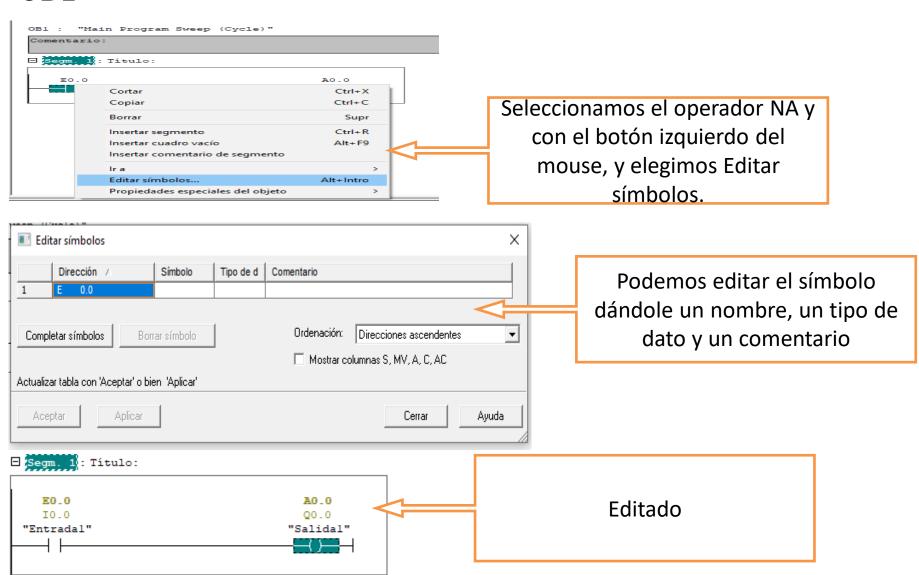


OB₁

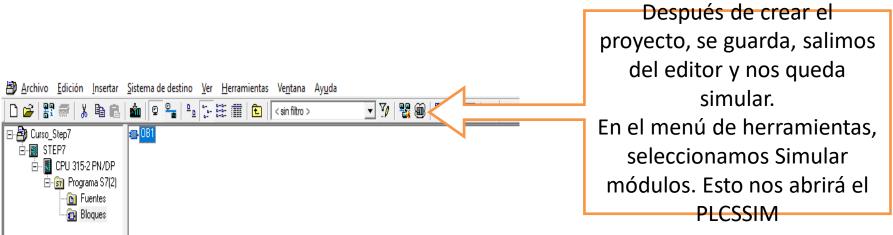


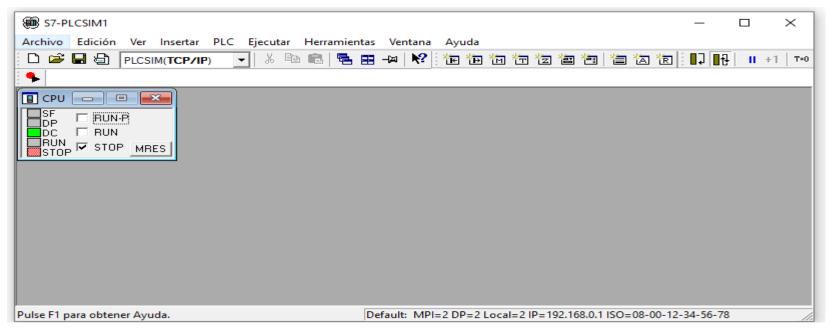


OB₁

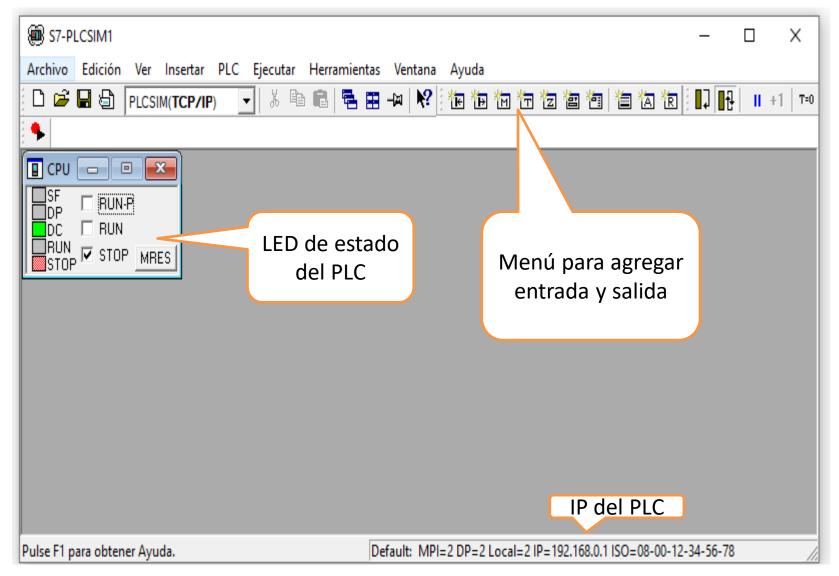












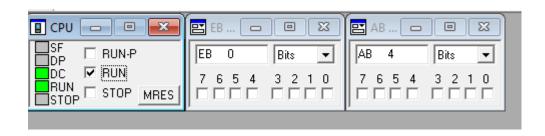




Mientras en PLCSIM este en STOP, presionamos este botón para cargar la configuración que hemos hecho



En el PLCSIM agregamos byte de entradas y uno de salida, y lo ponemos en RUN.
Entrada EBO y AB4.



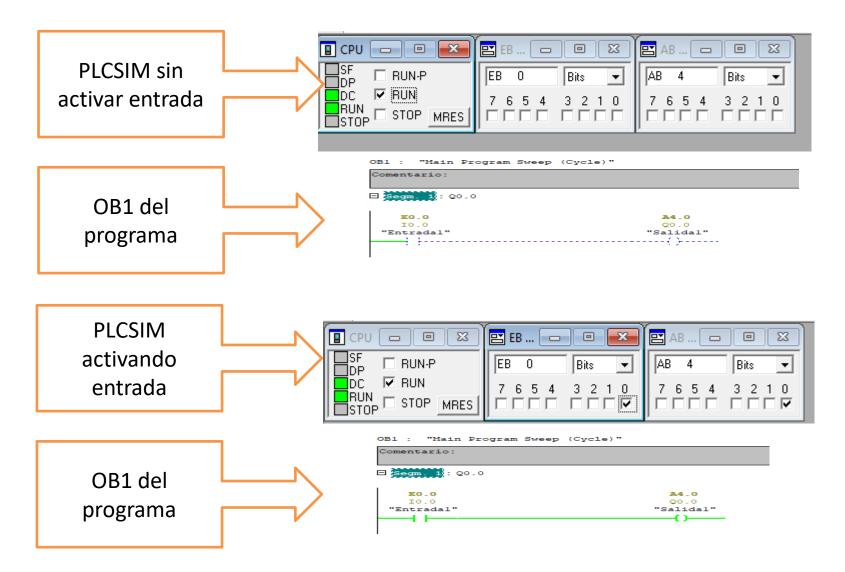


| DI16xDC24V | 6ES7 321-1BH00-0AA0 | | 01 | | |
|-----------------|---------------------|--|----|----|--|
| D016xDC24V/0.5A | 6ES7 322-1BH01-0AA0 | | | 45 | |
| | | | | | |

Cuando configuramos los módulos I/O,
Por defecto se le asigno
I 0...1
O 4...5
Se puede volver a cambiarlo, pero ojo
también cambiar en el programa









Operaciones booleana

| Norma Función | Nemónicos | Boole | DIN-40713-6 (relés) | NEMA (contactos) | Símbolos lógicos | Operadores lógicos UNE-20-004-75 (XVI) |
|---------------------|-----------|-------|------------------------|---------------------|---------------------|--|
| Y (Serie) | AND | • | | | А — — F | A—&—F |
| O (Paralelo) | OR | + | -[-]- | -다누 | A → F | A — ≥ F |
| Complemen- taria | NOT | ā | | | a- ├ ≫-ã | aō ā |
| Exclusiva | XOR | • | | 414 | A D-F | A & F |