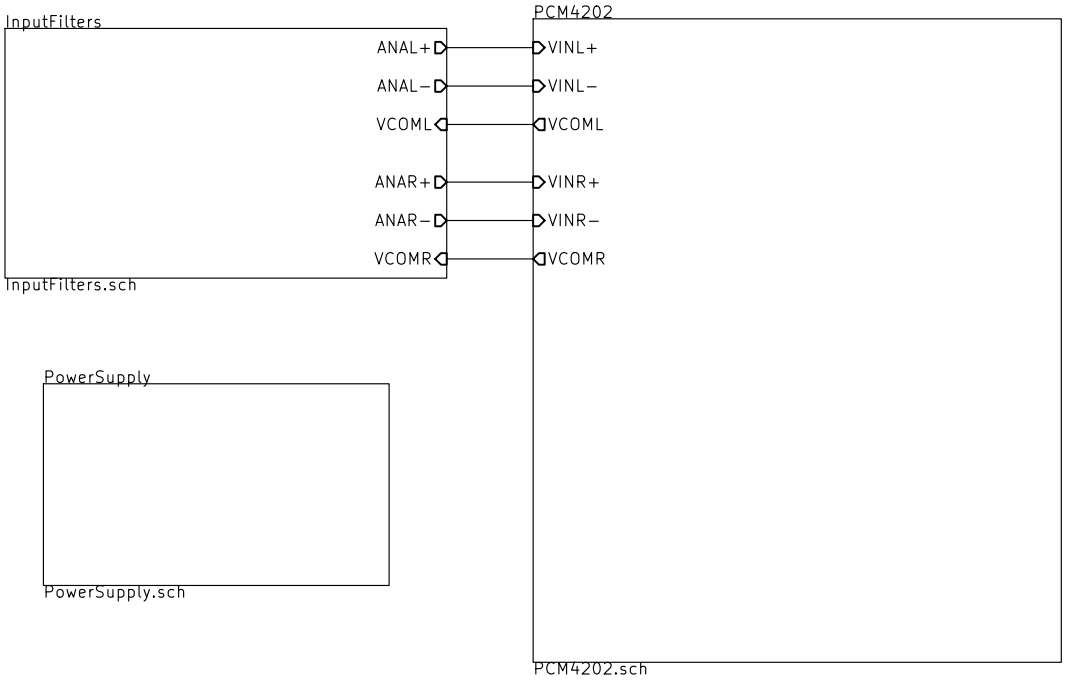
Open Hardware DSP Platform – www.ohdsp.org
PCM4202 ADC – Differential Inputs
Revision 2.0



Notes:

All digital I/O is 3V3. Use outside this voltage can cause damage.

See bill of materials for detailed parts information.

Trace impedance on MCLK and I2S is designed for approx 89ohm.
26AWG ribbon cable used with Ground–Signal–Ground is approx 89ohm.

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File: ADC–PCM4202.sch

Title: PCM4202 ADC – Differential Inputs

Size: A4	Date: 2016–07–01	**Rev: 2.0**
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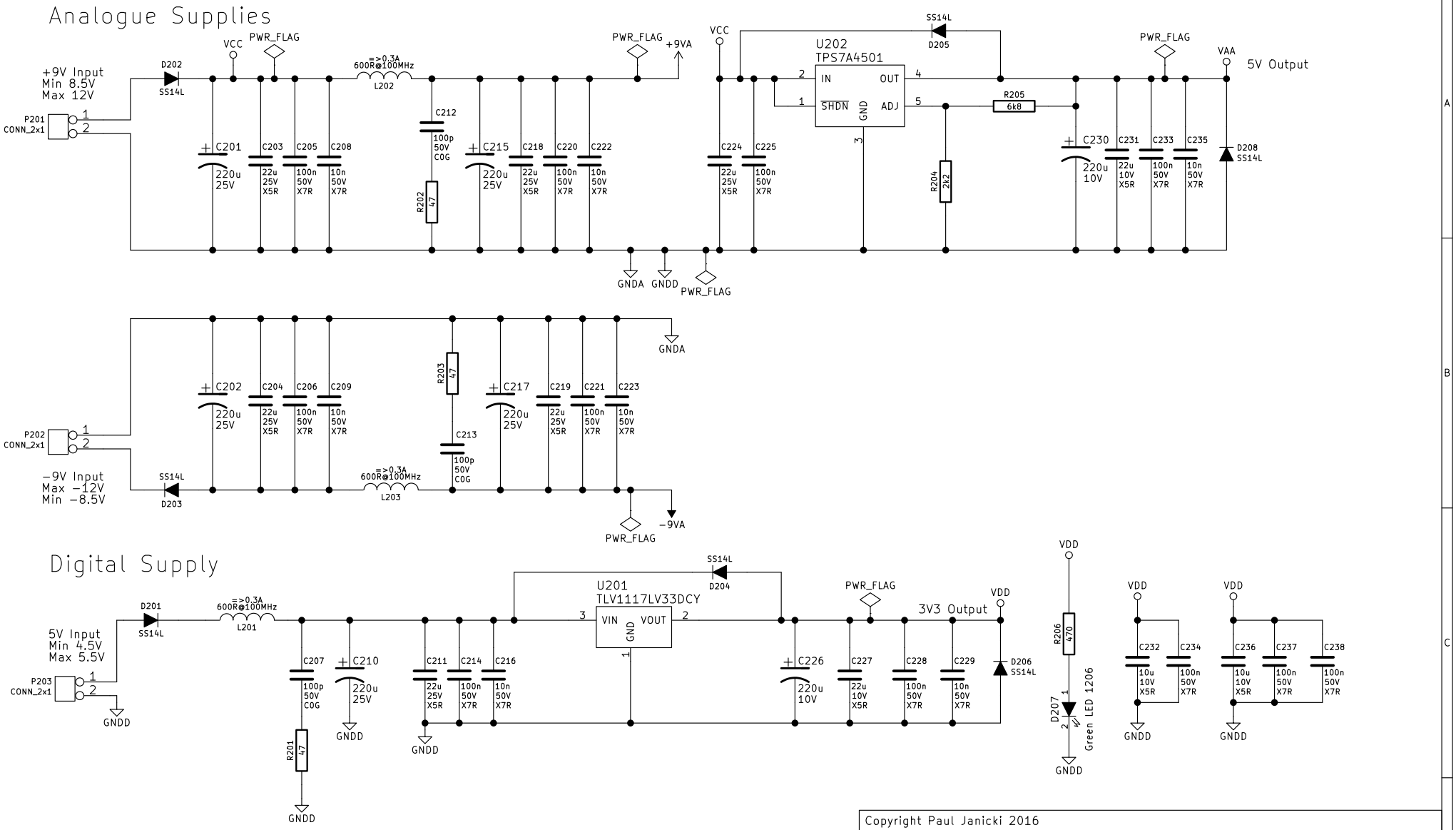

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File: PowerSupply.sch

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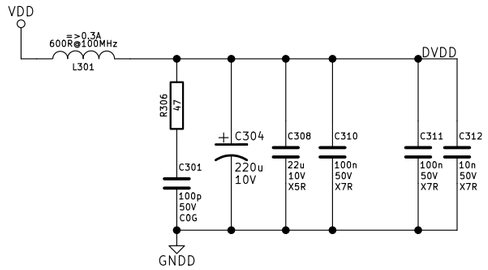
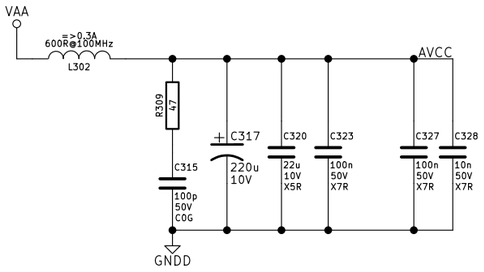
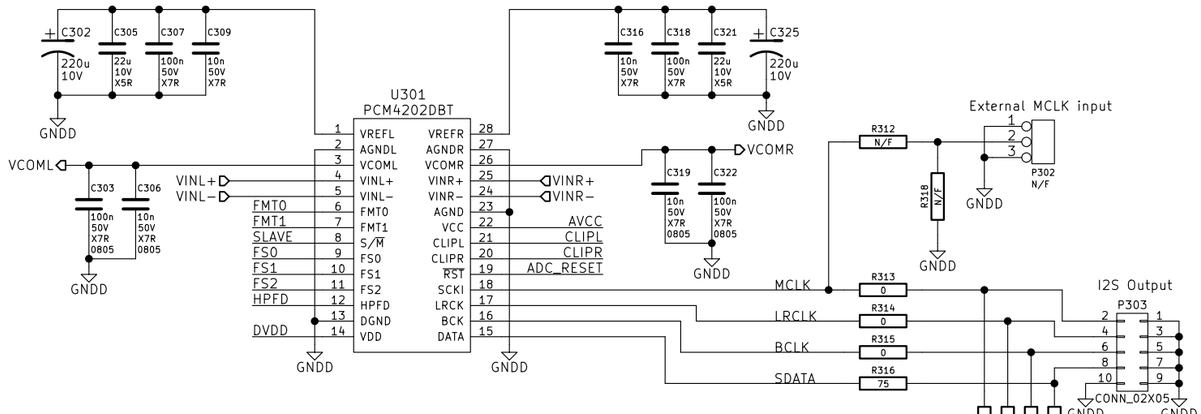
PCM4202 ADC





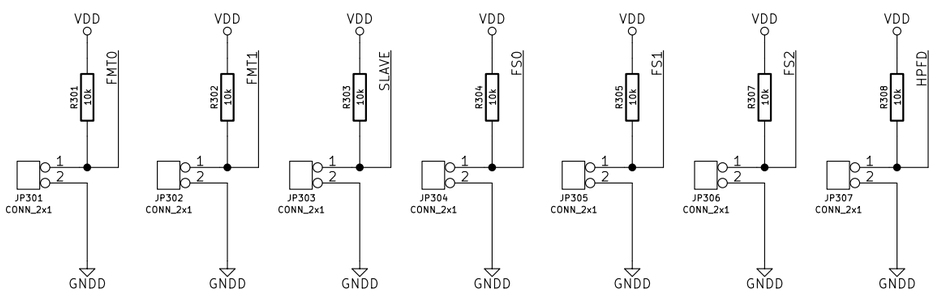


Note

Value of R314 and R315
75R if PCM4202 set as master
0R if PCM4202 set as slave

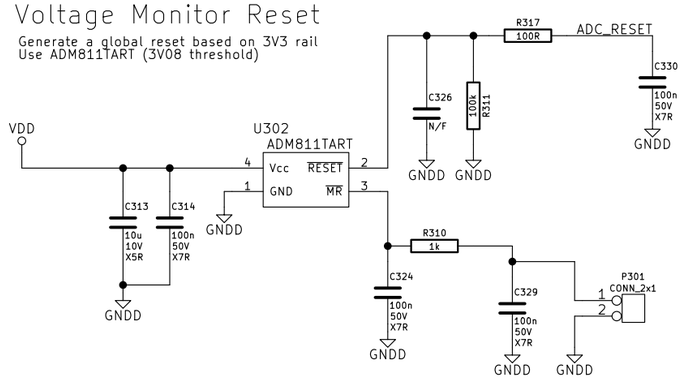
Configuration Pins

Refer to datasheet for information on configuration

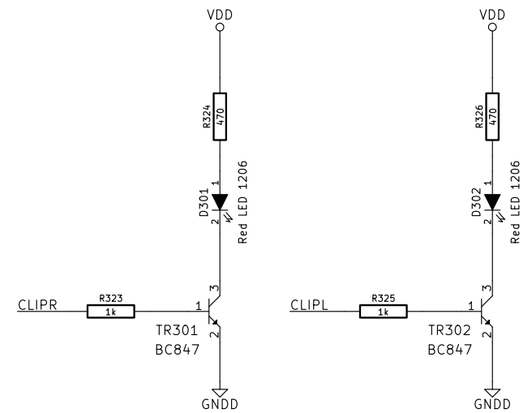


Voltage Monitor Reset

Generate a global reset based on 3V3 rail
Use ADM811TART (3V08 threshold)



Left/Right Clipping Indicators



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File: PCM4202.sch

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Size: A3	Date: 2016-07-01	**Rev: 2.0**
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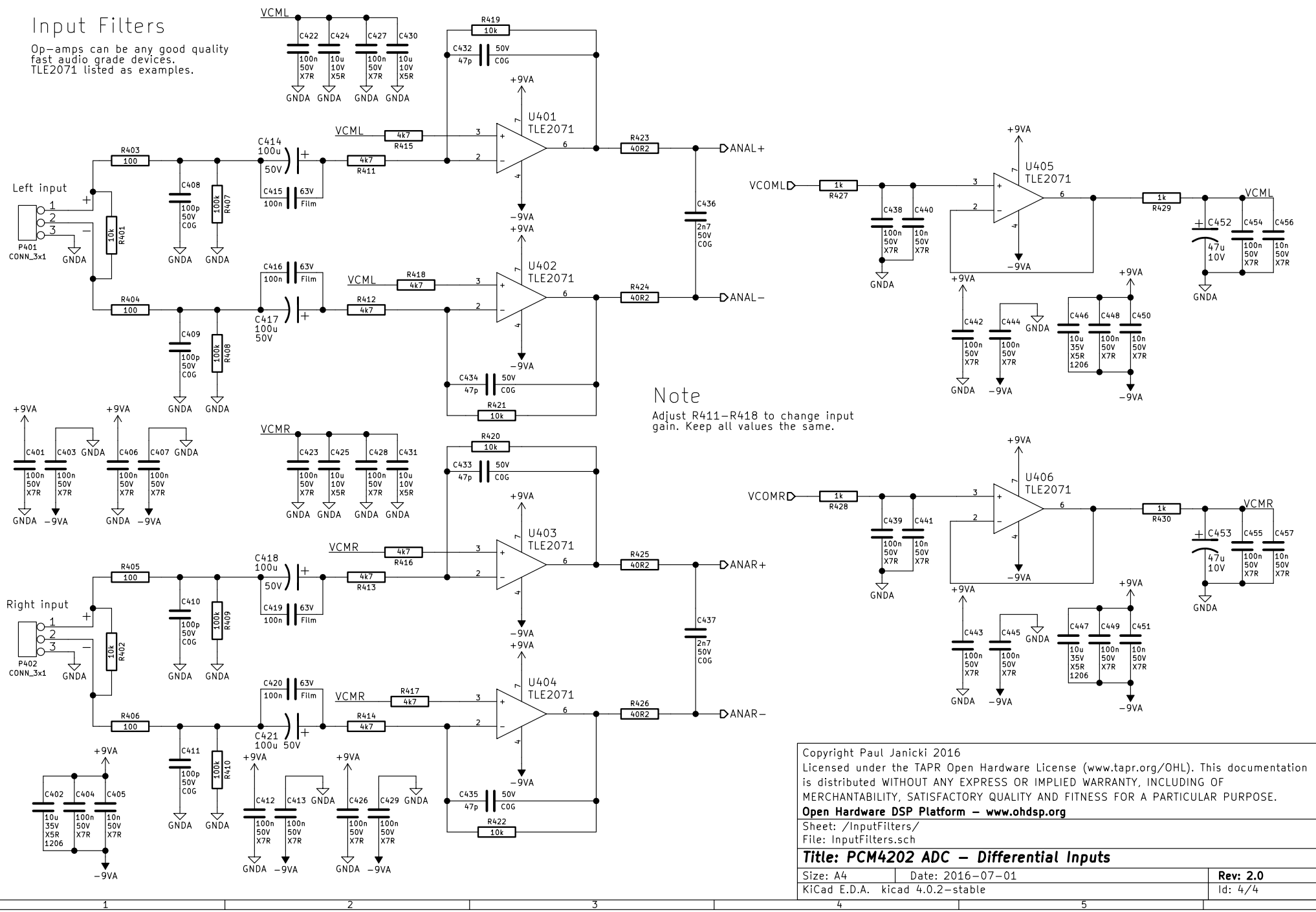
Input Filters
Op-amps can be any good quality fast audio grade devices.
TLE2071 listed as examples.
Left input
Right input
P401 CONN_3x1
P402 CONN_3x1
U401 TLE2071
U402 TLE2071
U403 TLE2071
U404 TLE2071
U405 TLE2071
U406 TLE2071
VCML
VCMR
VCOML
VCOMR
ANAL+
ANAL-
ANAR+
ANAR-
+9VA
-9VA
GNDA
Note
Adjust R411-R418 to change input gain. Keep all values the same.
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