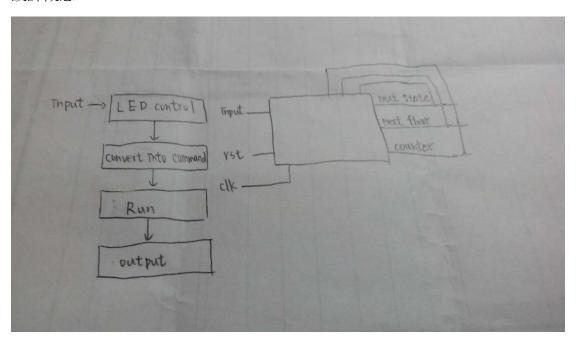
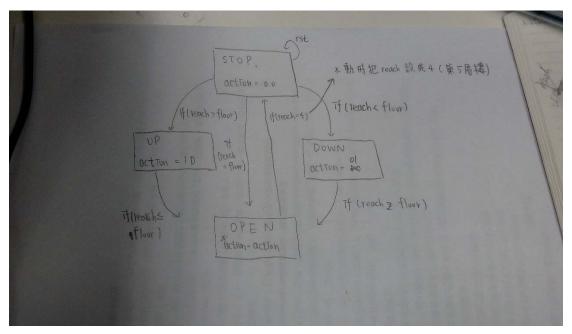
## 變數說明:

```
//定義 input 跟 output
input U1,
              U2,
                      U3,
                              D2,
                                      D3,
                                              D4;
output U1_led, U2_led, U3_led, D2_led, D3_led, D4_led;
Input F1,
            F2,
                     F3,
                             F4;
output F1_led, F2_led, F3_led, F4_led;
output Opened;
output [1:0] Direction;
output [1:0] Floor;
input clk, rst;
//這邊定義 state
parameter STOP = 2'b00,
         UP = 2'b01,
         OPEN = 2'b10,
         DOWN = 2'b11;
//state 存取
reg [1:0] state, next_state;
//LED 設置
reg U1_led,U2_led,U3_led,D2_led,D3_led,D4_led,F1_led,F2_led,F3_led,F4_led;
//先用 LED 燈號來決定哪些樓層要聽,再用這個變數配合 LED 決定 reach
reg [3:0] floorStopOrNot;
//程式碼判斷重點,決定下一個要停的樓層
reg [1:0] reach;
//目前的樓層
reg [1:0] Floor;
//未經處理過的 direction,可能會在 1 樓向下或 4 樓向上,主要是拿來配合
Floor 決定下一個 round 的 reach
wire [1:0] action;
```

## 設計概念:





Simulation 結果:

直接打 ncverilog elevator.v elevator\_test.v + access+r 即可

