Algorithm 1 - SIMD Implementations

GCC 7.5.0 with flags: -O3 -mfma -fno-ipa-cp-clone -mavx2

Speedup

- l2-norm (SIMD)
- l2-norm (SIMD) + quadsort
- l2-norm (SIMD) + quadsort + get_true_knn (unrolling & referencing) + compute_shapley
- quadsort + get_true_knn (SIMD blocking) + compute_shapley

Input size N