

A circuit diagram showing a 470R resistor in series with a 100nF capacitor (C11) connected to GND.

[illegible]

Diagram of JP2 pin connections:

- Pin 1: MCU_RST
- Pin 2: SWDIO PA13
- Pin 3: GND
- Pin 4: SWCLK PA14
- Pin 5: MCU_3.3V

[illegible][illegible]



