1. ao\_mem\_operation Register (0x40090000)

Table ao\_mem\_operation (0x40090000)

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| BIT | R/W | Reset Value | Name | Description |
| [31:8] | RO | 24’b0 | reserved\_1 | Reserved |
| [7:2] | RW | 6’d5 | operation\_addr | Ao\_memory operation address |
| [1] | RW | 1’b0 | operation\_select | Ao\_memory operation select  0: read ao\_memory  1: write ao\_memory |
| [0] | RW | 1’h0 | operation\_start | Ao\_memory operation Start (active high)  1: start one time ao\_memory operation  0: not operation ao\_memory  Note: one time ao\_memory operation finished self clear |

1. ao\_mem\_wr\_data Register (0x40090004)

Table 2 ao\_mem\_wr\_data (0x40090004)

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| BIT | R/W | Reset Value | Name | Description |
| [31:0] | RW | 32’b1 | wdata | Ao\_memory write data |