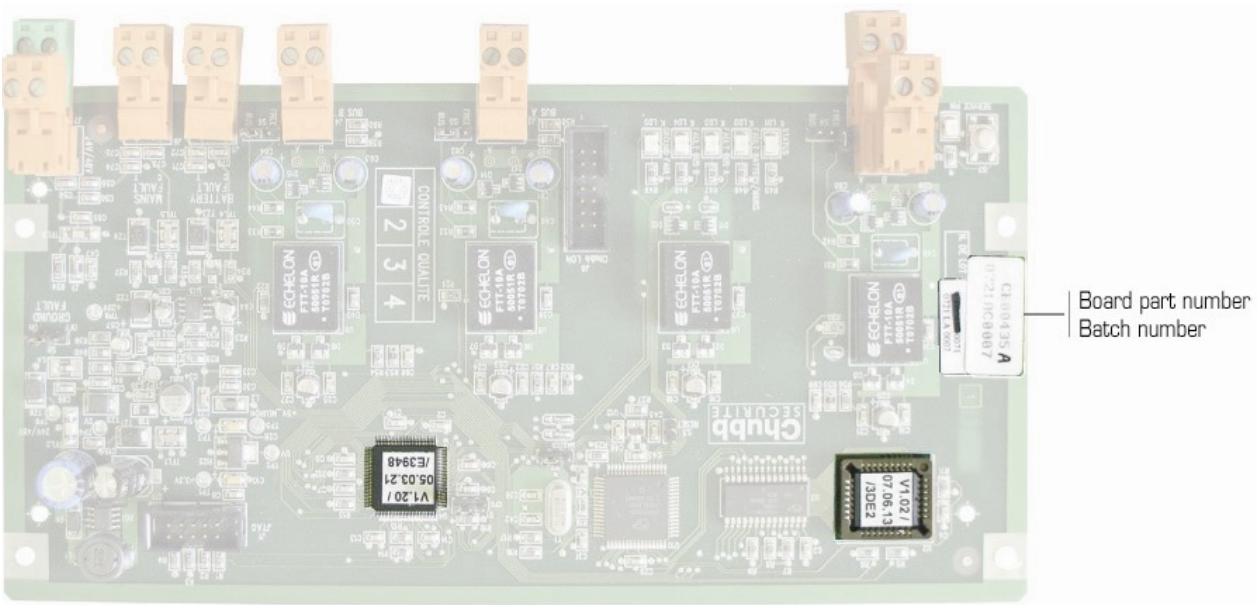
HIGH INTEGRITY LON ISOLATOR

Quick start

Checking the software version



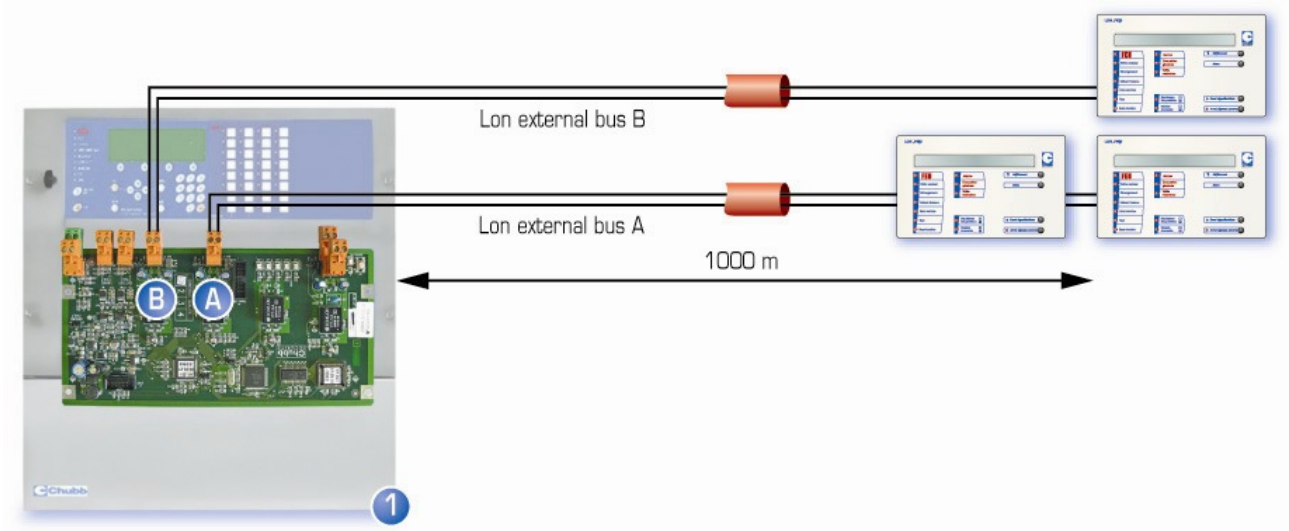
Nota : software version on the drawing are just for illustration and are not to be considered as corresponding to the valid version

Loop bus topology

Check which one of the three possible configurations is used:

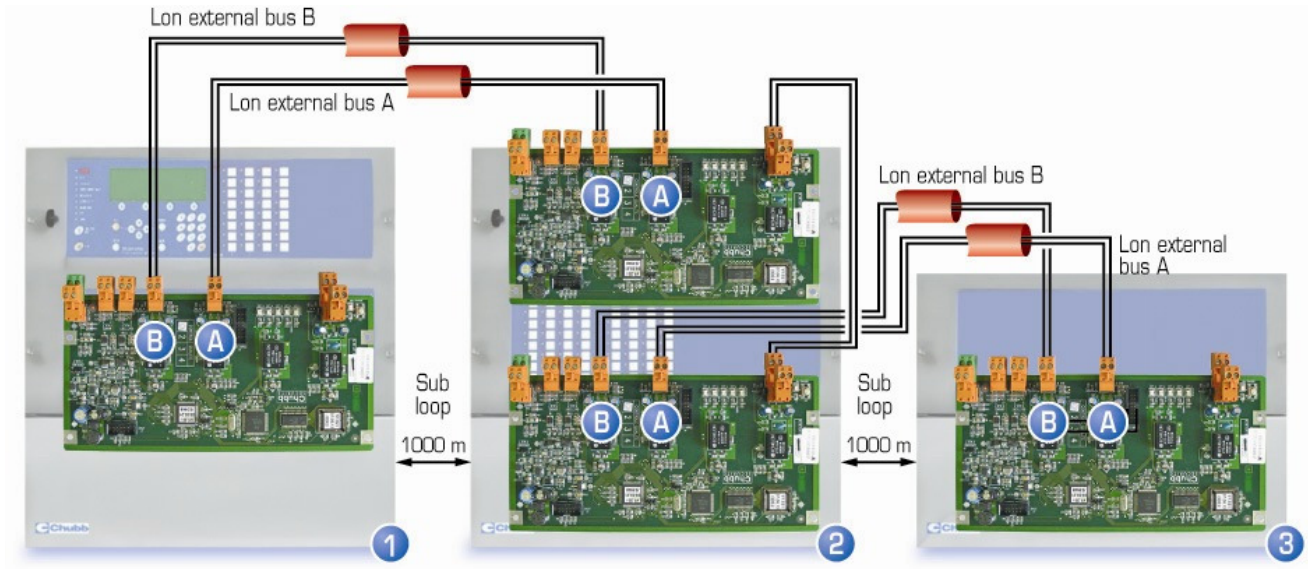
- Single bus topology
- Redundant bus topology (also call sub loop)
- Loop topology

Single bus topology



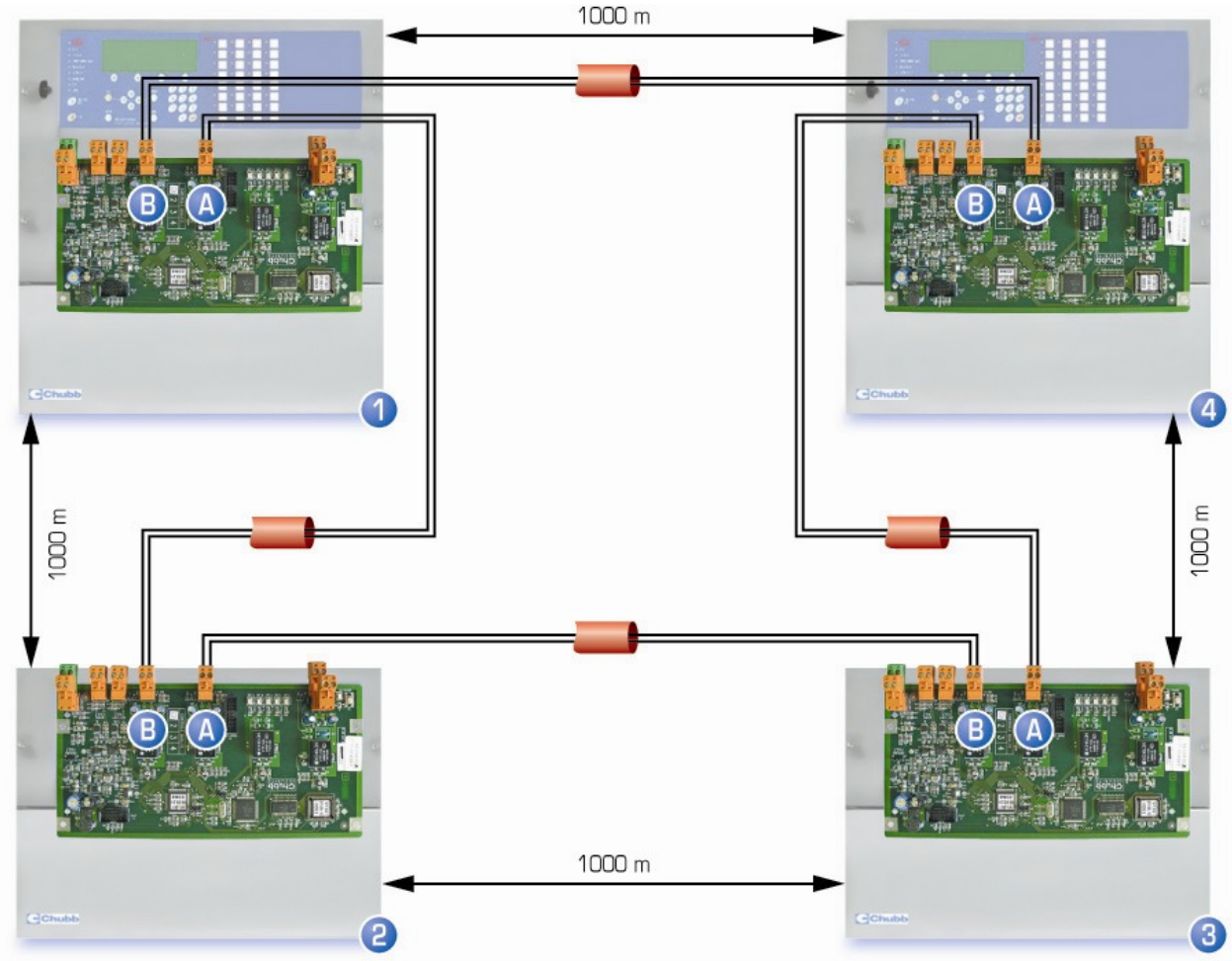
This topology is to be used for example for connection of Lon repeaters.
Connect a 105 Ohms resistor on the last repeater of each Lon external bus.
Configuring the jumper chapter.

Redundant bus topology (also called sub loop)



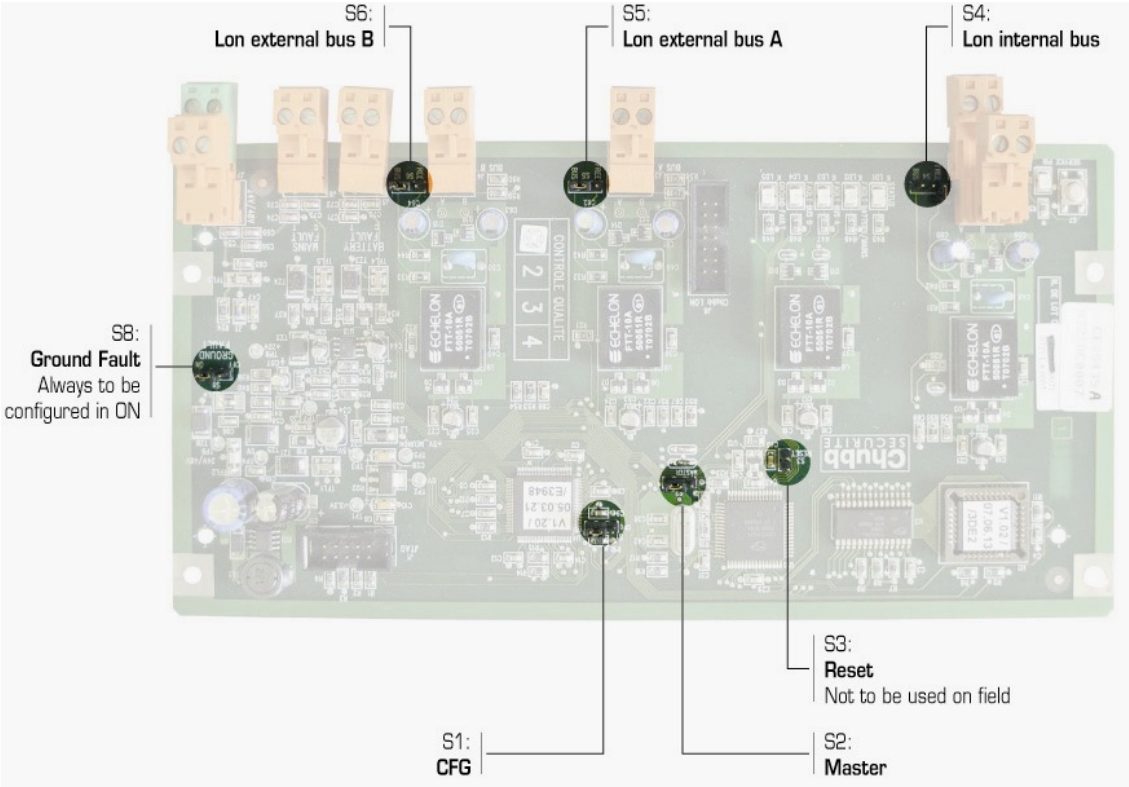
Maximum two remote units in serie.
Configuring the jumper chapter.

Loop bus topology



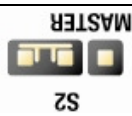
Minimum three High Integrity Lon Isolator on a loop configured in bus topology.
Maximum four High integrity Lon isolator on a loop configured in bus topology.
Number 1, 2, 3 and 4 give the High integrity Lon isolator position on the node.
1 needs to be fitted in a panel.
2 can be fitted in a panel or in a remote cabinet.
Assign numbers counter clockwise.
Assign values in Chubb expert..
Configuring the jumper chapter.

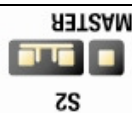
Configuring the jumpers



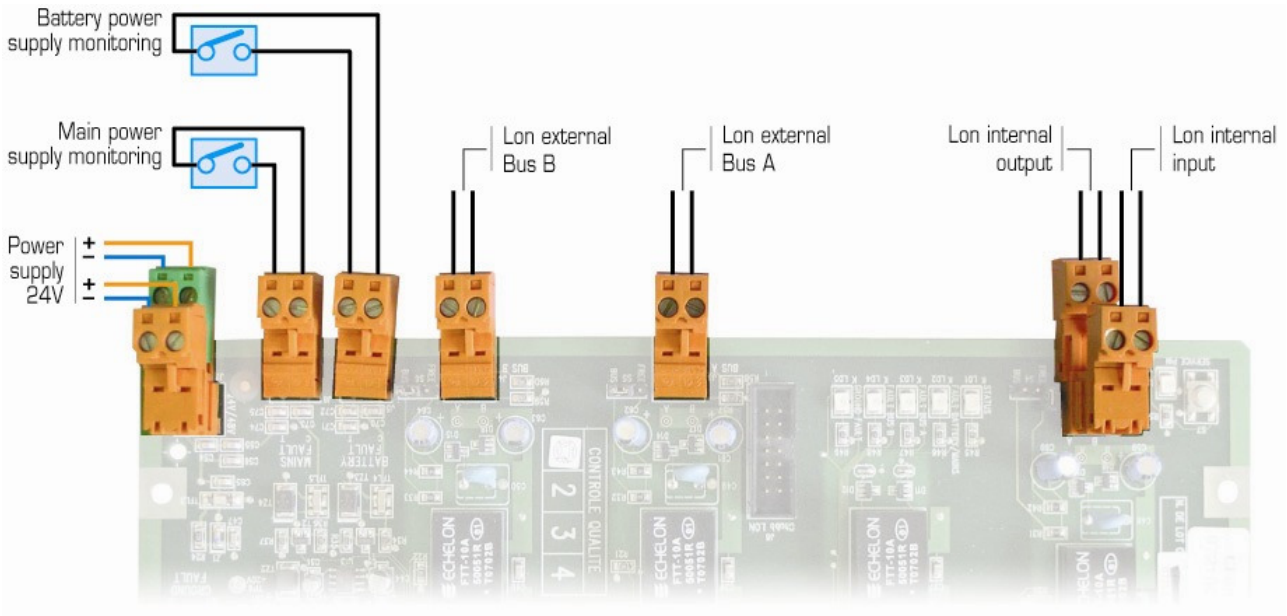
Jumpers settings shown below should match jumper settings on the board when installed inside the cabinets *(connectors at the top)*.

Single Bus topology *(see page 3)*					
Unit	**S1** (CFG)	**S2** (master)	S4 (Lon internal)	**S5** (Lon external bus A)	**S6** (Lon external bus B)
1					

Redundant bus – Also call sub loop *(see page 3)*					
Unit	**S1** (CFG)	**S2** (master)	S4 (Lon internal)	**S5** (Lon external bus A)	**S6** (Lon external bus B)
1					
2					
3					

Loop bus topology *(see page 4)*					
Unit	**S1** (CFG)	**S2** (master)	**S4** (Lon internal)	**S5** (Lon external bus A)	**S6** (Lon external bus B)
1					
2 /3 /4					

Wiring

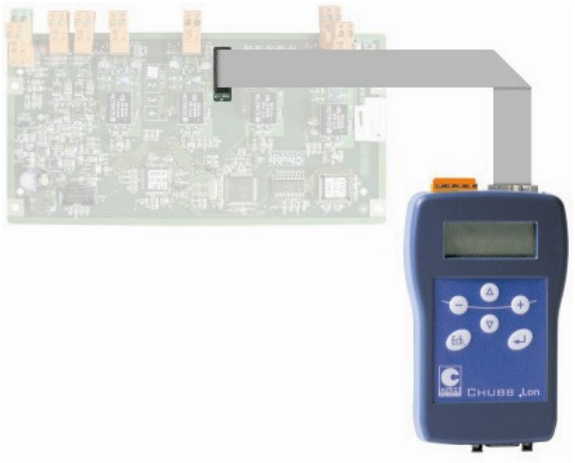
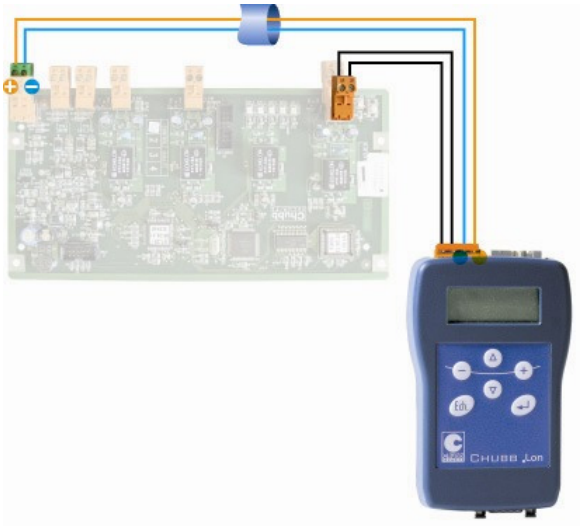


Nota :
No polarity for Lon connection.
Mains and battery power supply monitoring: the contact is closed to signal a fault.

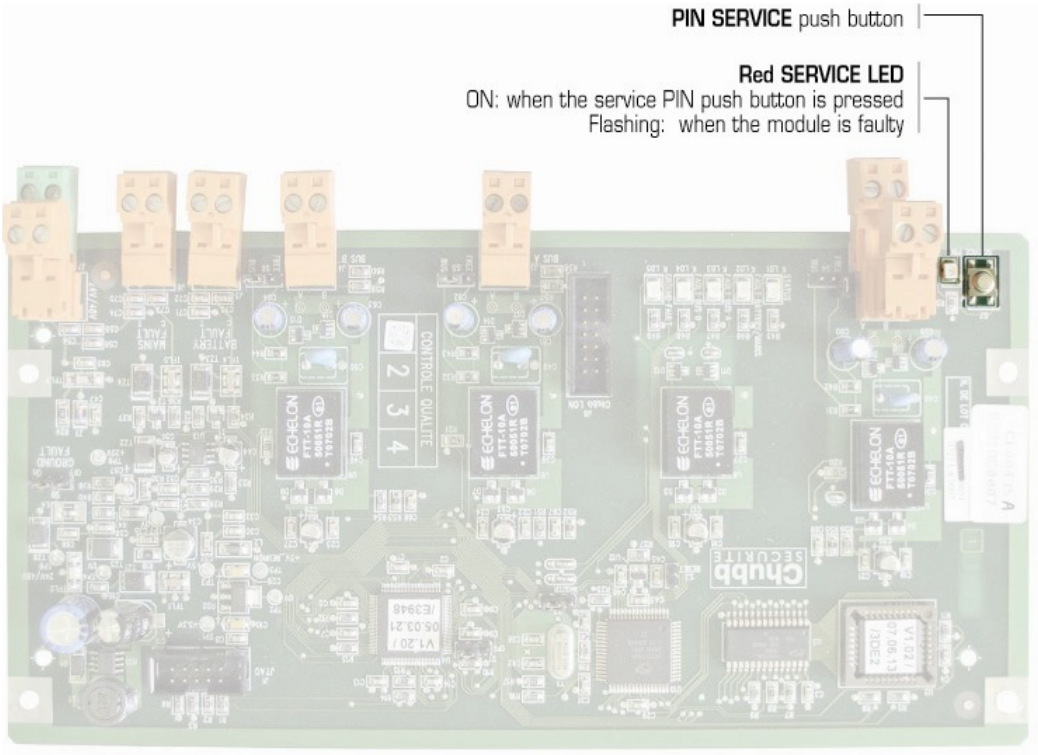
Configuring the high integrity Lon isolator address

This is done with the Chubb Lon tool connected to the module:

- Either with the flat cable.
- Or with lon 24V connection.

Cable ring connection	Lon / 24 V connection
	

On Chubb Lon select install



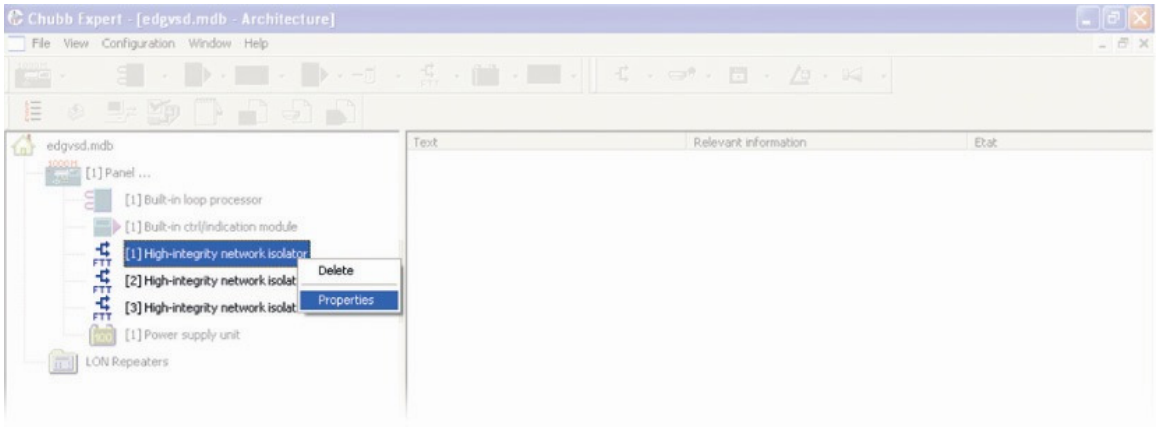
Press the PIN service push button.

Enter the panel number.

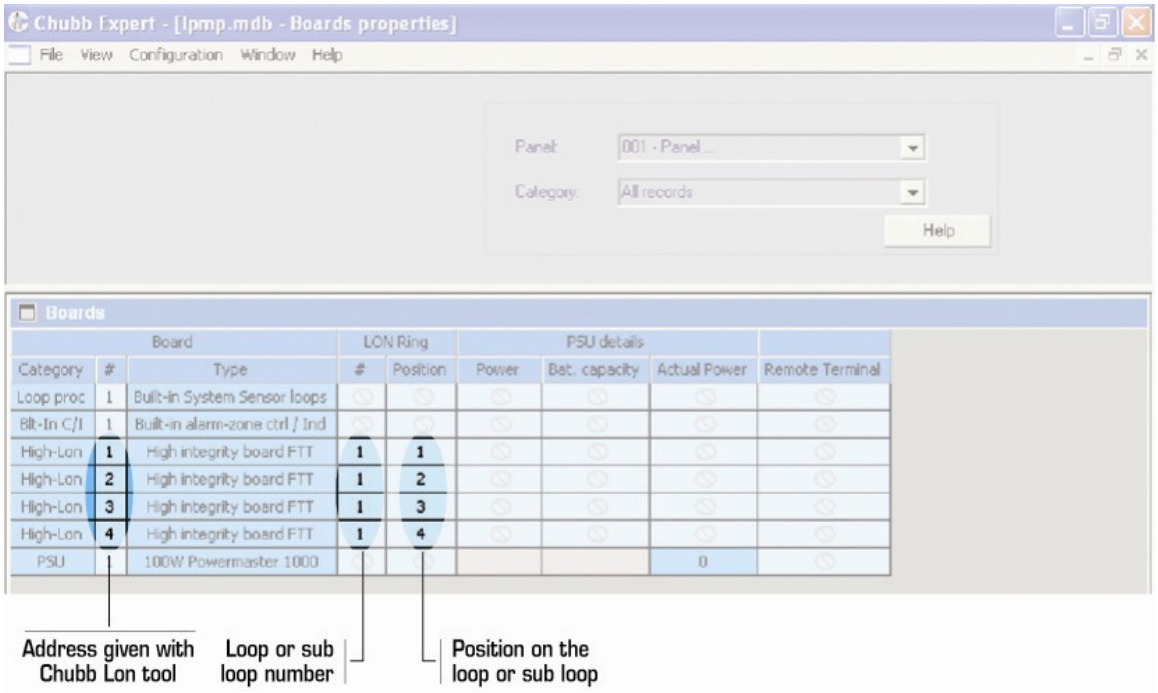
Enter the address of the high integrity Lon isolator module.

Configuring of the high integrity Lon isolator address with Chubb Expert

In the “Hardware” tab select the icon for High integrity Lon isolator and click right



Select properties and click left.



	Single loop topology	Redundant bus topology (also call sub loop)	Loop bus topology
LON Ring #	Assign a number between 1 and 64 maximum	Assign a number for each sub loop between 1 and 64 maximum	Assign a number for each loop between 1 and 64 maximum
LON Ring Position	Enter 0	One of the high integrity Lon isolator must have the number one and the other the number 2	Enter the number corresponding on the position on the loop

LED description