

## Controlling PlutoSDR

At this point you should have PlutoSDR connected with the latest firmware installed on the device. As a prerequisite for this workshop you should have installed the libIIO drivers, if you have not done so go the [libIIO Release page](#) and install the latest drivers.

To make sure we can see and connect to PlutoSDR open a terminal or command prompt. Type “iio\_info -s” to scan for devices. You should see an output similar to Figure 1.

```
Command Prompt
C:\Users\tcollins>iio_info -s
Library version: 0.14 (git tag: 17b73d3)
Compiled with backends: xml ip usb serial
Available contexts:
    0: 0456:b673 (Analog Devices Inc. PlutoSDR (ADALM-PLUTO)), serial=104473ce69910006220021006dad89b3b1 [usb:2.5.5]

C:\Users\tcollins>
```

Figure 1

Your device should have a unique serial number and URI. The URI is specific to the interface, here it is displayed as [usb;X.X.X], and can be used to interface with a specific device when required. When you have a network based device, like a FMComms board, the URI will be associated with an IP address such as: [ip:XXX.XXX.XXX.XXX]. If you do not observe a device, make sure PlutoSDR is connected to the correct USB port and has a one solid LED and one flashing LED. “iio\_info” is a useful tool for identifying devices locally connect to connected on the network as well.

Another useful libIIO command line tool is “iio\_attr”, which can be used to inspect, as well as read and write attributes. The help for this tool is shown in Figure 2, which outlines that “iio\_attr” can be used access different types of attributes including: device, channel, debug, buffer, and context.

```
Select Command Prompt
C:\Users\tcollins>iio_attr -h
Usage:
    iio_attr [OPTION]... -d [device] [attr] [value]
                        -c [device] [channel] [attr] [value]
                        -B [device] [attr] [value]
                        -D [device] [attr] [value]
                        -C [attr]

Options:
    -h, --help          : Show this help and quit.
    -I, --ignore-case    : Ignore case distinctions.
    -q, --quiet          : Return result only.
    -a, --auto           : Use the first context found.

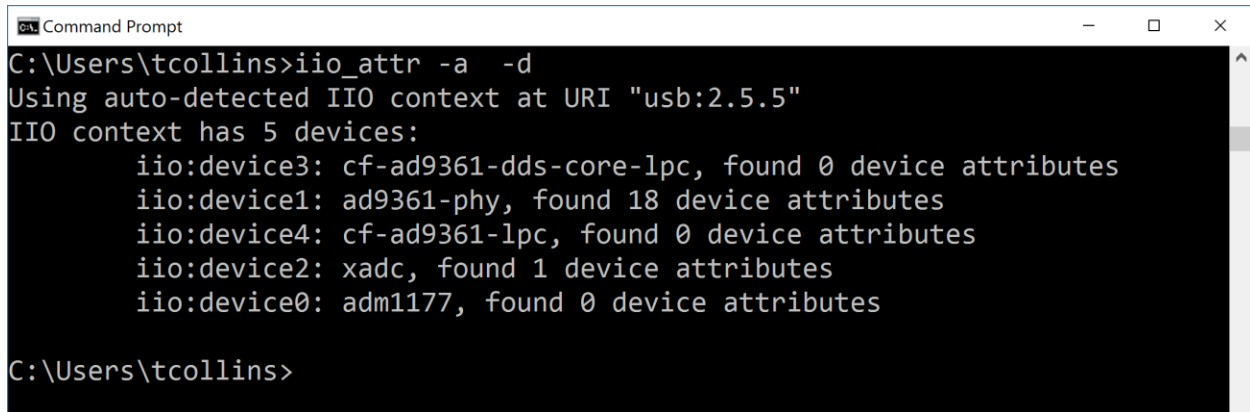
Optional qualifiers:
    -u, --uri            : Use the context at the provided URI.
    -i, --input-channel  : Filter Input Channels only.
    -o, --output-channel : Filter Output Channels only.

Attribute types:
    -s, --scan-channel   : Filter Scan Channels only.
    -d, --device-attr    : Read/Write device attributes.
    -c, --channel-attr   : Read/Write channel attributes.
    -C, --context-attr   : Read IIO context attributes.
    -B, --buffer-attr    : Read/Write buffer attributes.
    -D, --debug-attr     : Read/Write debug attributes.

C:\Users\tcollins>
```

Figure 2

The first step when using “iio\_attr” is to select a device to inspect. Since PlutoSDR is connected locally we can use the “auto” context feature and list the devices using the command “iio\_attr -a -d”. Use this command to list your devices as shown in Figure 3.



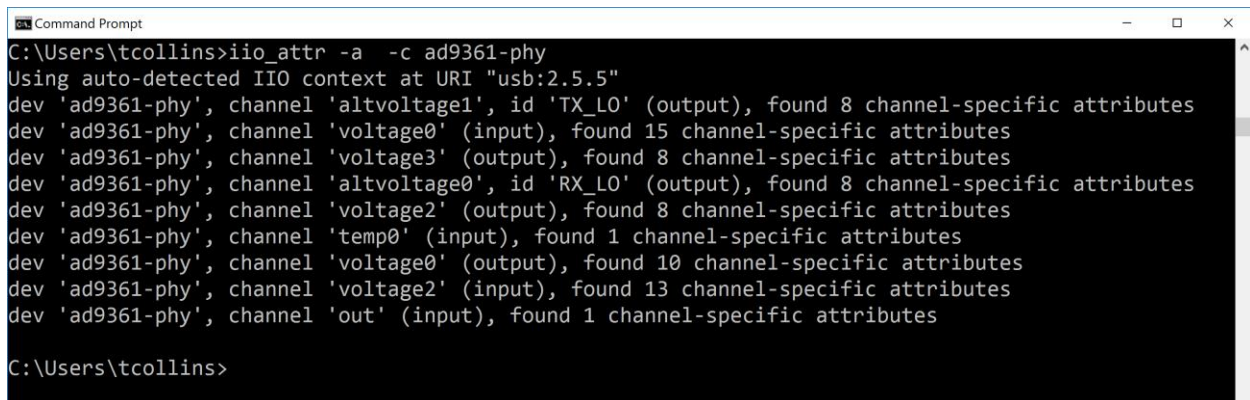
```
Command Prompt
C:\Users\tcollins>iio_attr -a -d
Using auto-detected IIO context at URI "usb:2.5.5"
IIO context has 5 devices:
    iio:device3: cf-ad9361-dds-core-lpc, found 0 device attributes
    iio:device1: ad9361-phy, found 18 device attributes
    iio:device4: cf-ad9361-lpc, found 0 device attributes
    iio:device2: xadc, found 1 device attributes
    iio:device0: adm1177, found 0 device attributes

C:\Users\tcollins>
```

Figure 3

This command also prints some context information. Alternative, try using the “uri” tag to address your PlutoSDR. For the case in Figure 3, the new command would be “iio\_attr -u usb:2.5.5 -d”. Construct this command and run it yourself to check if the devices match.

Next, we will examine some of the channel attributes for a given device. Run the command “iio\_attr -a -c ad9361-phy”, as done in Figure 4, which lists the channels associated with the PHY driver. These channels are related to certain aspects of the transceiver like TX, RX, temp sensors, and even some custom filters in the FPGA data path.



```
Command Prompt
C:\Users\tcollins>iio_attr -a -c ad9361-phy
Using auto-detected IIO context at URI "usb:2.5.5"
dev 'ad9361-phy', channel 'altvoltage1', id 'TX_LO' (output), found 8 channel-specific attributes
dev 'ad9361-phy', channel 'voltage0' (input), found 15 channel-specific attributes
dev 'ad9361-phy', channel 'voltage3' (output), found 8 channel-specific attributes
dev 'ad9361-phy', channel 'altvoltage0', id 'RX_LO' (output), found 8 channel-specific attributes
dev 'ad9361-phy', channel 'voltage2' (output), found 8 channel-specific attributes
dev 'ad9361-phy', channel 'temp0' (input), found 1 channel-specific attributes
dev 'ad9361-phy', channel 'voltage0' (output), found 10 channel-specific attributes
dev 'ad9361-phy', channel 'voltage2' (input), found 13 channel-specific attributes
dev 'ad9361-phy', channel 'out' (input), found 1 channel-specific attributes

C:\Users\tcollins>
```

Figure 4

By selecting one of these channels, in this case voltage0 which is the real channel names for both TX and RX, we can view some of the attributes related to those channels. For reference, input channels are with respect to RX and output channels are with respect to TX. To list these attributes run the command “iio\_attr -a -c ad9361-phy voltage0”.

```
Command Prompt
C:\Users\tcollins>iio_attr -a -c ad9361-phy voltage0
Using auto-detected IIO context at URI "usb:2.5.5"
dev 'ad9361-phy', channel 'voltage0' (input), attr 'hardwaregain_available', value '[-3 1 71]'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'hardwaregain', value '71.000000 dB'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'rssi', value '109.50 dB'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'rf_port_select', value 'A_BALANCED'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'gain_control_mode', value 'slow_attack'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'rf_port_select_available', value 'A_BALANCED B_BALANCED C_BALANCED A_N A_P B_N B_P
C_N C_P TX_MONITOR1 TX_MONITOR2 TX_MONITOR1_2'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'rf_bandwidth', value '18000000'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'rf_dc_offset_tracking_en', value '1'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'sampling_frequency_available', value '[2083333 1 61440000]'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'quadrature_tracking_en', value '1'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'sampling_frequency', value '30720000'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'gain_control_mode_available', value 'manual fast_attack slow_attack hybrid'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'filter_fir_en', value '0'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'rf_bandwidth_available', value '[200000 1 56000000]'
dev 'ad9361-phy', channel 'voltage0' (input), attr 'bb_dc_offset_tracking_en', value '1'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'rf_port_select', value 'A'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'hardwaregain', value '-10.000000 dB'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'rssi', value '0.00 dB'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'hardwaregain_available', value '[0 250 89750]'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'sampling_frequency_available', value '[2083333 1 61440000]'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'rf_port_select_available', value 'A B'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'filter_fir_en', value '0'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'sampling_frequency', value '30720000'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'rf_bandwidth_available', value '[200000 1 40000000]'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'rf_bandwidth', value '18000000'

C:\Users\tcollins>
```

Figure 5

To filter out one of these attributes simply add it to the end of the last used command. For example, inspecting the hardwaregain would be called as “iio\_attr -a -c ad9361-phy voltage0 hardwaregain”, as shown in Figure 6.

```
Command Prompt
C:\Users\tcollins>iio_attr -a -c ad9361-phy voltage0 hardwaregain
Using auto-detected IIO context at URI "usb:2.5.5"
dev 'ad9361-phy', channel 'voltage0' (input), attr 'hardwaregain', value '71.000000 dB'
dev 'ad9361-phy', channel 'voltage0' (output), attr 'hardwaregain', value '-10.000000 dB'

C:\Users\tcollins>
```

Figure 6

Next, we want to only view output signals (those related to the transmitter), which we can do by adding the output filter tag “iio\_attr -a -o -c ad9361-phy voltage0 hardwaregain”.

```
Command Prompt
C:\Users\tcollins>iio_attr -a -o -c ad9361-phy voltage0 hardwaregain
Using auto-detected IIO context at URI "usb:2.5.5"
dev 'ad9361-phy', channel 'voltage0' (output), attr 'hardwaregain', value '-10.000000 dB'

C:\Users\tcollins>
```

Figure 7

Finally, lets actually write to the attribute by adding a value to the end of the last command “iio\_attr -a -o -c ad9361-phy voltage0 hardwaregain -30”. When values are written, they are first always read from beforehand, and then read from after the update is applied. If the value supplied is invalid, you will get an error.

```
Command Prompt
C:\Users\tcollins>iio_attr -a -o -c ad9361-phy voltage0 hardwaregain -30
Using auto-detected IIO context at URI "usb:2.5.5"
dev 'ad9361-phy', channel 'voltage0' (output), attr 'hardwaregain', value '-10.000000 dB'
wrote 4 bytes to hardwaregain
dev 'ad9361-phy', channel 'voltage0' (output), attr 'hardwaregain', value '-30.000000 dB'
C:\Users\tcollins>
```

Figure 8

Going through the context, to the devices, to their channels, and finally to channel attributes, you can see how the driver controlling PlutoSDR is structured. We will build off of these ideas in following labs, but first we will do something more interesting.

Using the command line alone is pretty slow, non-intuitive, and repetitive. You can write your own applications, like “iio\_attr” in C, or in other languages like Python or MATLAB. However, there exists a specific GUI application for interacting with IIO devices called IIO-Oscilloscope (OSC), which is may more civilized than poking around at attributes through “iio\_attr” all day.

### IIO-Oscilloscope (OSC)

As a prerequisite for these labs you should have already installed OSC, if not please install it from the [Release Page here](#).

Launch OSC, and you should be greeted by a connection window similar to Figure 9.

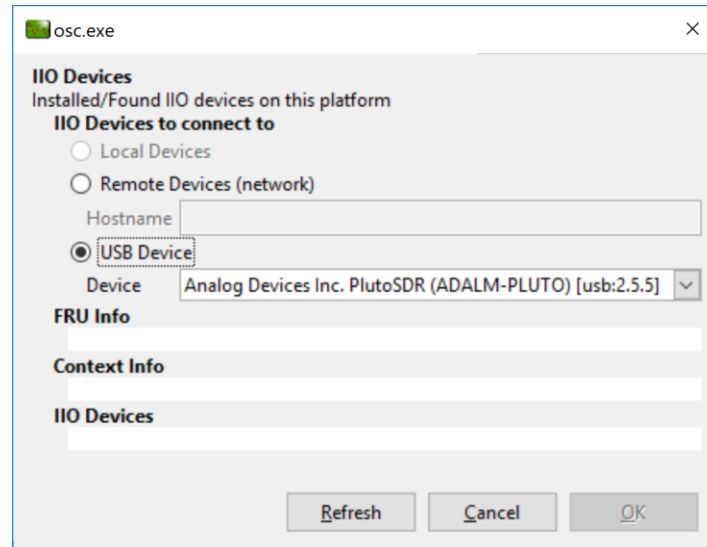


Figure 9

Next, select the USB device, and the PlutoSDR. Click the refresh button, and then OK. Next the main window should appear with the necessary control tabs.

Explore IIO oscilloscope, and the control tabs. These tabs let you control the high to the low levels of the device and visualize the signals that are going in/out of the device. Some specific parts to look at are shown in Figure 10.

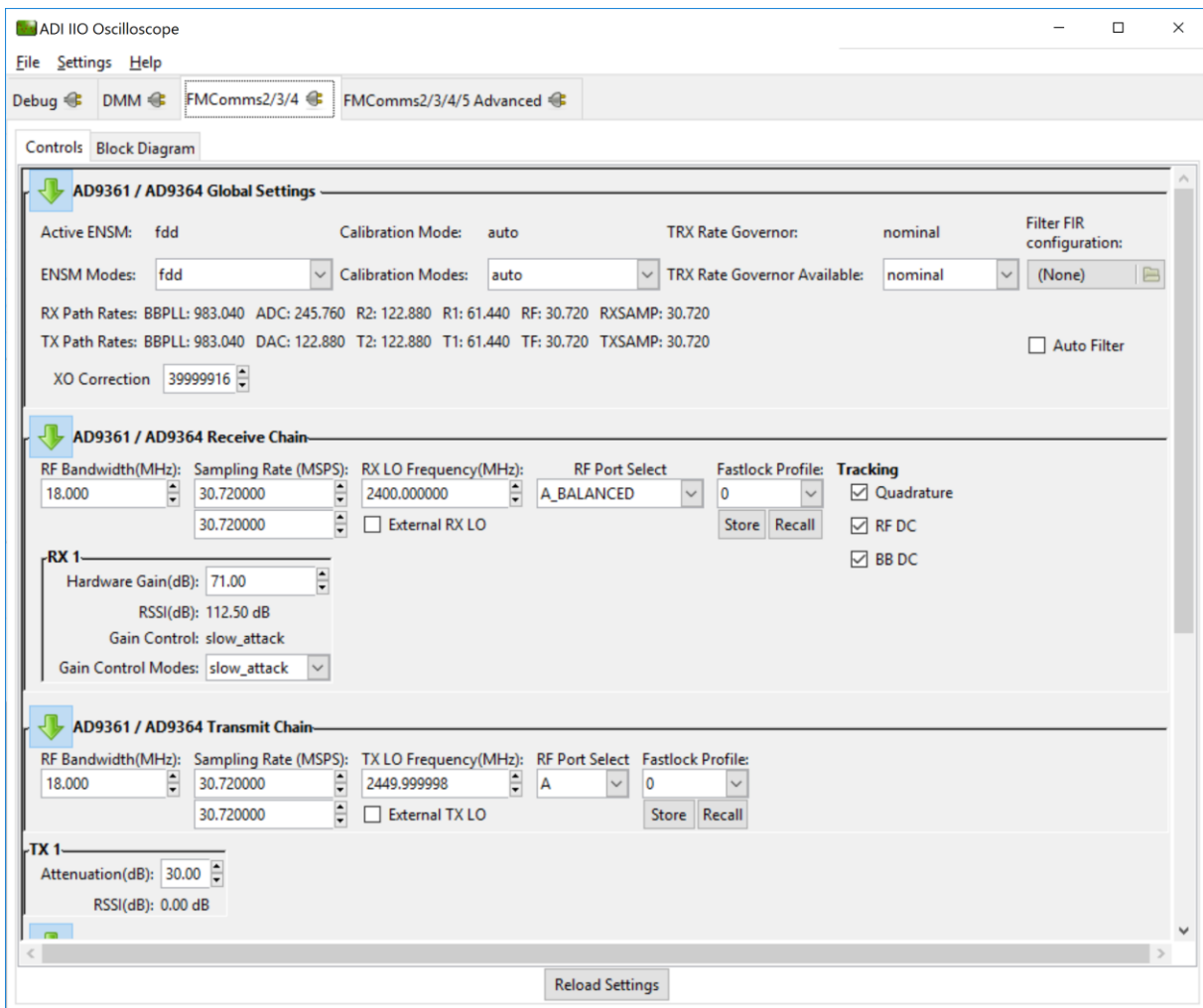
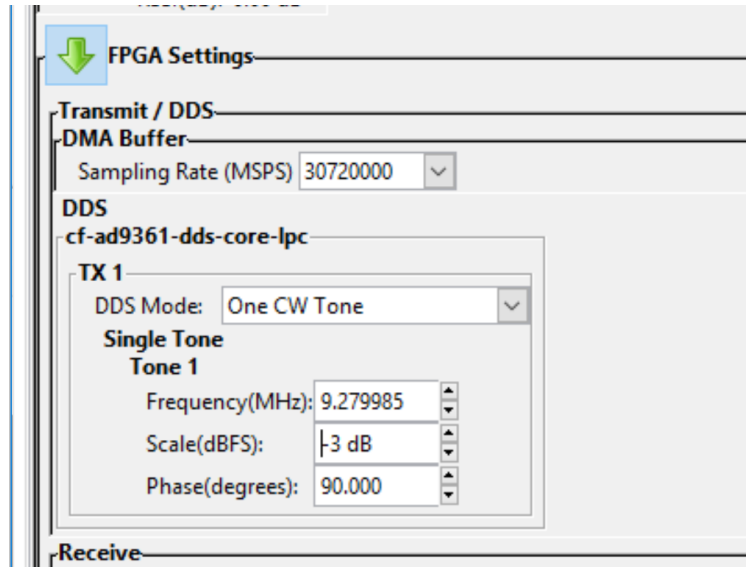


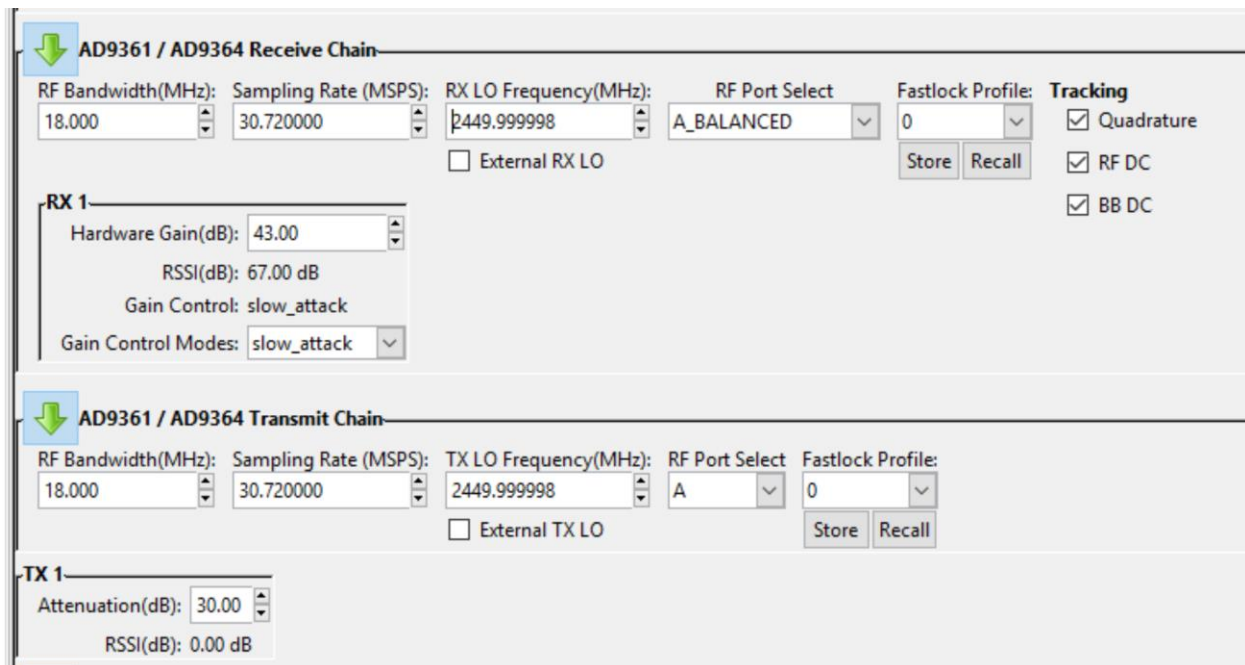
Figure 10

From the “FMComms2/3/4” tab shown in Figure 10, you can examine and change the RF bandwidth, the DAC sample rate, the Tx LO Frequency, and the signals that are being sent from the FPGA to the AD9363 inside the ADALM-PLUTO SDR. This tab, as the name define is shared among a few SDR that proceeded PlutoSDR, specifically FMComms2/3/4.

Using the controls in the “FMComms2/3/4” tab, set the DDS in the “FPGA Settings” to a single tone at -3dB scale.



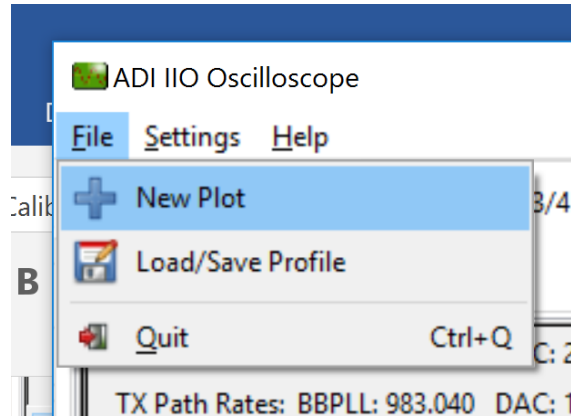
In the Receive and Transmit Chain panels, set the Sample Rate to 30.72 MSPS, with a 18 MHz RF Bandwidth, at a TX and RX LO of 2450 MHz.



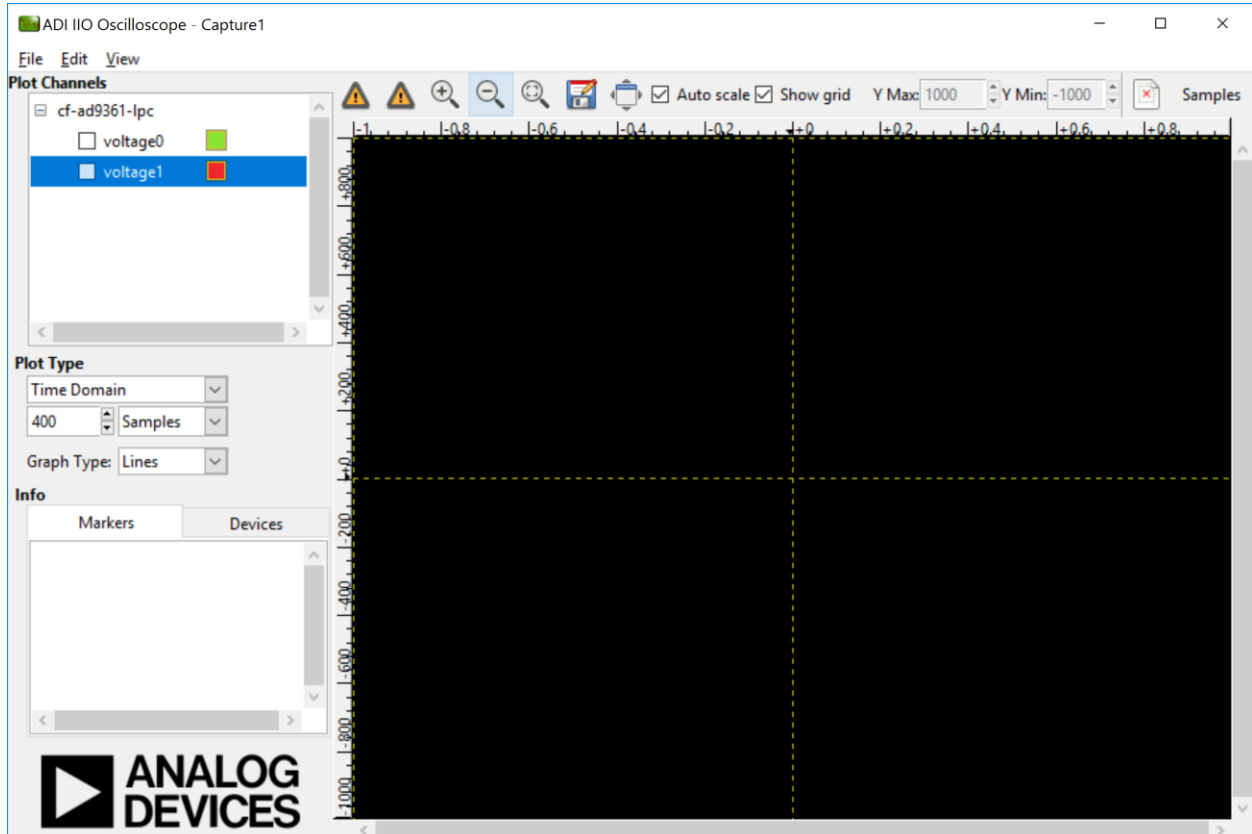
OSC also visualization capabilities, which we will explore next.

## Visualizations

Create a new plot window from the file menu.



These plot, or capture, windows provide real-time visualization with time domain plots, frequency domain plots, cross-correlation, and constellation diagrams. Enable some of the voltage channels and play with the scopes a bit to get a feeling for them. Once you are done, move on to the next section.



For now, enable both voltage channels, “voltage0” and voltage1, and select the constellation diagram plot. Then press the play button to start capturing data.

Next, we will setup the transmitter to send some useful data. Back in the “FMComms2/3/4” tab, in the FPGA Settings panel. As show in Figure 11, select “DAC Buffer Output” as the DDS Mode and load the file “qpsknofilt\_30M.txt” for voltage0 and voltage1. Make sure to hit load. This will set the transmitter to continuously repeat a buffer of data, which are just some QPSK symbols.

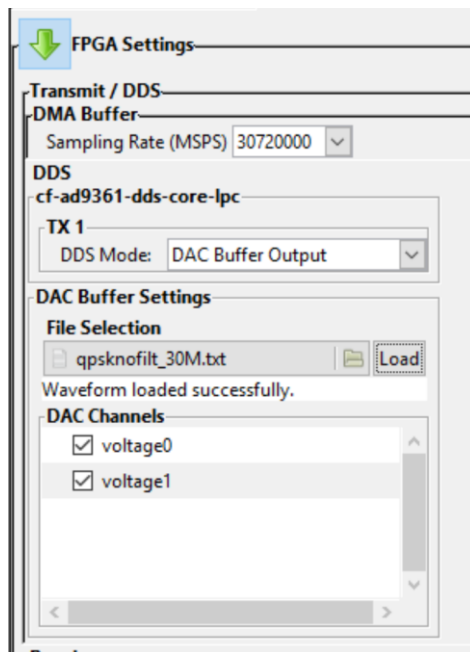


Figure 11

Back on the constellation diagram, you should observe something similar to Figure 12. Since there is a fractional delay between the antennas we are observing some inter-symbol interference. This will be random for each user and each radio initialization.



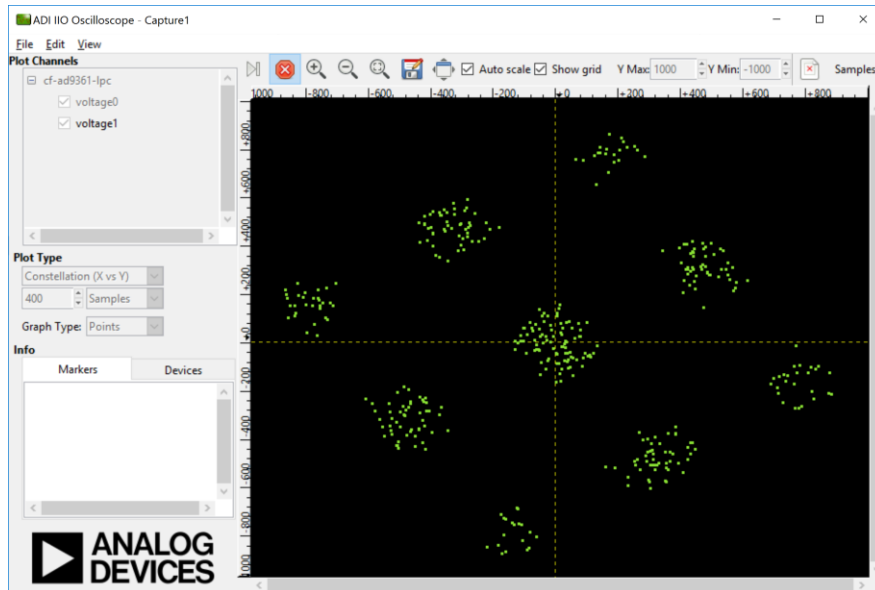


Figure 12

Now for debugging it can be useful to view digitally looped back data, which has not gone out to the RF. To enable this loopback go to the “FMComms2/3/4/5 Advanced” tab, the BIST subtab, and select “Digital TX -> Digital RX” as the Loopback mode as in Figure 13.

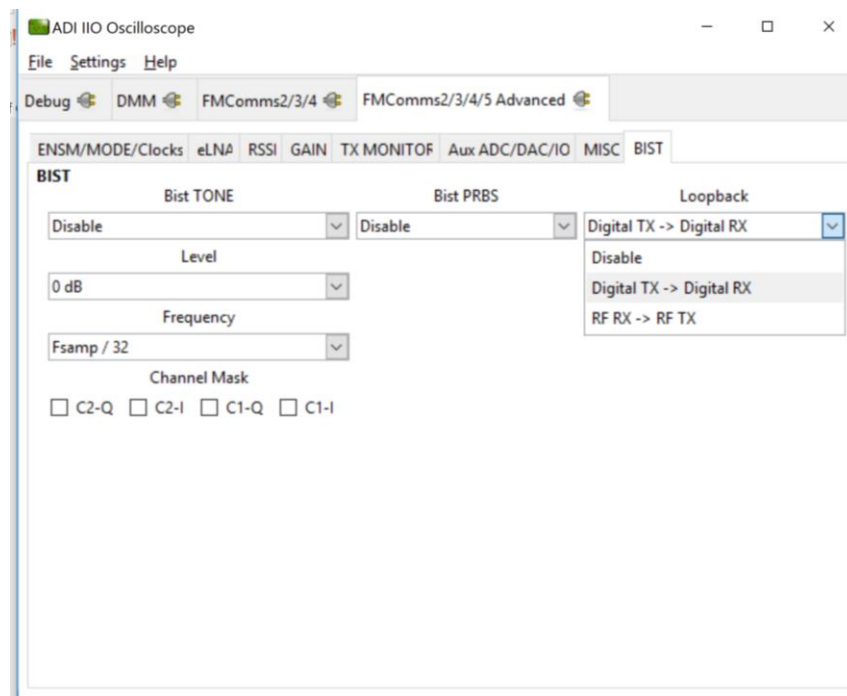


Figure 13

Going back to the constellation diagram, enabling lines instead of points, we should observe a perfect constellation with perfect crossing in the middle as in Figure 14.

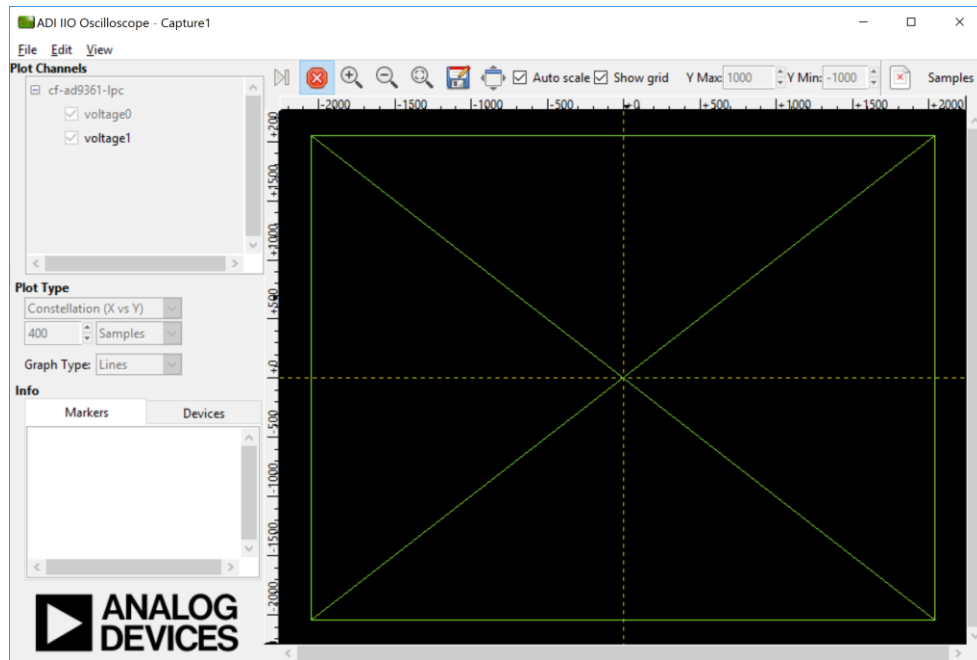


Figure 14

With free time remaining before the next lecture section explore OSC on your own and play with all the knobs if you wish.