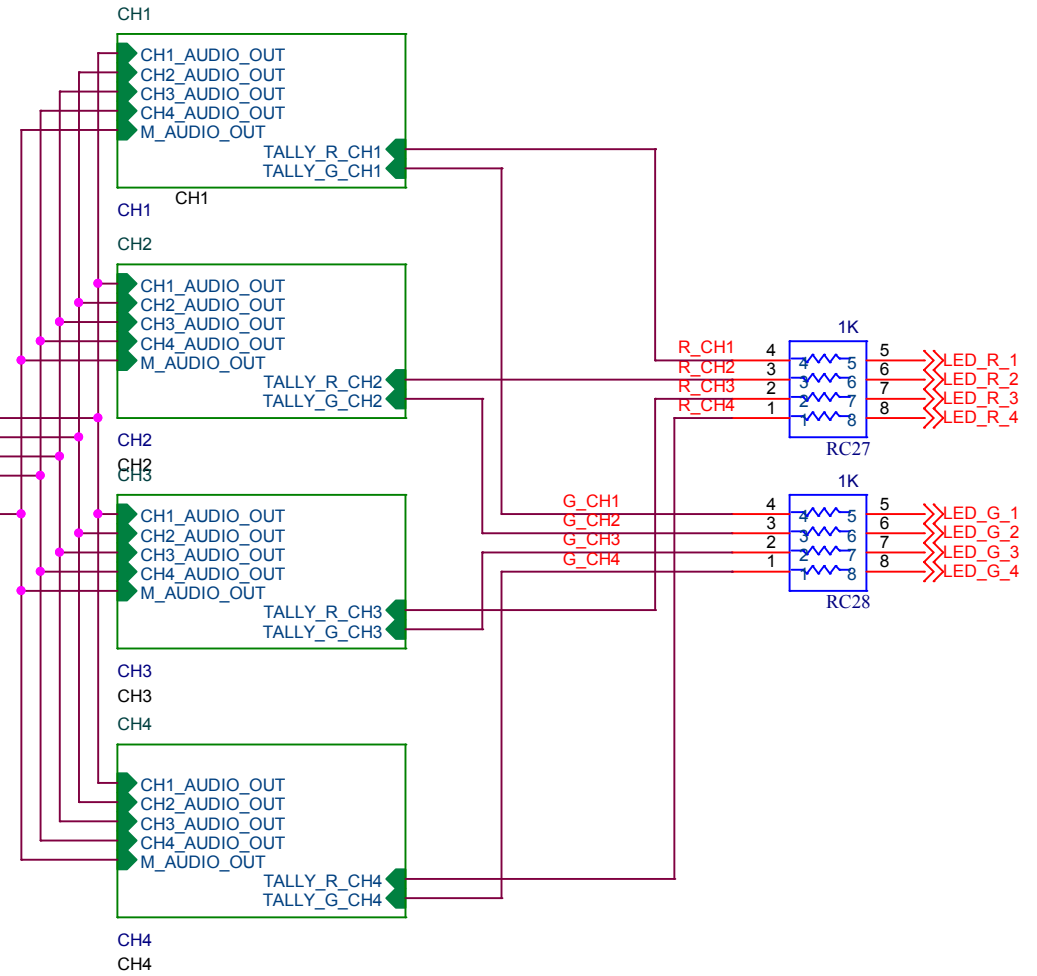
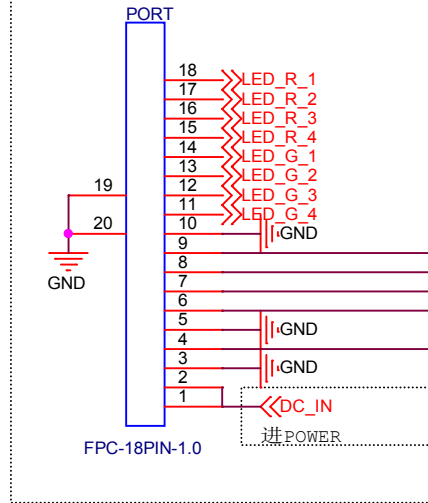
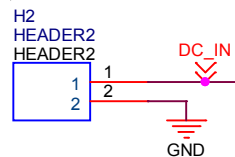


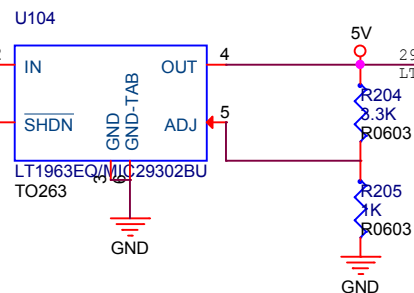
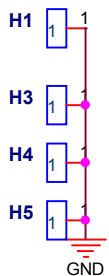
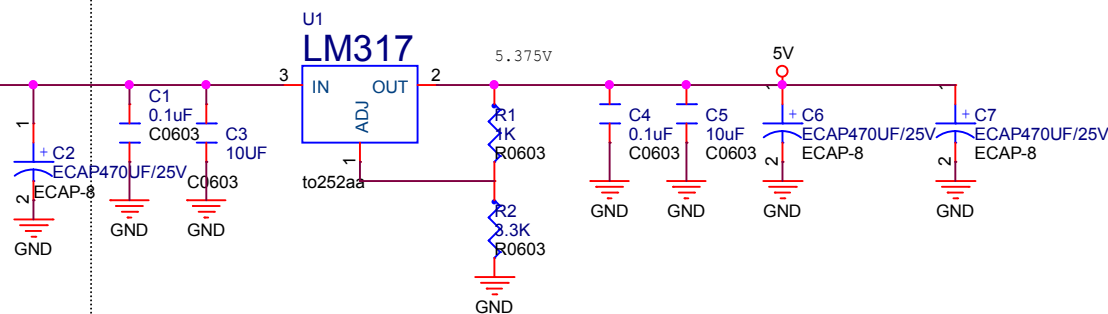
和MASTER对接



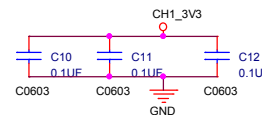
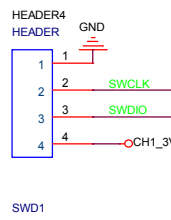
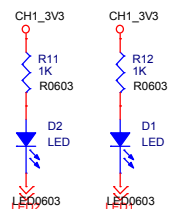
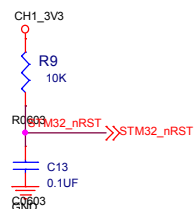
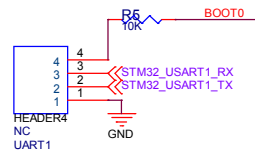
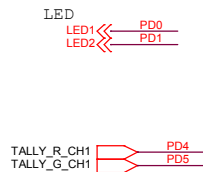
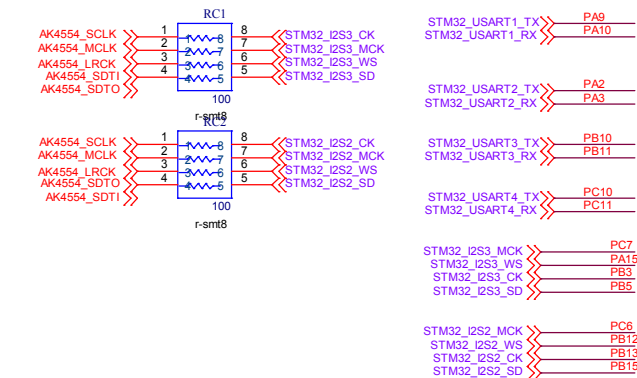
来自主站电源



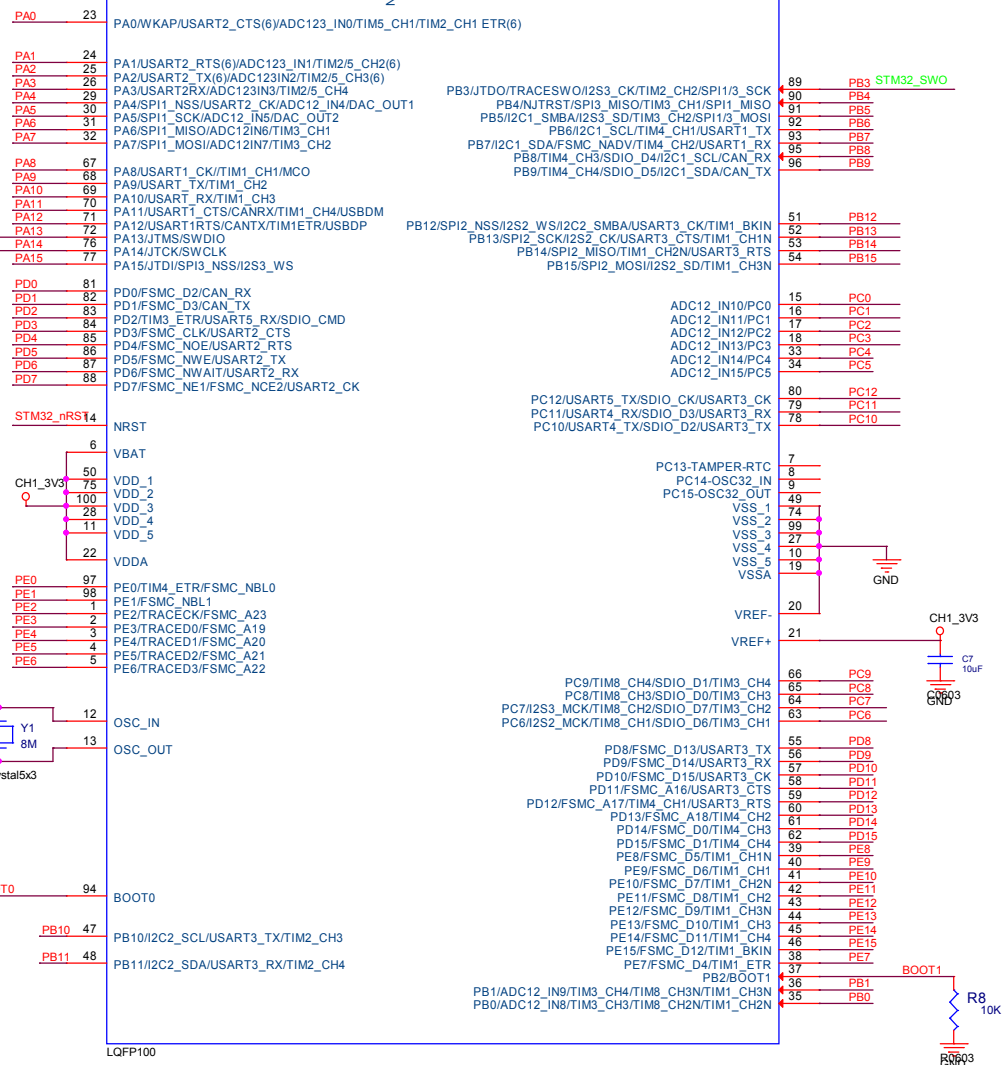
二选一 分站5532供电



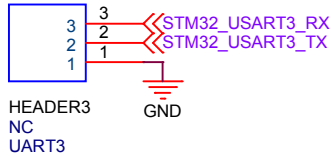
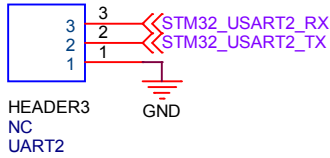
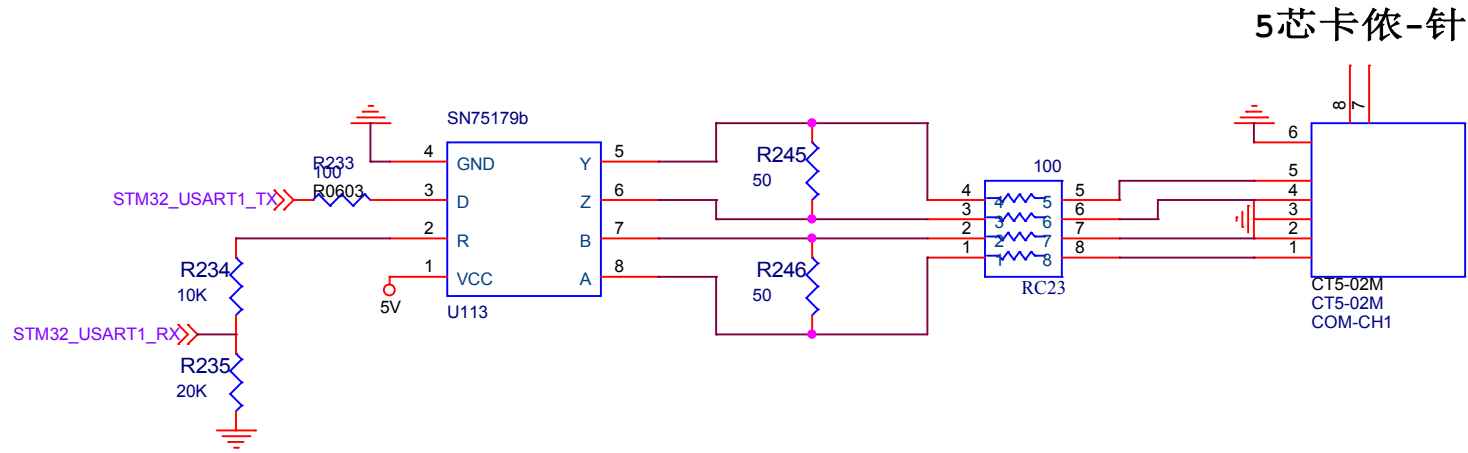
Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	<RevC>
Date:	Friday, February 17, 2017	Sheet 2 of 18



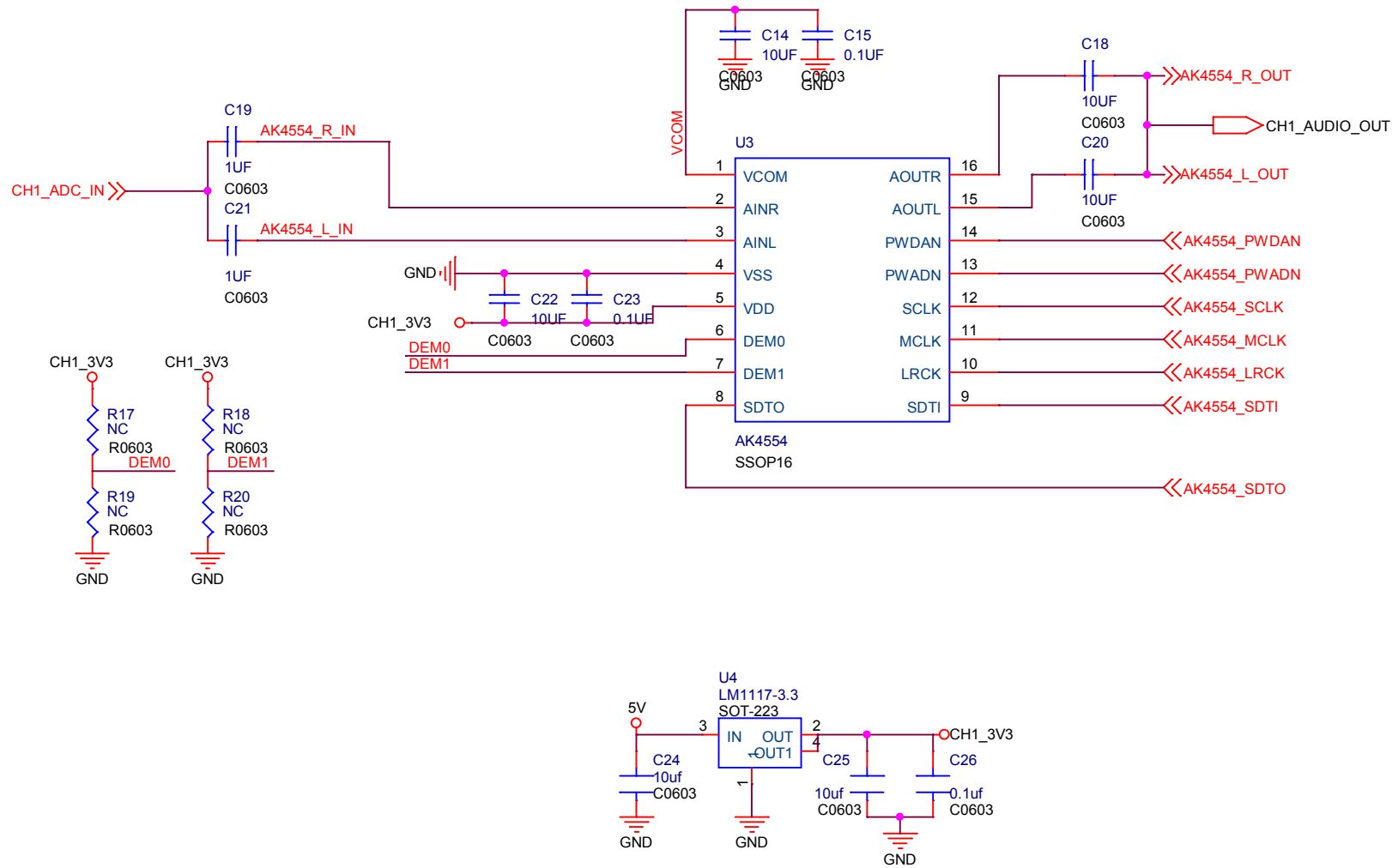
STM32F103VCT6



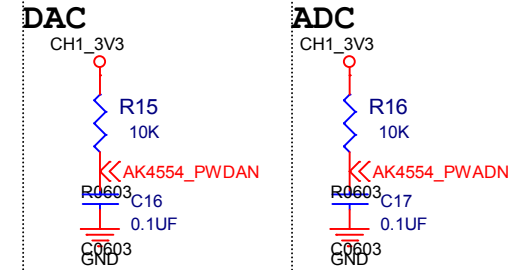
Title			CPU		
Size	Document Number				Rev
A3	01-CPU				3.0
Date:	Thursday, February 16, 2017	Sheet	3	of	18



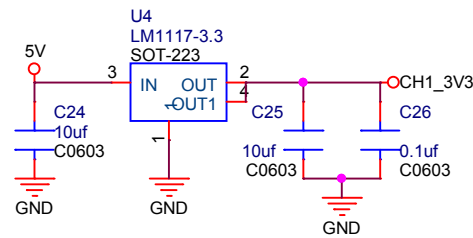
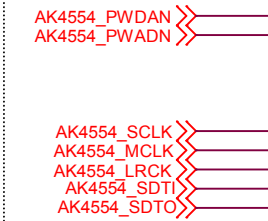
CODEC



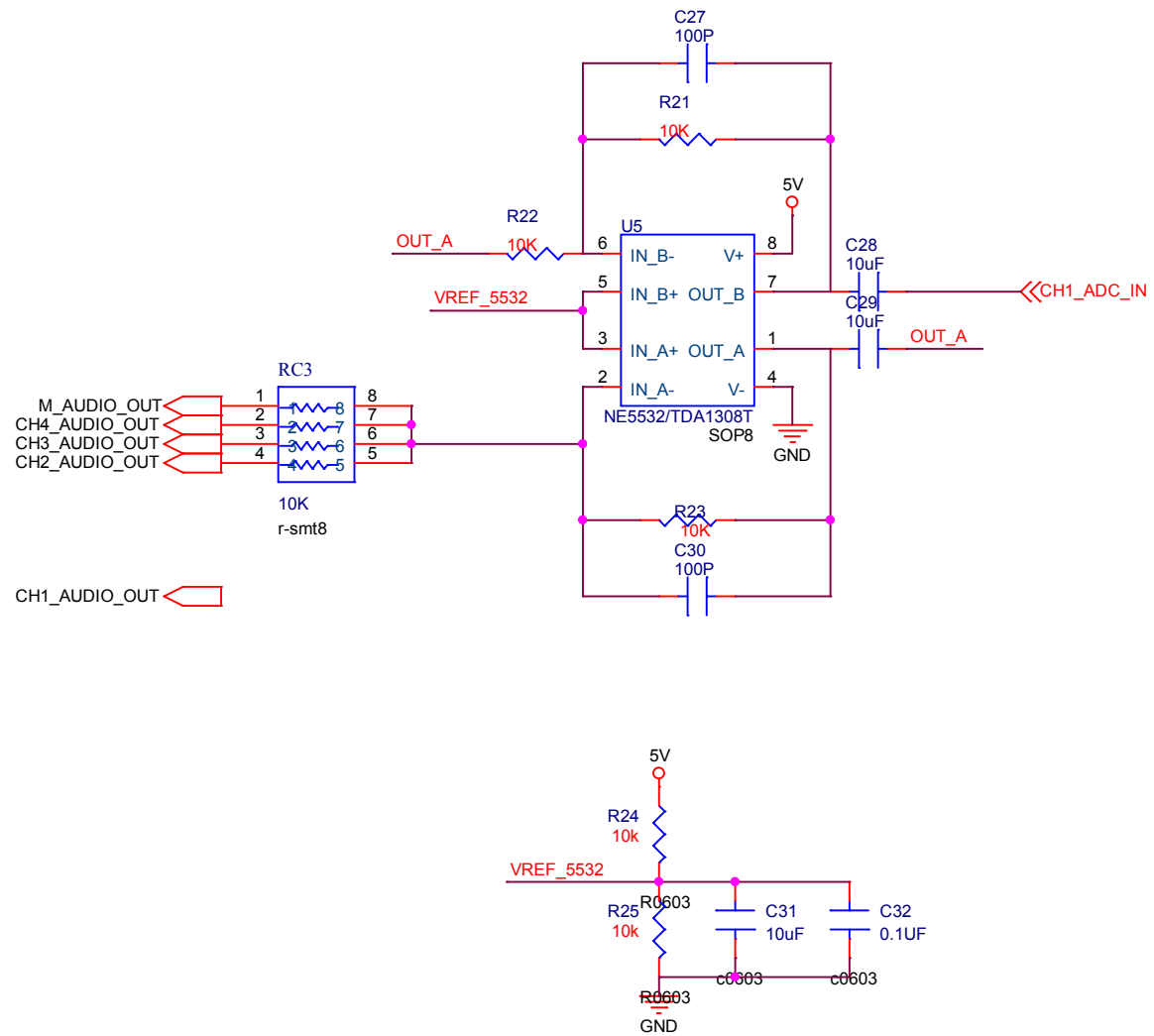
POWER DOWN



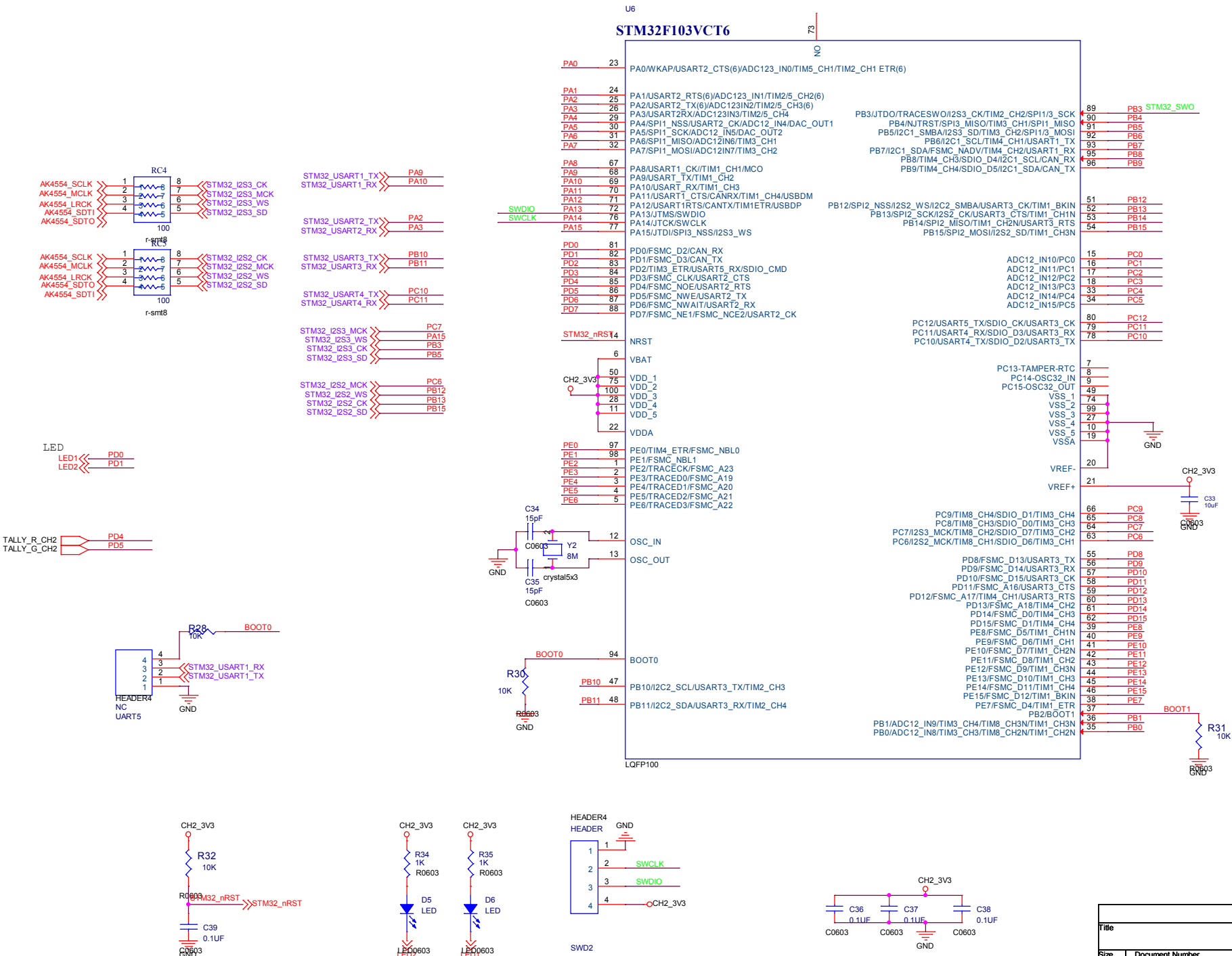
接STM32 I2S2 I2S3



Title <Title>			
Size A4	Document Number <Doc>		Rev <RevC>
Date:	Thursday, February 16, 2017	Sheet 5	of 18
2		1	

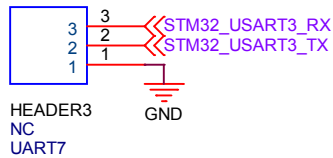
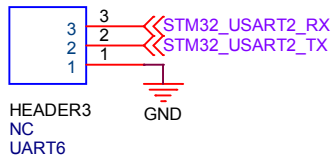
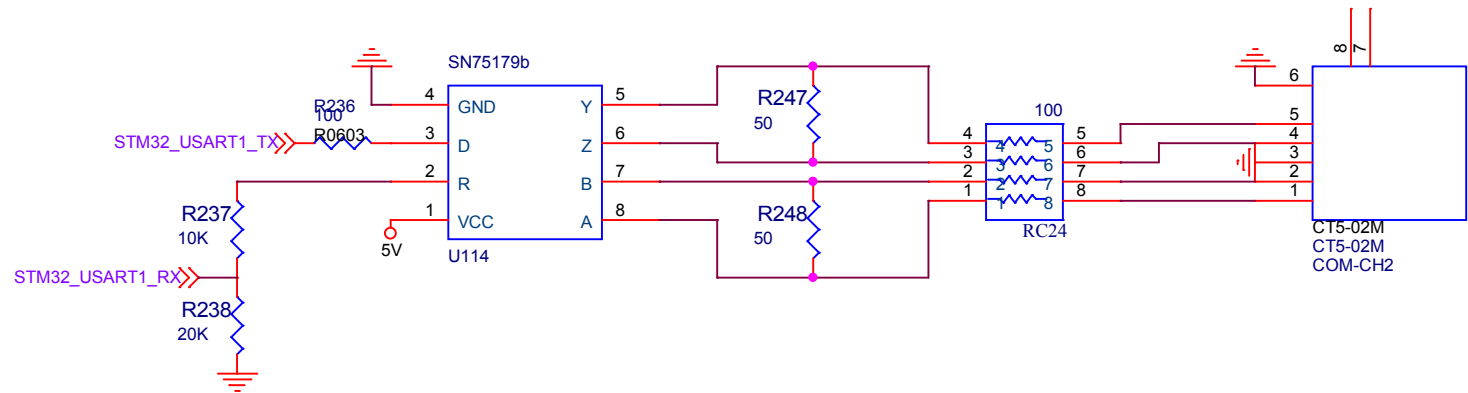


Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	<RevC>
Date:	Thursday, February 16, 2017	Sheet 6 of 18



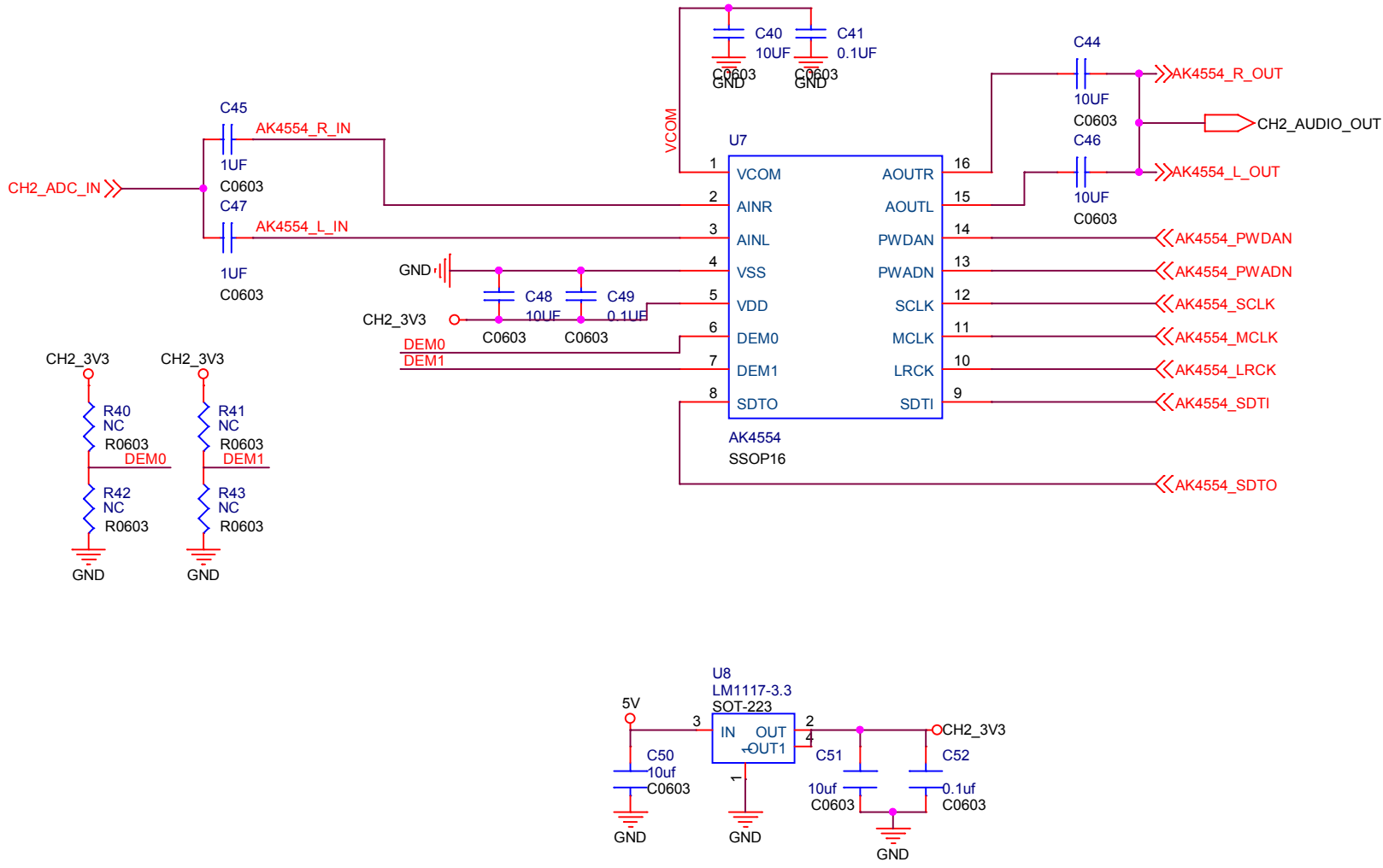
Title			CPU
Size	Document Number	Rev	
A3	01-CPU	3.0	
Date:	Thursday, February 16, 2017	Sheet	7 of 18

5芯卡侖-针



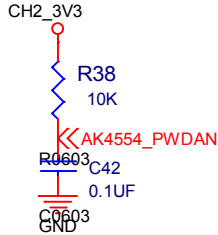
Title			
<Title>			
Size	Document Number		Rev
A4	<Doc>		<RevC>
Date:	Thursday, February 16, 2017	Sheet	8 of 18

CODEC

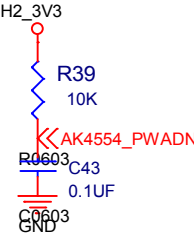


POWER DOWN

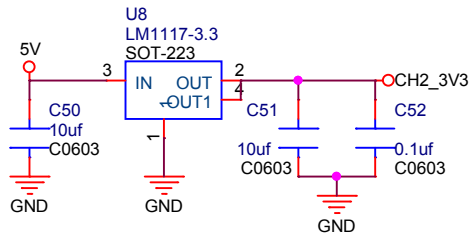
DAC

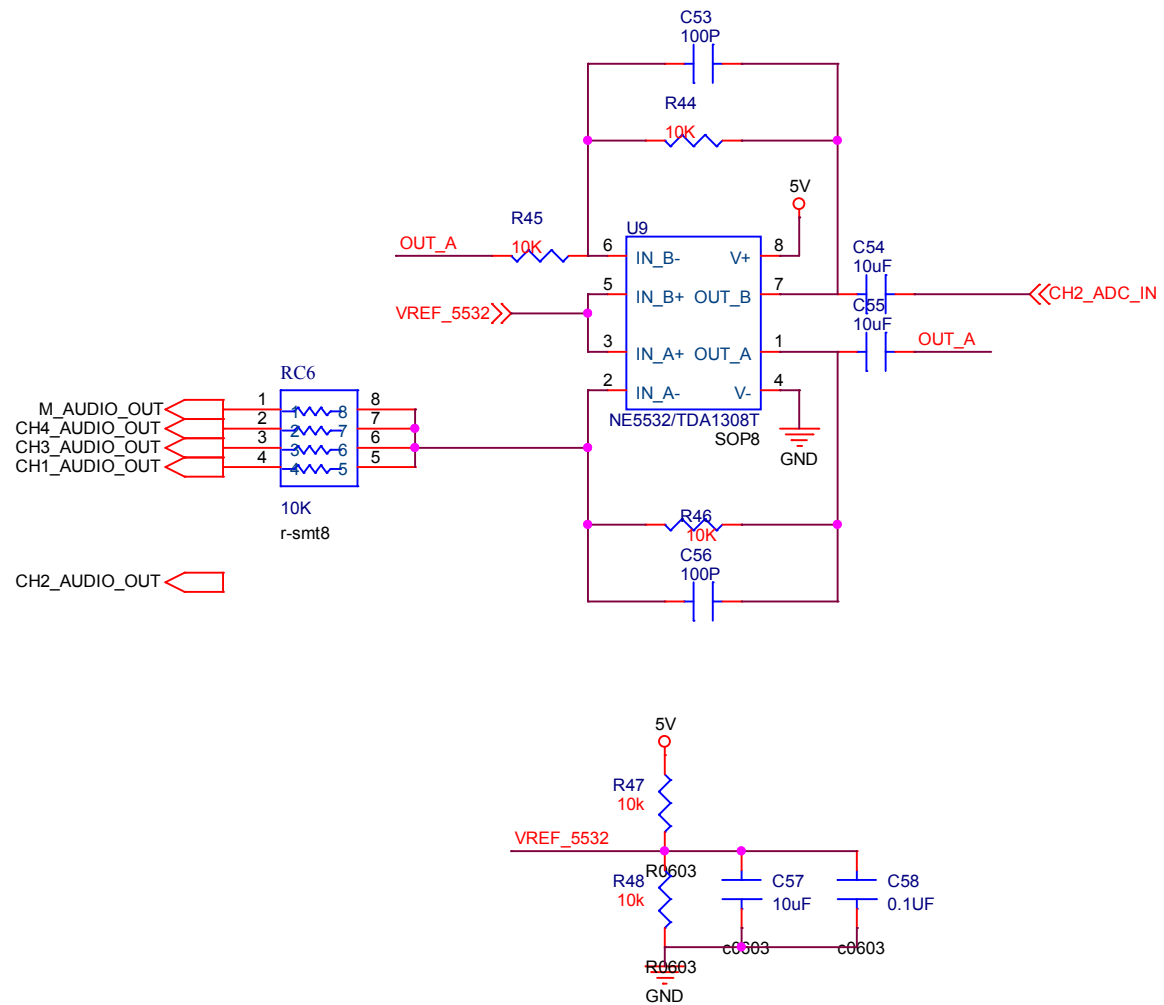


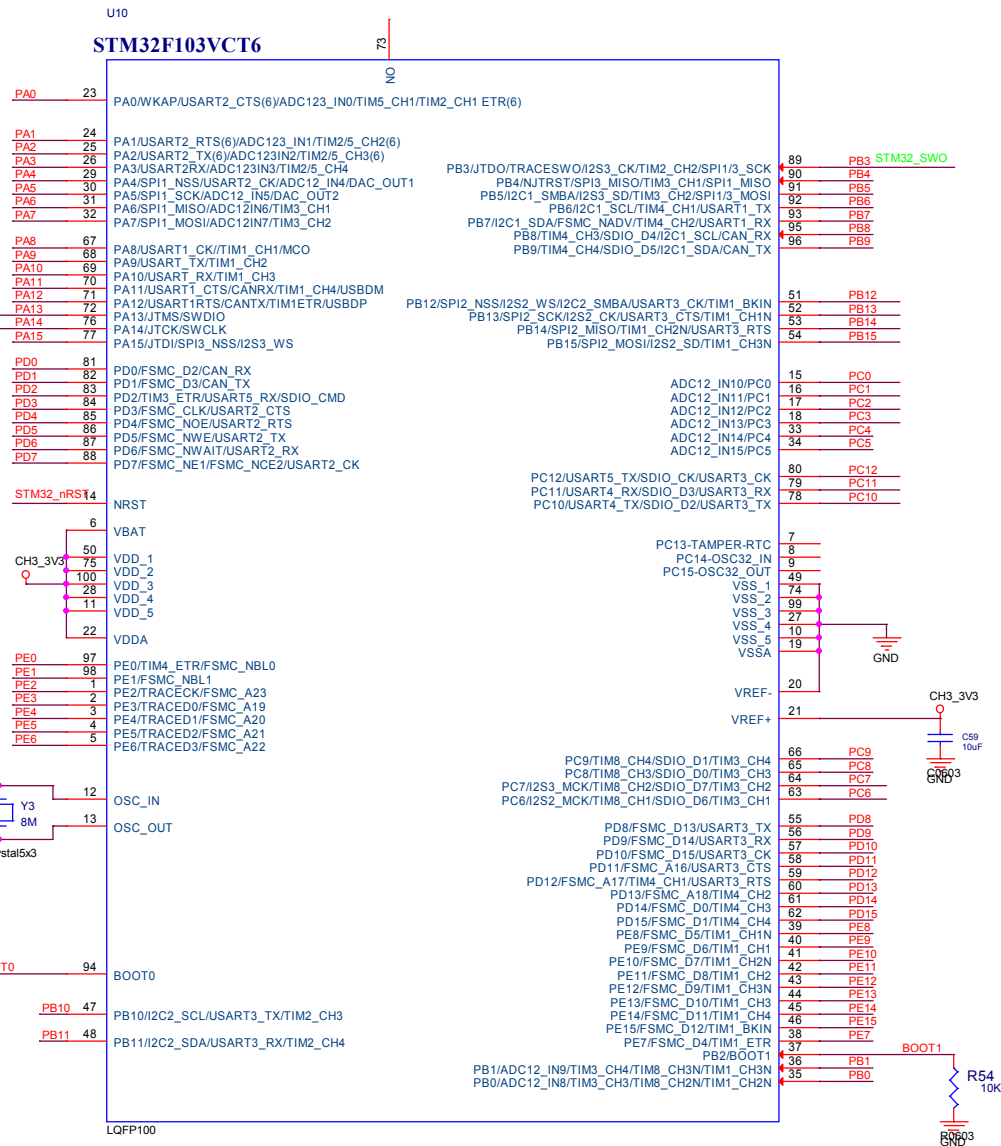
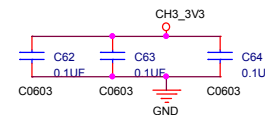
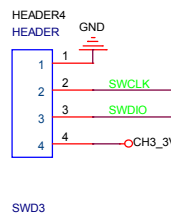
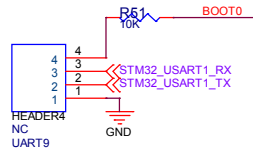
ADC



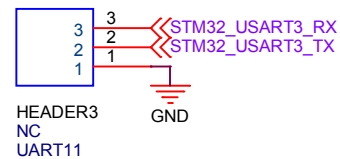
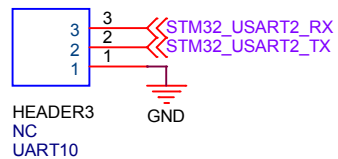
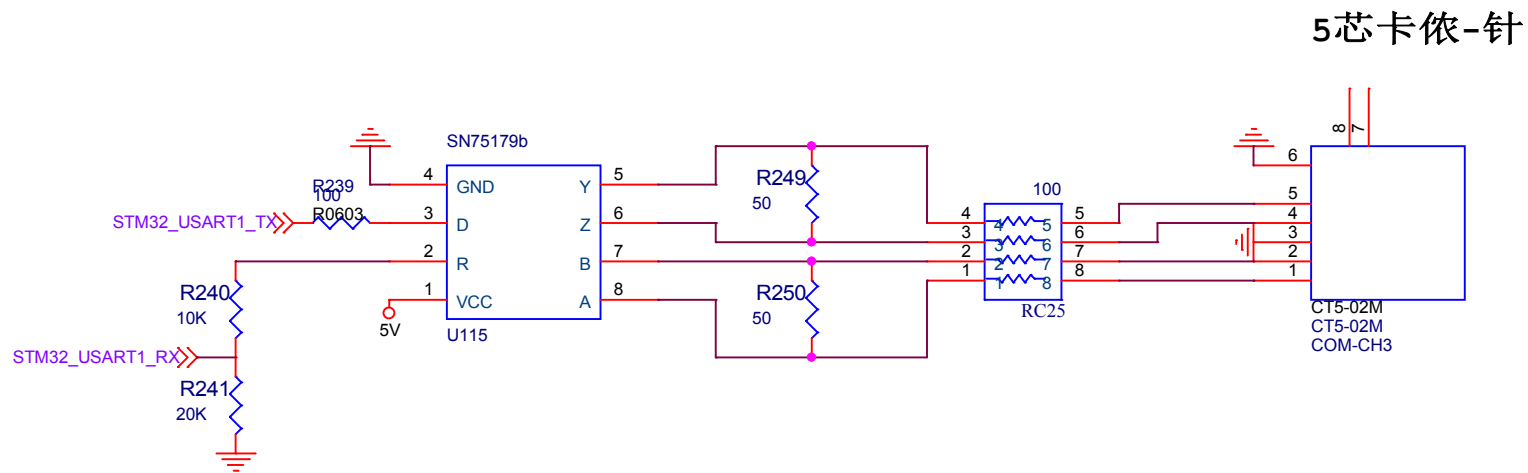
接STM32 I2S2 I2S3





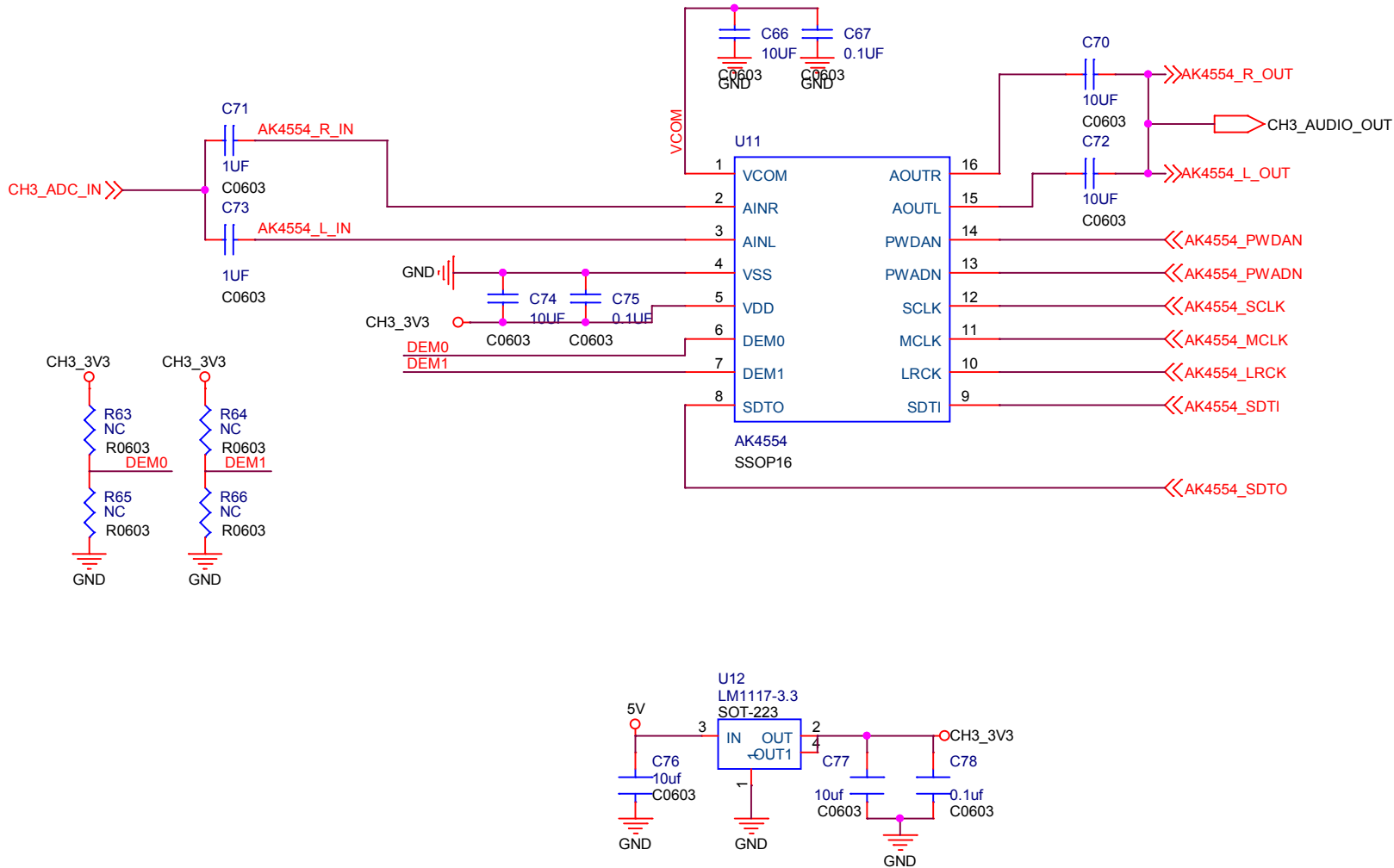


Title			
CPU			
Size A3	Document Number 01-CPU	Rev 3.0	
Date:	Thursday, February 16, 2017	Sheet	11 of 18



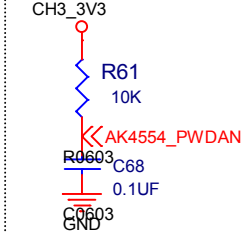
Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	<RevC>
Date: Thursday, February 16, 2017		
Sheet 12 of 18		

CODEC

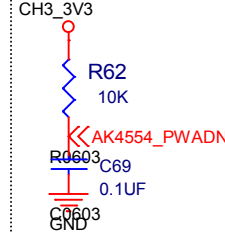


POWER DOWN

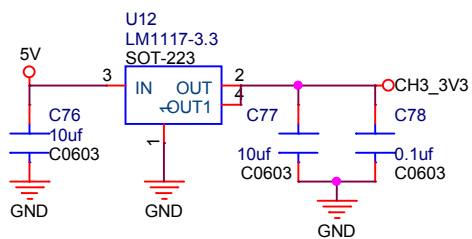
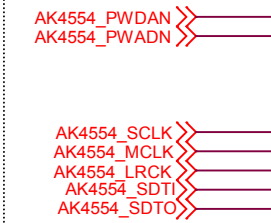
DAC

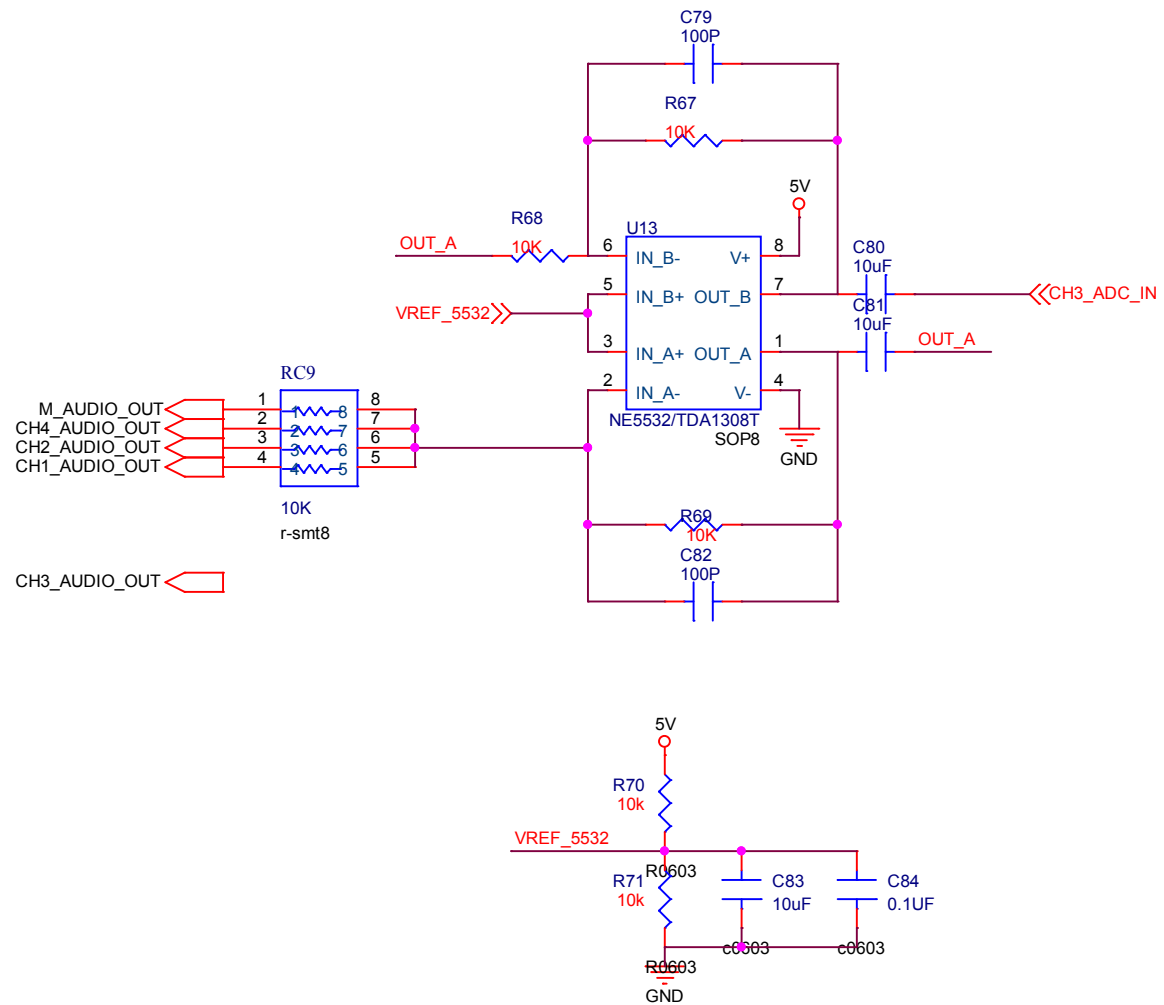


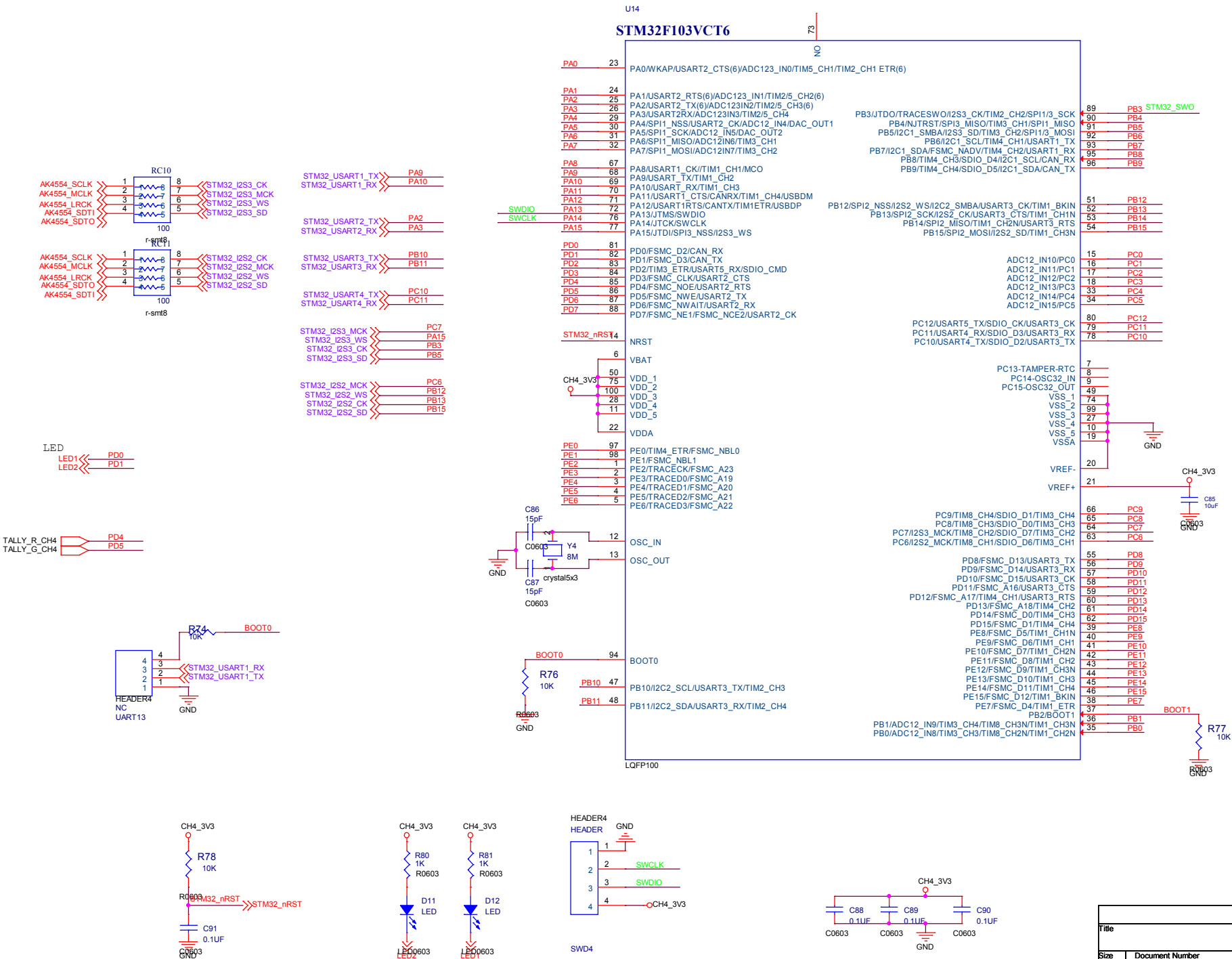
ADC



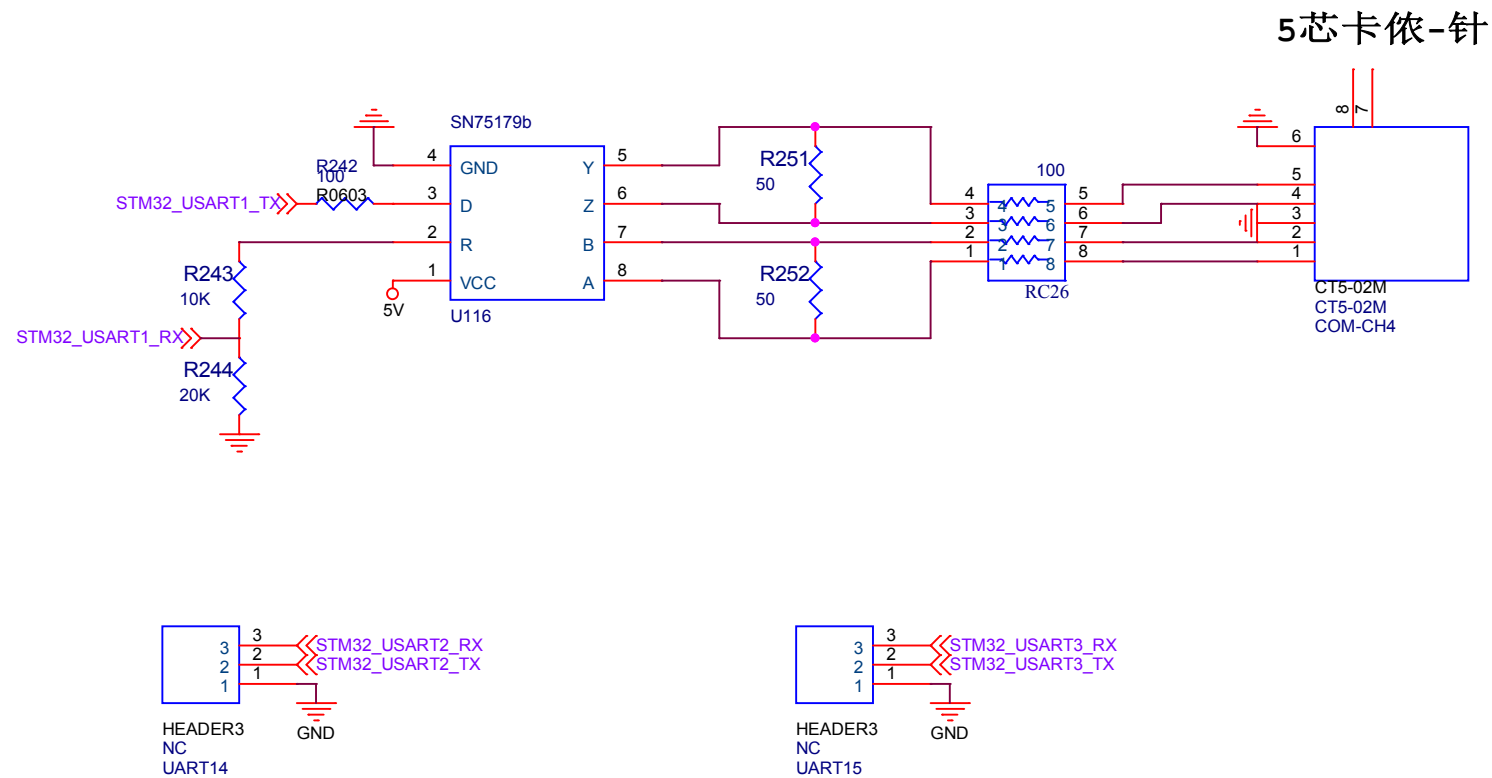
接STM32 I2S2 I2S3



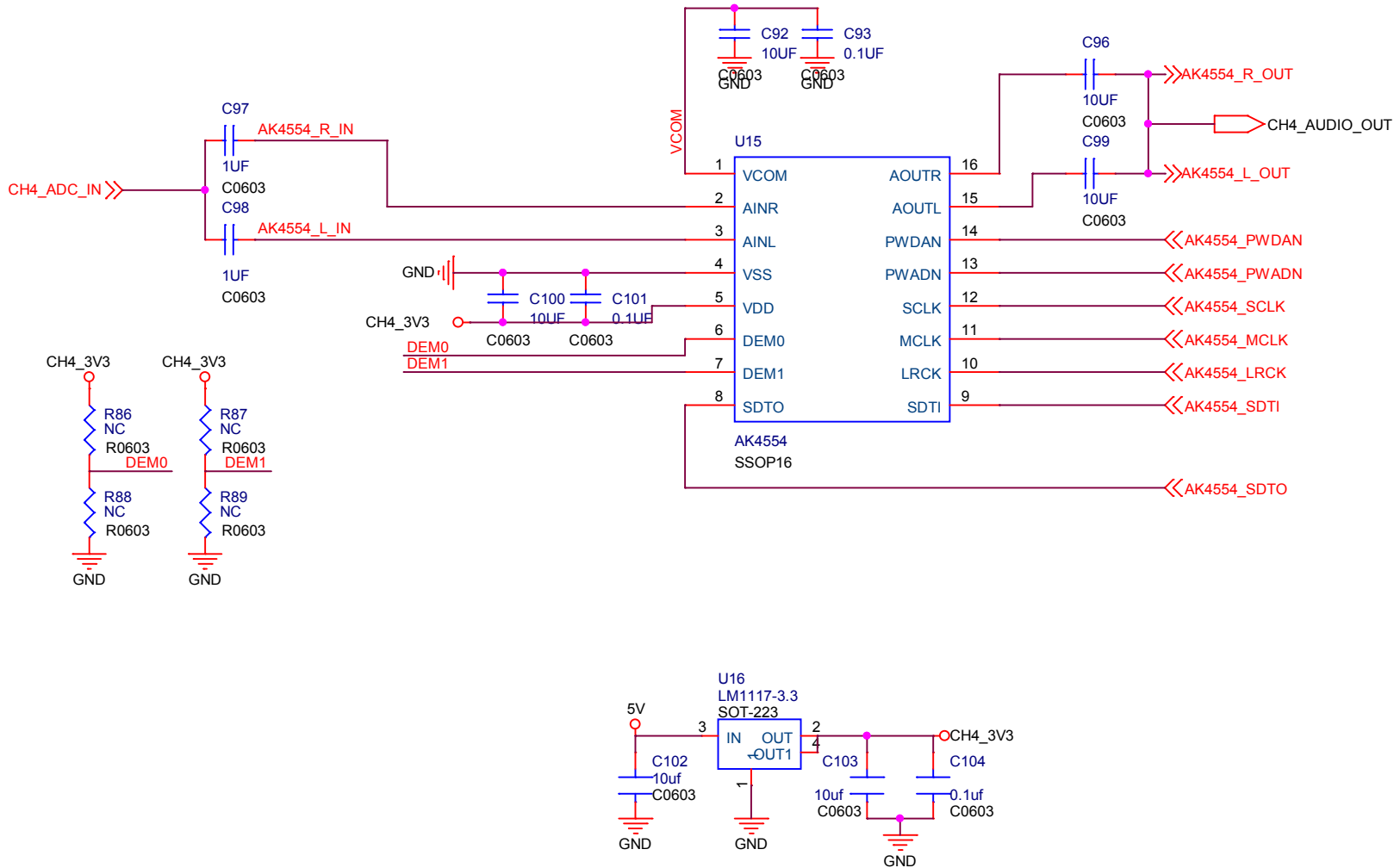




Title			CPU	Rev 3.0
Size	Document Number	01-CPU		
A3	Date: Thursday, February 16, 2017		Sheet 15 of 18	

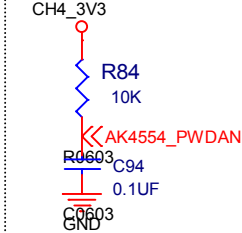


CODEC

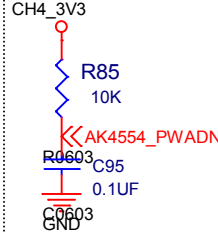


POWER DOWN

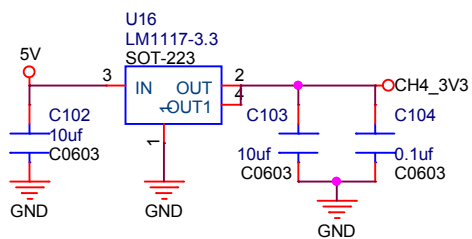
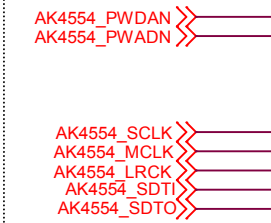
DAC

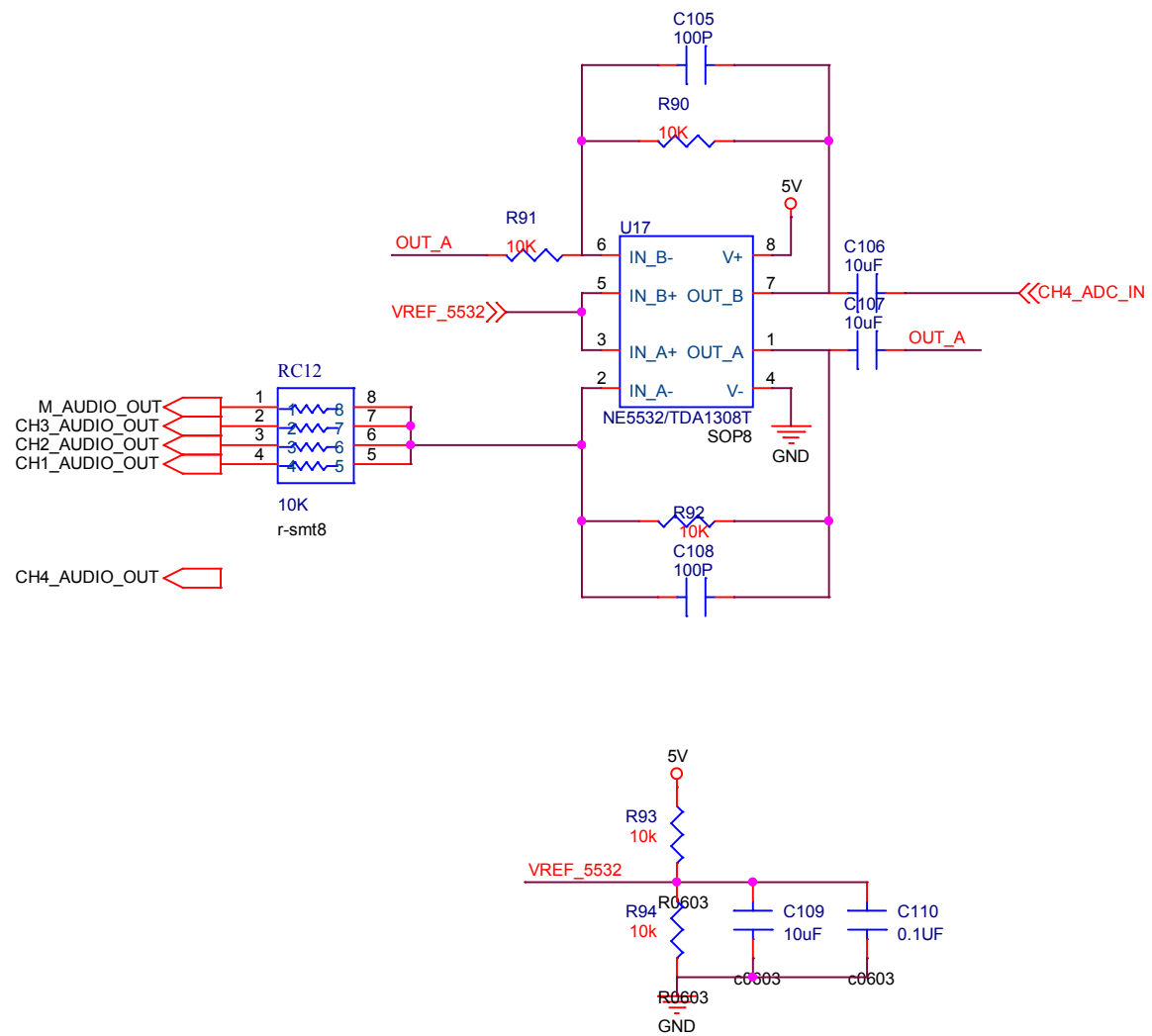


ADC



接STM32 I2S2 I2S3





Title		
<Title>		
Size	Document Number	Rev
A4	<Doc>	<RevC>
Date:	Thursday, February 16, 2017	Sheet 18 of 18