RegisterPlus_73

Register8_0

out_reg[7:0]
RST
C
D
Q
RTL_REG_SYNC

out[7:0]

out_i
S=1'b1 I0
S=default I1
O
S
RTL_MUX

clk
in[7:0]
load
rst

reset_reg
C
D
Q
RTL_REG

value_reg[7:0]
RST
C
CE
D
Q
RTL_REG_SYNC

save

TC_Register

RegisterPlus