# **Power Splitter/Combiner**

## 2 Way-0°/180°

## $50\Omega$

## 50 to 200 MHz

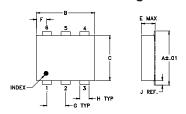
### **Maximum Ratings**

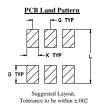
Operating Temperature	-40°C to 85°C					
Storage Temperature	-55°C to 100°C					
Power Input (as a splitter)	0.5W max.					
Internal Dissipation	0.125W max.					
Permanent damage may occur if any of these limits are exceeded						

#### **Pin Connections**

SUM PORT	3
PORT 1	6
PORT 2	4
PORT J	1
GROUND	2,5

## **Outline Drawing**

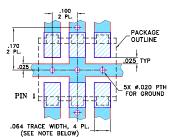




#### Outline Dimensions (inch mm)

Α	В	С	D	Е	F	G
.272	.310	.220	.100	.162	.055	.100
6.91	7.87	5.59	2.54	4.11	1.40	2.54
Н	J	K	L			wt
H .030	J .026	.065	.300			wt grams

#### Demo Board MCL P/N: TB-211 Suggested PCB Layout (PL-097)



NOTES: 1. TRACE WIDTH IS SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .030" ± .002"; COPPER: 1/2 02. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH MAY NEED TO BE MODIFIED. 2. BOTTOM SIDE OF THE POB IS CONTINUOUS GROUND PLANE.

DENOTES PICE COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)

#### DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

#### **Features**

- low insertion S-1 and S-2, 0.25 dB typ; J-1 and J-2, 0.8 dB typ.
- very good input VSWR, 1.10 typ. and good output VSWR, 1.12 typ.
- excellent amplitude unbalance, 0.1 dB typ.
- excellent phase unbalance, 1 deg. typ.
- high isolation S-J ports and 1-2 ports, 35 dB typ.
- protected under US Patent 6,133,525

#### **Applications**

- satellite
- IF receiver

# Devices/Reel 20, 50, 100, 200 500, 1000

Generic photo used for illustration purposes only

CASE STYLE: CD636

+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site

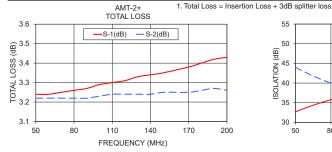
for RoHS Compliance methodologies and qualifications

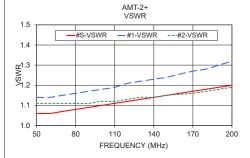
#### **Electrical Specifications**

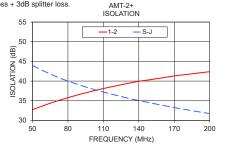
FREQ. RANGE (MHz)	ISOLATION (dB)	INSERTION LOSS (dB) ABOVE 3.0 dB	PHASE UNBALANCE (Degrees)	AMPLITUDE UNBALANCE (dB)				
f <sub>L</sub> -f <sub>U</sub>	Typ. Min.	Тур. Мах.	Max.	Max.				
50-200	35 20	0.8 1.2	2	0.3				

#### Typical Performance Data

Freq. (MHz)			Amplitude Unbal. (dB)		on Loss IB)	Amplitude Unbal. (dB)					Phase Unbal. (deg.)		VSWR S	VSWR 1	VSWR 2
	S-1	S-2	(S-1)-(S-2)	J-1	J-2	(J-1)-(J-2)	1-2	S-J	(S-1)-(S-2)	(J-1)-(J-2)					
50.00 60.00 70.00 80.00 90.00	3.24 3.24 3.25 3.26 3.27 3.29	3.22 3.22 3.22 3.22 3.22 3.23	0.02 0.02 0.03 0.04 0.05	3.76 3.76 3.77 3.77 3.77	3.77 3.77 3.77 3.77 3.78 3.79	0.00 0.00 0.01 0.01 0.02 0.01	32.71 33.84 34.86 35.78 36.61 37.39	44.00 42.49 41.16 40.00 38.99 38.05	0.05 0.06 0.02 0.05 0.04	179.87 179.87 179.81 179.80 179.79	1.06 1.06 1.07 1.08 1.09	1.14 1.14 1.15 1.16 1.17	1.11 1.11 1.11 1.11 1.11 1.12		
110.00 120.00 130.00 140.00	3.30 3.31 3.33 3.34	3.24 3.24 3.24 3.24	0.06 0.07 0.09 0.10	3.78 3.80 3.80 3.81	3.81 3.82 3.82 3.84	0.02 0.02 0.02 0.03	38.10 38.74 39.36 39.94	37.21 36.45 35.71 35.04	0.05 0.02 0.00 0.02	179.79 179.75 179.73 179.69	1.11 1.12 1.13 1.14	1.19 1.21 1.22 1.23	1.12 1.13 1.14 1.14		
150.00 170.00 180.00 190.00 200.00	3.35 3.38 3.40 3.42 3.43	3.25 3.25 3.26 3.27 3.26	0.11 0.13 0.14 0.16 0.17	3.81 3.82 3.83 3.84 3.85	3.85 3.87 3.88 3.90 3.91	0.03 0.05 0.05 0.06 0.06	40.36 41.30 41.67 42.03 42.39	34.44 33.27 32.75 32.22 31.74	0.02 0.09 0.06 0.10 0.15	179.68 179.72 179.68 179.68 179.68	1.15 1.17 1.18 1.19 1.20	1.24 1.27 1.28 1.30 1.32	1.15 1.16 1.17 1.18 1.19		







#### electrical schematic



- S-J ports, isolation 40 typical
- Inphase ports, S-1 and S-2 insertion loss 0.2 dB typical
   Amplitude unbalance defined by input S or J ports to output 1 and 2

- Notes
  A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.

  B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.

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