

Frequ\_level\_of\_littl\_Socket (X\_0) over Frequ\_level\_of\_littl\_Socket (X\_0),Core\_0\_state (X\_1),Core\_1\_state (X\_2),  
Core\_2\_state (X\_3),Core\_3\_state (X\_4),Core\_4\_state (X\_5),  
Core\_5\_state (X\_6), Medium\_Socket\_State&Frequ (X\_7),  
Big\_Socket\_State&Frequ (X\_8).

