

Medium_Socket_State&Freque (X_7) over Frequ_level_of_littl_Socket (X_0),Core_0_state (X_2),
Core_2_state (X_3),Core_3_state (X_4),Core_4_state (X_5),
Core_5_state (X_6), Medium_Socket_State&Freque (X_7),
Big_Socket_State&Freque (X_8).

