

JZ2440 V3 SCHEMATIC

SD_JTAG
SD_JTAG.Sch



Camera
Camera.Sch



lcd
lcd.Sch



CPU1.Sch
CPU1.Sch



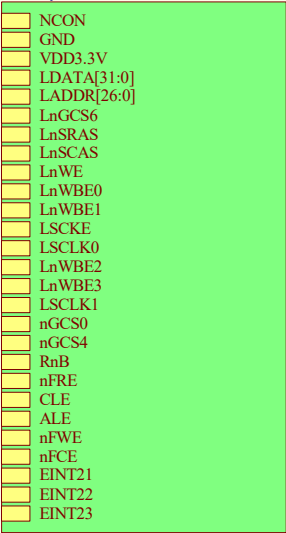
Power
Power.Sch



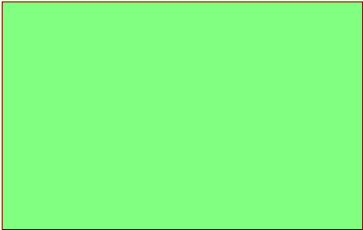
USB
USB.Sch



Memory
Memory.sch



CPU2.Sch
CPU2.Sch



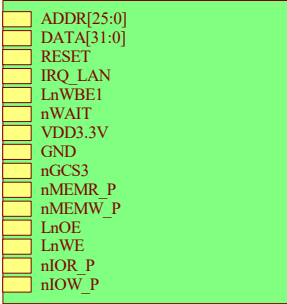
Port
Port.sch



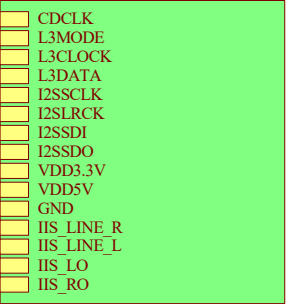
Connect
Connect.Sch



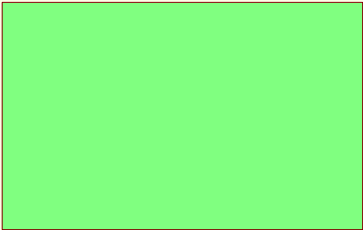
Net
Net.sch



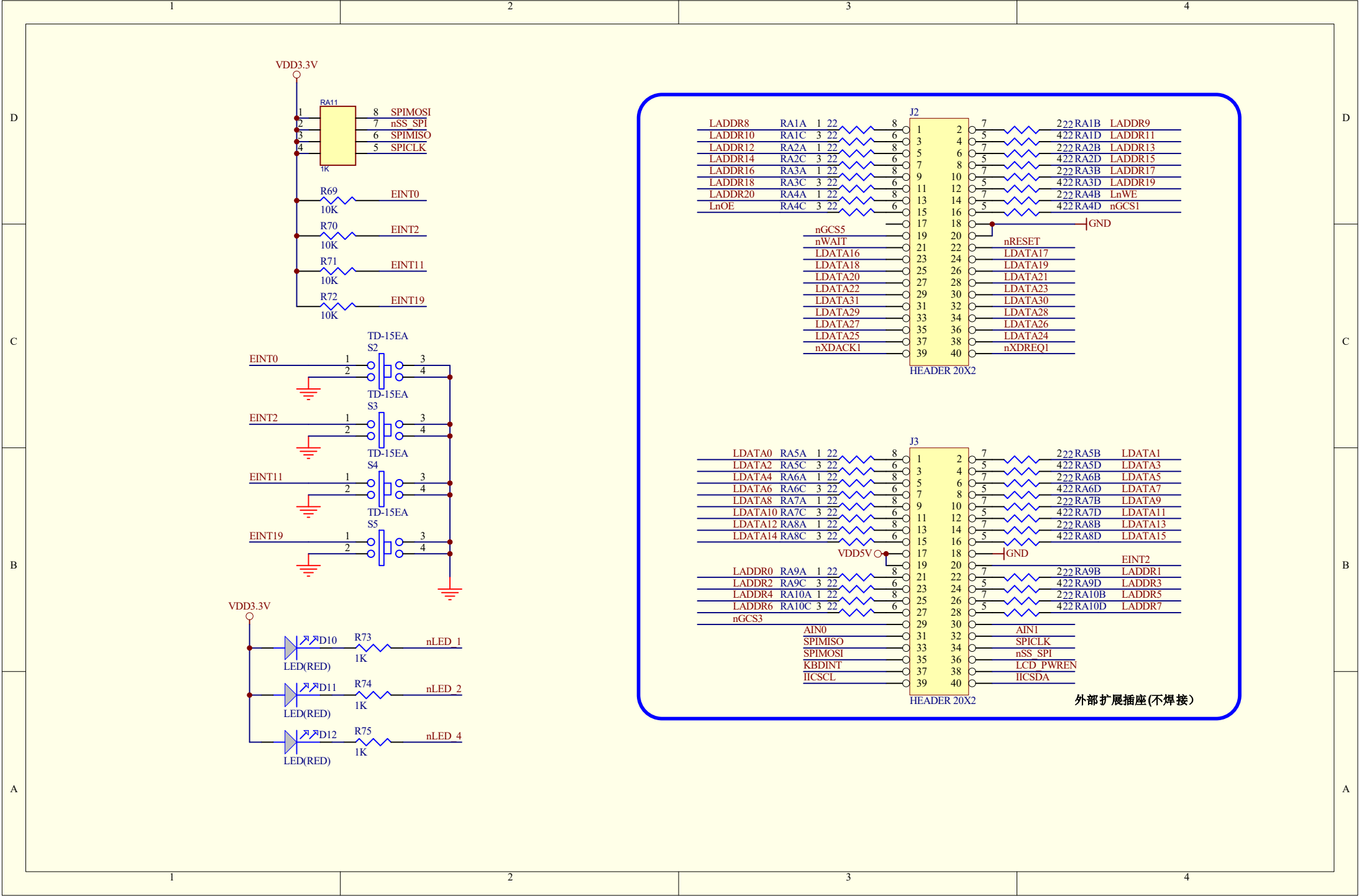
media
media.sch

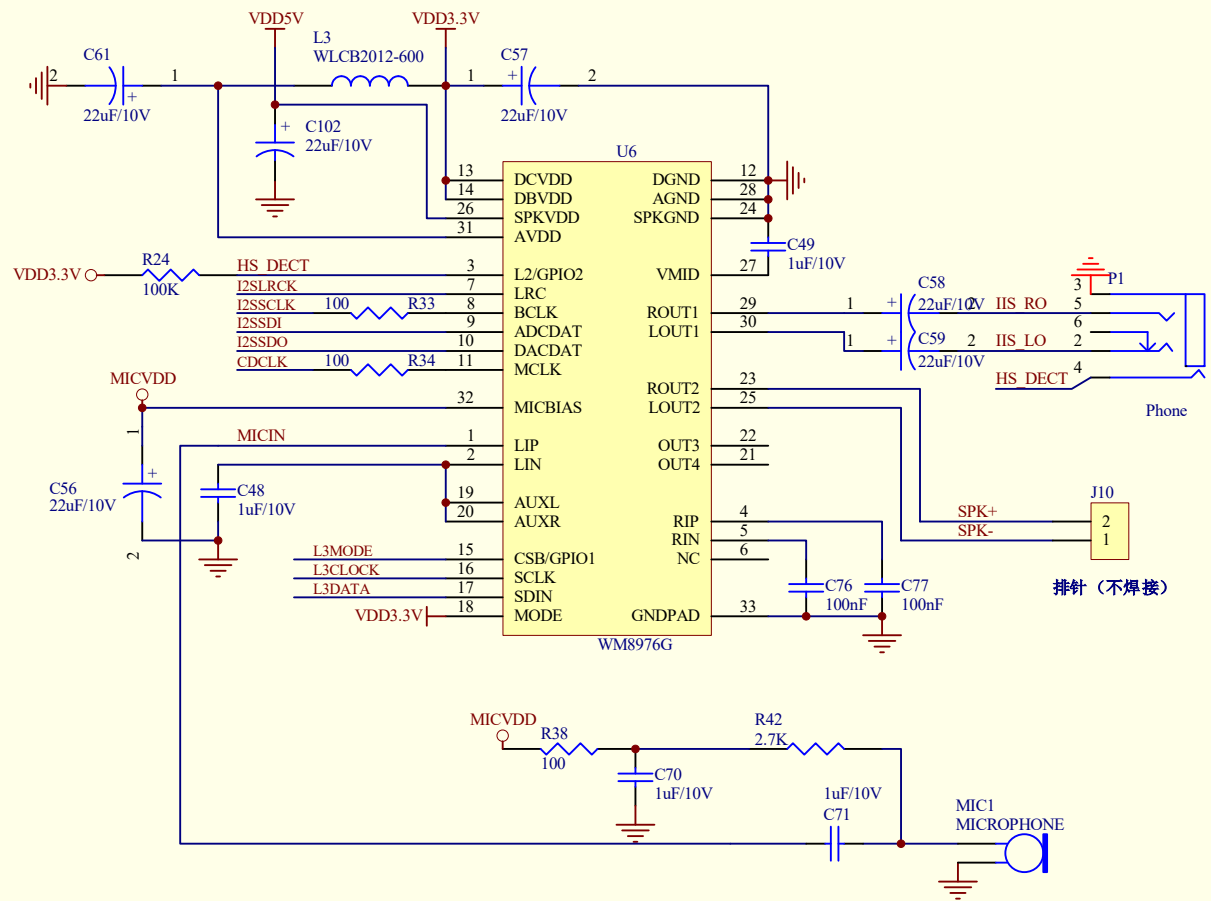


CPU3.Sch
CPU3.Sch

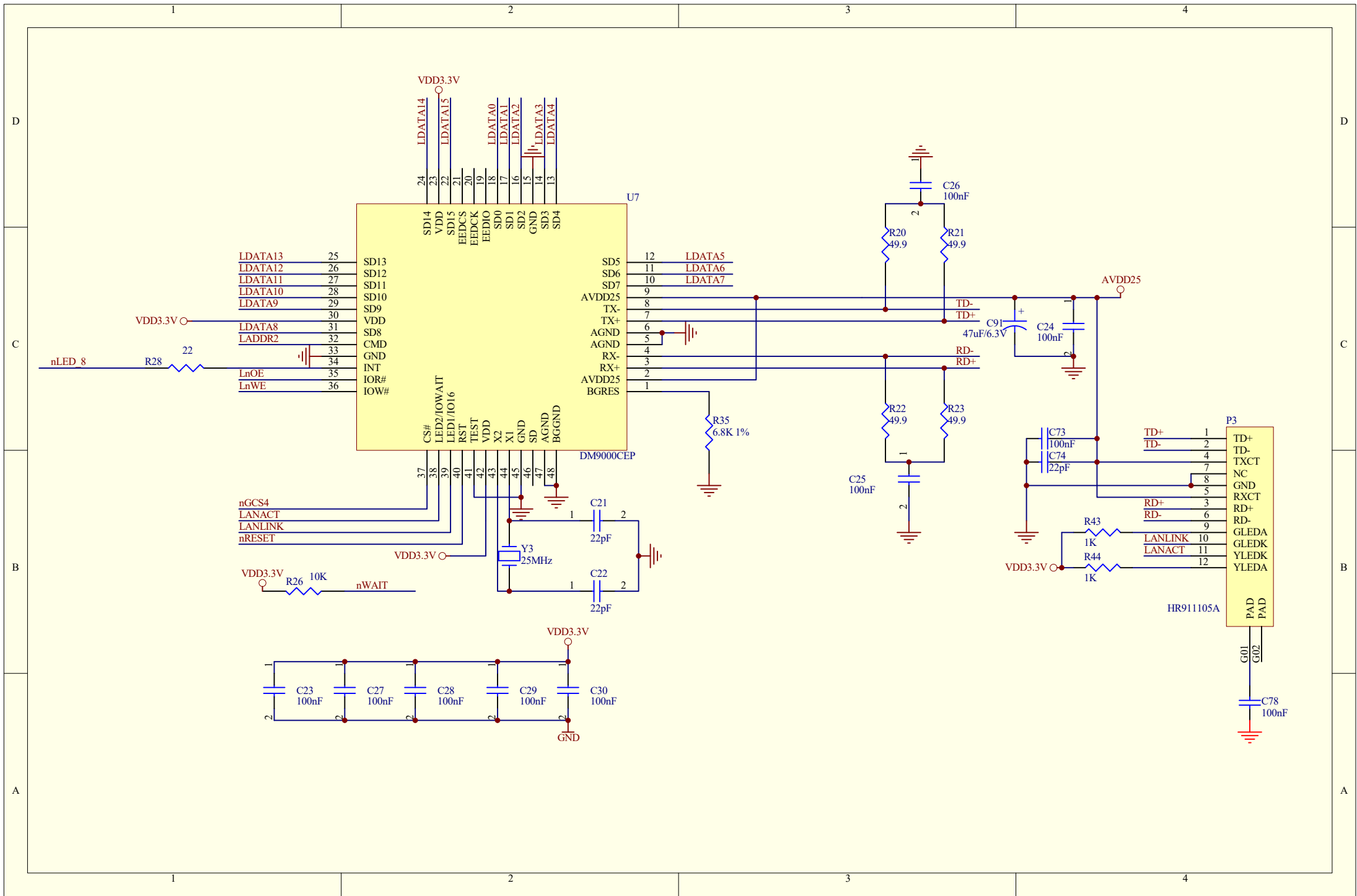


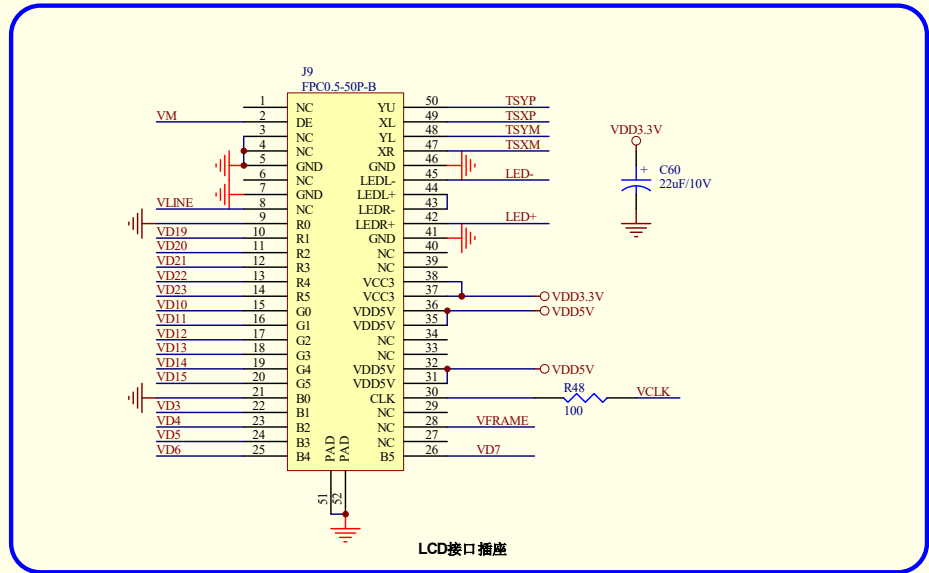
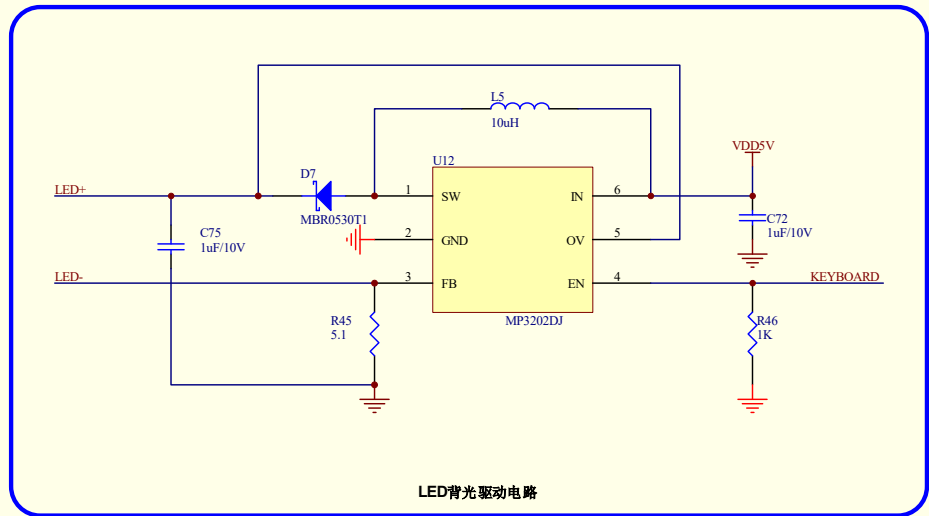
10K(NC)表示这个电阻不焊接

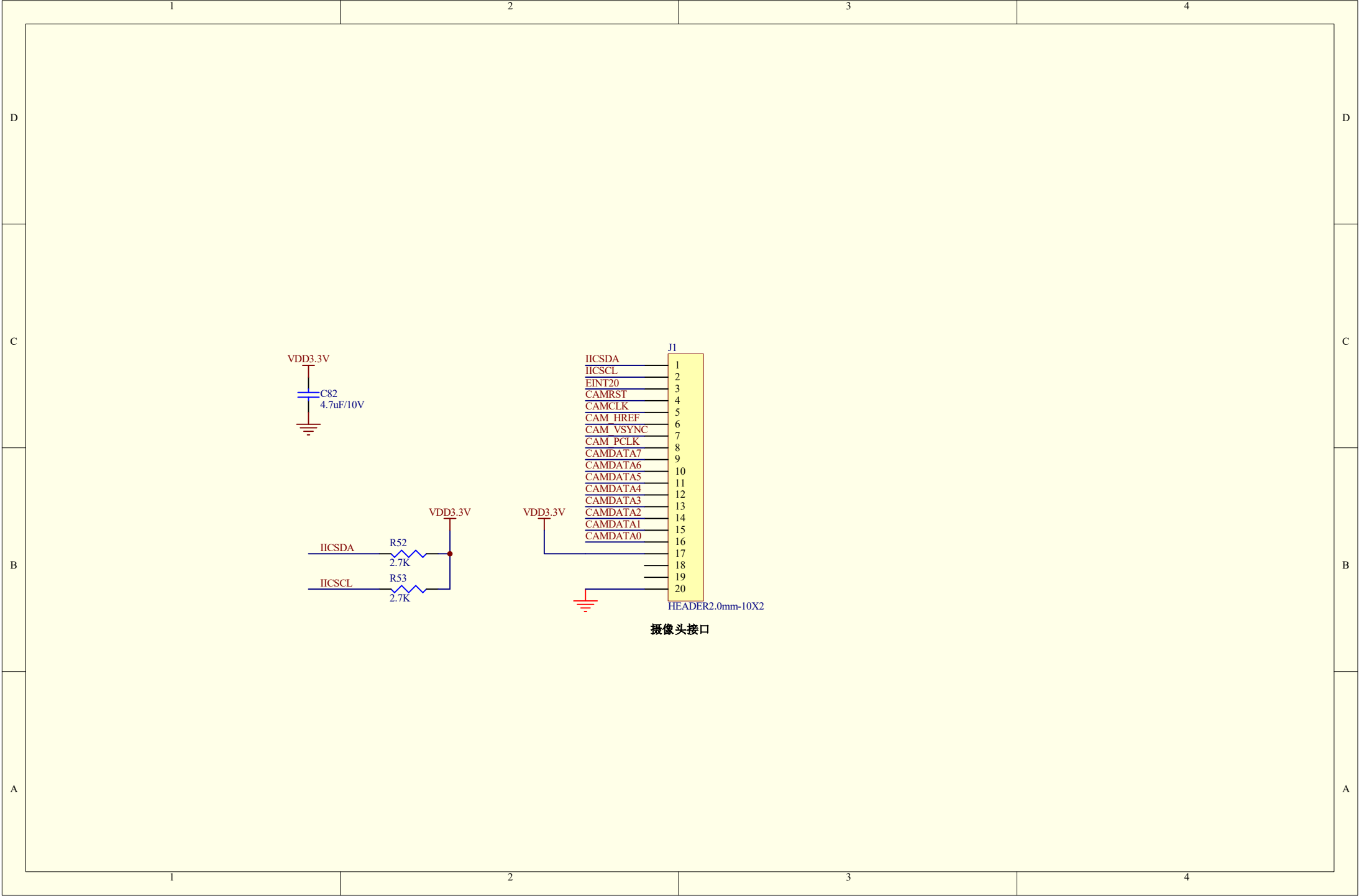


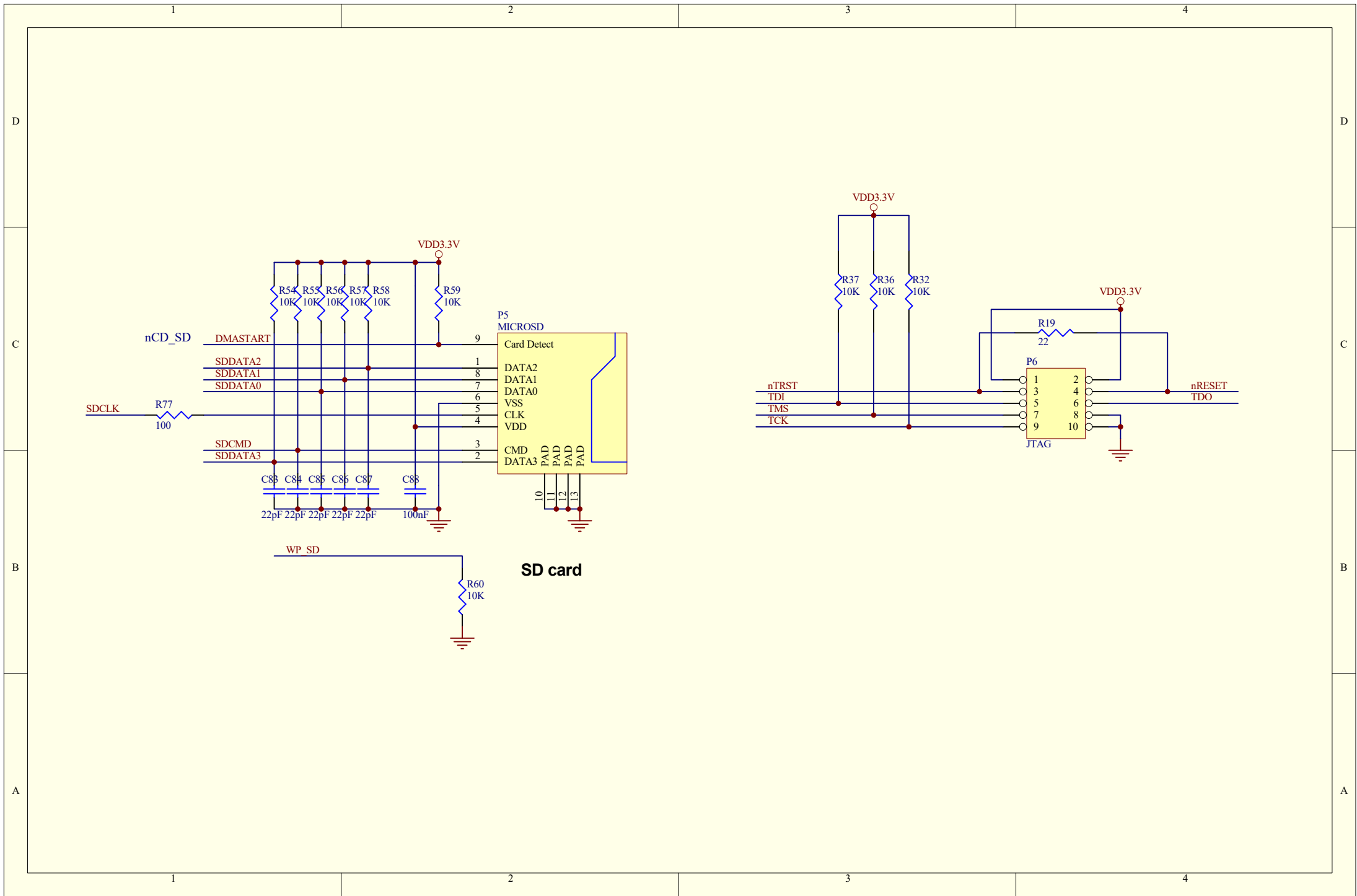


音频输入输出

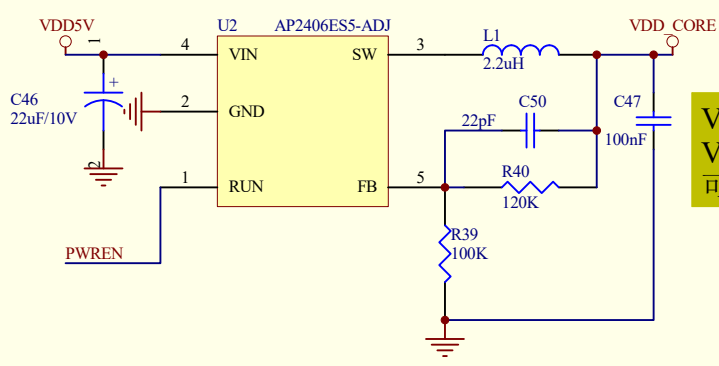
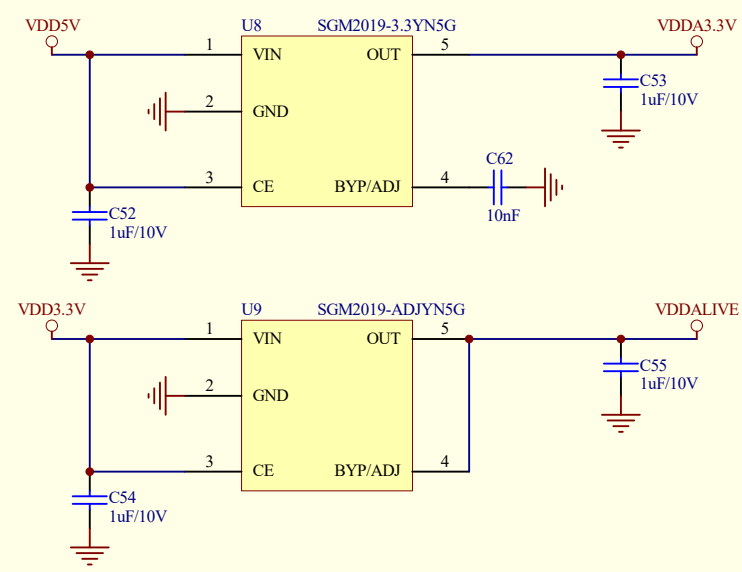
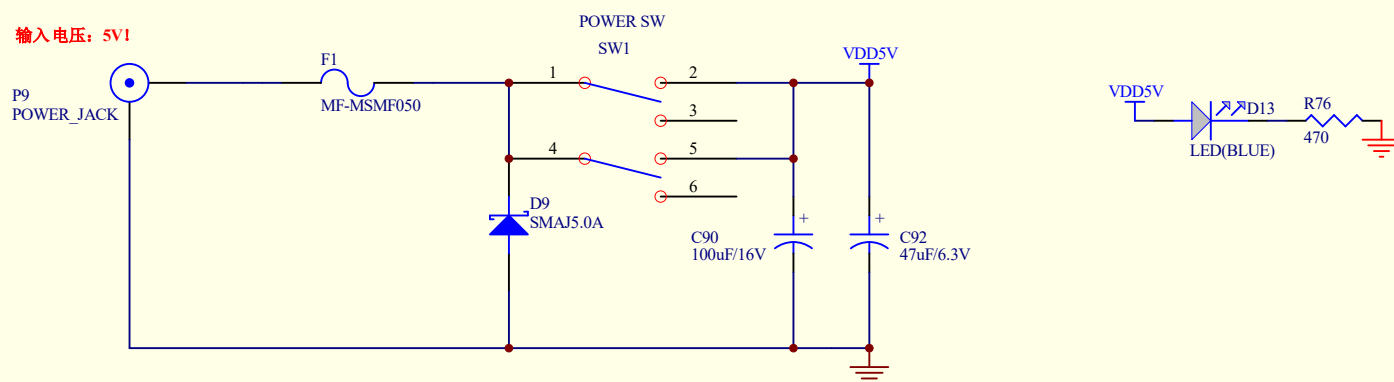




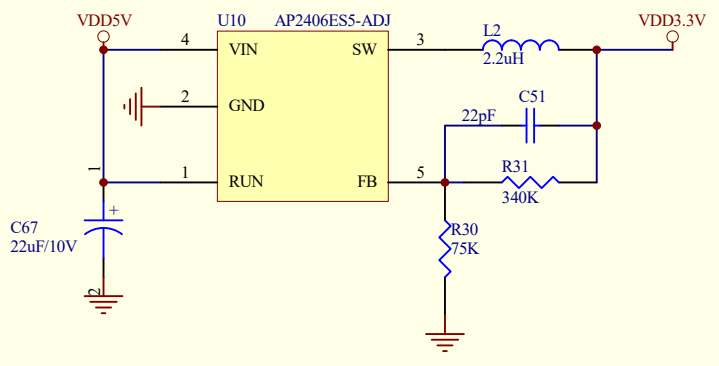




输入电压: 5V!



VDD_CORE电压为1.3V
VDD_CORE在CPU睡眠期间
可以通过软件控制PWREN关闭



DC/DC电源和LDO电源

