|  |  |  |
| --- | --- | --- |
| Offset | name | des |
| 0x008h | PMU\_SOCLDO\_UP\_SETTINGS |  |

|  |  |  |  |
| --- | --- | --- | --- |
| PMU\_SOCLDO\_UP\_SETTINGS | | 0x008h | |
| 0x0064\_012c | | | |
| des | | | |
| Bits | Reset value | R/W | Description |
| 31:16 | / | / | / |
| 24:16 | ‘d100 | R/W | SOCLDO\_TIMING\_SETTINGS[8:0] |
| des |
| 15:0 | ‘d300 | R/W | SOCLDO\_\_UP\_SETTINGS[15:0] |
| dss |

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| --- | --- | --- | --- |
| PMU\_SOCLDO\_UP\_SETTINGS | | 0x008h | |
| 0x0064\_012c | | | |
| des | | | |
| Bits | Reset value | R/W | Description |
| 24:16 | ‘d100 | R/W | SOCLDO\_TIMING\_SETTINGS[8:0] |
| des |
| 15:0 | ‘d300 | R/W | SOCLDO\_\_UP\_SETTINGS[15:0] |
| des |