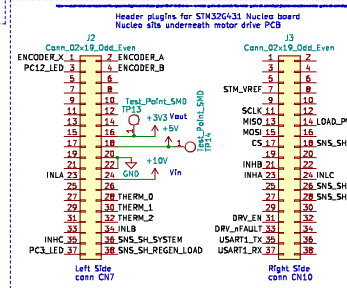
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Figure 1: Schematic diagram of the test circuit for the LED array. The circuit shows four columns of LEDs connected to a common ground (GND). The first column has two LEDs (D7, D8) connected to PC12_LED. The second column has two LEDs (D7, D8) connected to PC3_LED. The third column has two LEDs (D7, D8) connected to +10V. The fourth column has two LEDs (D6, D7) connected to +5V. The test points are labeled: TP27 (D7), TP28 (D8), TP29 (D6), TP30 (D7), TP31 (D6), and TP32 (D7). The test points are connected to the test equipment: TP27 to INA, TP28 to INB, TP29 to INB, TP30 to INB, TP31 to INB, and TP32 to GLC.

The diagram illustrates the ESD protection circuit for motor connector pins H1, H2, and H3. Each pin is connected to a unidirectional TVS diode. The cathode of each diode is connected to a common ground (GND), and the anode is connected to the respective pin (PH.C, PH.B, PH.A). The diodes are labeled D_TVS_35V. The motor connector pins are labeled MotorConnector_Ped.

The schematic diagram illustrates the test board layout. It features three test points (TP1, TP3, TP5) and their connections to the board components. TP1 is connected to Test_Point_SMD and Test_Point_S40. TP3 is connected to Test_Point_SMD and Test_Point_S40. TP5 is connected to Test_Point_SMD + 3V3 and Test_Point_S40. The board also shows other components like resistors R16, R18, R21, capacitors C16, C10, C33, and thermistors THERM_0, THERM_1, THERM_2.

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