Quad 2-Input NAND Gates

FEATURES

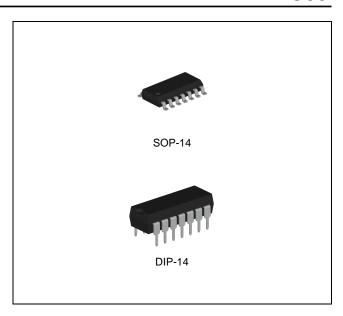
- Wide Operating Voltage Range of 2.0V to 6.0V
- · Outputs Can Drive up to 10 LSTTL Loads
- Low Power Consumption, 20μA Maximum I_{CC}
- Typical t_{pd}: 8ns
- ±4mA Output Drive at 5.0V
- Low Input Current of 1µA Maximum

APPLICATIONS

- AV Receivers
- Portable Audio Docks
- · Blu-ray Players and Home Theater
- · Wireless Devices



The 74HC00 contain four independent, 2-input NAND gates. They perform the Boolean function $Y = A \times B$ or Y = A + B in positive logic. Inputs include clamp diodes.



ORDERING INFORMATION

Device	Package
74HC00D	SOP-14
74HC00N	DIP-14

ABSOLUTE MAXIMUM RATINGS (Note 1)

CHARACTER	RISTIC	SYMBOL	MIN.	MAX.	UNIT
DC Supply Voltage		Vcc	-0.5	7	V
Input Clamp Current (Note 2)	$V_I < 0 \text{ or } V_I > V_{CC}$	I _{IK}	-	±20	mA
Output Clamp Current (Note 2)	V ₀ < 0	I _{OK}	-	±20	mA
Continuous Output Current	$V_{\rm O} = 0$ to $V_{\rm CC}$	I _{IN}	-	±25	mA
Continuous Current through V _{CC}	or GND		-	±50	mA
Maximum Junction Temperature		TJ	-	150	°C
Storage Temperature		T _{STG}	-65	150	°C

Note 1. Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Note 2. The input and output negative-voltage ratings may be exceeded if the input and output clamp current ratings are observed.

RECOMMENDED OPERATING CONDITIONS (Note 3)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Supply Voltage	V _{CC}	2	6	V
DC Input Voltage	V _{IN}	0	Vcc	V
DC Output Voltage	V _{OUT}	0	Vcc	V
Operating Free-Air Temperature Range	T _A	-40	85	°C

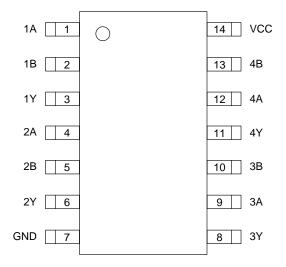
74HC00

ORDERING INFORMATION

Package	Order No.	Description	Supplied As	Status
SOP-14	74HC00D	Quad 2-Input NAND Gates	Tape & Reel	Active
DIP-14	74HC00N	Quad 2-Input NAND Gates	Tube	Contact Us

Note 3. The device is not guaranteed to function outside its operating ratings.

PIN CONFIGURATION

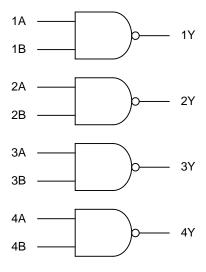


SOP-14 / DIP-14

PIN DESCRIPTION

Pin	No.	5: N	B: F :		
SOP-14	DIP-14	Pin Name	Pin Function		
1	1	1A	Input 1A		
2	2	1B	Input 1B		
3	3	1Y	Output 1		
4	4	2A	Input 2A		
5	5	2B	Input 2B		
6	6	2Y	Output 2		
7	7	GND	Ground		
8	8	3Y	Output 3		
9	9	3A	Input 3A		
10	10	3B	Input 3B		
11	11	4Y	Output 4		
12	12	4A	Input 4A		
13	13	4B	Input 4B		
14	14	VCC	Power Supply		

BLOCK DIAGRAM



DC ELECTRICAL CHARACTERISTICS

Over operating free-air temperature range (unless otherwise noted); Voltages referenced to GND

SYMBOL	PARAMETER	TEST CO	NDITION	V _{CC}	MIN	TYP	MAX	UNIT		
	High-Level Input Voltage			2.0 V	1.5	1.2	-			
V _{IH}				4.5 V	3.15	2.4	-	V		
				6.0 V	4.2	3.2	-			
				2.0 V	-	0.8	0.5			
V _{IL}	Low-Level Input Voltage			4.5 V	-	2.1	1.35	V		
				6.0 V	-	2.8	1.8			
	V _{OH} High-Level Output Voltage		I _{OH} = −20μA	2.0 V	1.9	2.0	-			
		V _{IN} = V _{IH} or V _{IL}		4.5 V	4.4	4.5	-			
V _{OH}				6.0 V	5.9	6.0	-	V		
		I _{OH} = −4mA	4.5 V	3.84	4.32	-				
					I _{OH} = −5.2mA	6.0 V	5.34	5.81	-	
				2.0 V	-	0	0.1			
			I _{OH} = 20μA	4.5 V	-	0	0.1			
V_{OL}	V _{OL} Low-Level Output Voltage	vel Output Voltage $V_{IN} = V_{IH}$ or V_{IL}	$V_{IN} = V_{IH} \text{ or } V_{IL}$		6.0 V	-	0	0.1	V	
			$I_{OH} = 4mA$	4.5 V	-	0.15	0.33			
			I _{OH} = 5.2mA	6.0 V	-	0.16	0.33			
I _{IN}	Input Leakage Current	V _{IN} = V _{CC} or GND		6.0 V	-	-	±1.0	μΑ		
Icc	Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND, $I_O = 0A$		6.0 V	-	-	2.0	μA		

AC ELECTRICAL CHARACTERISTICS

Over operating free-air temperature range (unless otherwise noted); $C_L = 50$ pF, $Z_O = 50\Omega$, Input $t_r = t_f = 6$ ns

SYMBOL	PARAMETER	V _{CC}	MIN	TYP	MAX	UNIT
		2.0 V	1	25	115	
t _{PLH} ,	t _{PLH} , Propagation Delay, Input A or B to Output Y t _{PHL} (Figure 2)		1	9	23	ns
(Figure 2)	(i igure 2)	6.0 V	1	7	20	
,			1	19	95	
t _{TLH} ,	t _{TLH} , Transition Time, Any Output t _{THL} (Figure 2)	4.5 V	1	7	19	ns
THE (Figure 2)		6.0 V	-	6	16	

FUNCTION TABLE

Input (A)	Input (B)	Output (Y)
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

SWITCHING CHARACTERISTICS

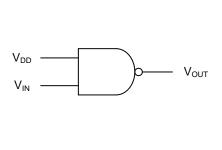


Fig. 1. Test Circuit

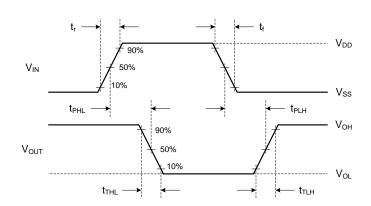


Fig 2. Switching Time Waveforms

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74HC00

TYPICAL OPERATING CHARACTERISTICS

T.B.D.

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REVISION NOTICE

The description in this datasheet is subject to change without any notice to describe its electrical characteristics properly.