

a. Description:

This project is to make a second timer, using the 4-digit 7 segment LED display, range to 60mins. Increasement per sec.

b. Timer display and control:

IO configuration

- PORTB connect to button 0 and 1. (INT0, INT1)
- PORTC display 7-segment LED number.
- PORTD choose the digit of display.

User input

- INT0 (button 0) used as control start/resume and stop function.
- INT1 (button 1) used as reset function, reset the timer to 00:00

Time count

- TMR0 used for counting 1 second and TMR0 interrupt is used.

Display

- The 1<sup>st</sup> and 2<sup>nd</sup> LED use to show mins, range from 00 to 60. Additional dot is add at 2<sup>nd</sup> digit.
- The 3<sup>rd</sup> and 4<sup>th</sup> LED show seconds.

c. Program flow:

1. Keep looping to display four-digit number.
2. When INT0 is detected, toggle TMR0, timer start.
3. If TMR0 overflow, increase the register that store the 4<sup>th</sup> digit BCD by 1.
4. Check if 4<sup>th</sup> digit is 10. If 10 then increase the 3<sup>rd</sup> digit, check 3<sup>rd</sup> digit if equal 6, and so on.
5. If TMR0 is ON and INT0 is detected, stop TMR0.
6. Whenever TMR0 on or not, INT1 always reset the timer and display.

d. Problem solved:

1. As I want to use 4-digit 7-segment LED, so polling to check button input will be inefficient and input may be missed. Interrupt driven is better choice.
2. As BCD to 7-segment routine is keep calling, so when a interrupt is occur, it will immediately jump to 0x0008. While the stack pointer still pointing to the address of the routine, it increases by 1 as interrupt occurs, and will not pop out address. So, there is a problem that stack will overflow. I decided to sacrifice some program memory and removed all the call and return function, even retfie is also removed. The GIE will all be controlled by program.

Appendix --- code explanation   blue: code   orange: registers variable   green: label

LED display:

Display:

```

        movff    in_1, input
        goto     bcd_7seg_1
D_1     movwf    PORTC
        clrf     PORTD
        movff    in_2, input
        goto     bcd_7seg_2
D_2     addlw    b'10000000'
        movwf    PORTC
        incf     PORTD
        movff    in_3, input
        goto     bcd_7seg_3
D_3     movwf    PORTC
        incf     PORTD
        movff    in_4, input
        goto     bcd_7seg_4
D_4     movwf    PORTC
        incf     PORTD
        GOTO     Display
```

Similar to the assignment, but I removed the delay. 4-digit BCD is stored in `in_1`, `in_2`, `in_3` and `in_4` registers.

## Digit increment function:

<pre>inc4:  bcf      T0CON,TMR0ON         incf     in_4,w         DAW         andlw    b'00001111'         movwf    in_4         BZ       inc3         bcf      INTCON,TMR0IF         bsf      INTCON,GIE         goto     Again  inc3:   incf     in_3         decf     cs6         bz       inc2         bcf      INTCON,TMR0IF         bsf      INTCON,GIE         goto     Again</pre>	<pre>inc2:   movlw    d'6'         movwf    cs6         clrf     in_3         incf     in_2,w         DAW         ANDLW    b'00001111'         movwf    in_2         BZ       inc1         bcf      INTCON,TMR0IF         bsf      INTCON,GIE         goto     Again  inc1:   incf     in_1         decf     cm6         btfsc    STATUS,Z         goto     Main         bcf      INTCON,TMR0IF         bsf      INTCON,GIE         goto     Again</pre>
--	--

When TMR0 interrupt occur, the 4<sup>th</sup> digit will increase by 1, after that decimal adjustment is done, next the first nibble is masked. After that, check if is 0. If 0 then increase the 3<sup>rd</sup> digit. I use **cs6** to check if the 3<sup>rd</sup> digit is 6 by decreasing it by 1 every increasement of 3rd digit.

Same method is used at **inc2** and **inc1**. In inc1, when **cm6** is decreased to 0, that means the counter counted to 60 mins, then the timer will start from 00:00.

TMR0, INT0, INT1 configuration:

intcon:

```
movlw    b'00000100'  
movwf    T0CON  
movlw    b'01100000'  
movwf    INTCON2  
movlw    b'00001000'  
movwf    INTCON3  
movlw    b'10110000'  
movwf    INTCON  
goto     Restart
```

Again:

```
movlw    0x85 ;movlw    0xAA  
movwf    TMR0H  
movlw    0xEE  
movwf    TMR0L  
bsf      T0CON,TMR0ON
```

As I want to generate 1s, I use 1:32 prescaler, and set the TMR0 as 0x85EE (-31250)

## Interrupt check:

<b>Checkint:</b>  POP btfss INTCON,INT0IF goto inc4 btfss T0CON,TMR0ON goto Resume btfsc INTCON3,INT1IF goto Restart goto Stop	<b>Stop:</b>  bcf T0CON,TMR0ON bcf INTCON,INT0IF bsf INTCON,GIE goto Display
	<b>Resume:</b>  bcf INTCON,INT0IF bsf INTCON,GIE goto Again
	<b>Restart:</b>  bcf T0CON,TMR0ON clrf In_1 clrf in_2 clrf in_3 clrf in_4 bcf INTCON3,INT1IF bsf INTCON,GIE goto Display

When interrupt occur, the program goto **Checkint**.

If INT0 flag is 0, that means timer is working, goto **inc4**.

If TMR0 is not using , that means the timer stop, goto **Resume**.

If INT1 is interrupting, goto **Restart**.

Otherwise, goto **Stop**.

**Stop:**

Stop TMR0, just display number.

**Resume:**

Start TMR0, re-calculate 1 sec.

**Restart:**

Clear all the digit and stop the timer, wait for INT0 to start the timer.

Whole code:

```
LIST    P=18F4520
#include <P18F4520.INC>
CONFIG OSC = XT
CONFIG WDT = OFF
CONFIG LVP = OFF
cblock 0x10
input, DELAY_L, DELAY_H, in_1, in_2, in_3, in_4,Eint,Wint,cs6,cm6,stopf
endc
ORG 0x00
goto Main
ORG 0x0008
goto Checkint

ORG 0x50
Main:    movlw    0x0F
        movwf    ADCON1
        clrf     TRISD
        clrf     PORTD
        clrf     TRISC
        clrf     PORTC
        setf     TRISB
        clrf     in_1
        clrf     in_2
        clrf     in_3
        clrf     in_4
        movlw    d'6'
        movwf    cs6
        movwf    cm6

intcon:
        movlw    b'00000100'
        movwf    T0CON
        movlw    b'01100000'
        movwf    INTCON2
        movlw    b'00001000'
        movwf    INTCON3
        movlw    b'10110000'
        movwf    INTCON
        goto     Restart

Again:
```

```

    movlw    0x85    ;movlw 0xAA
    movwf    TMR0H
    movlw    0xEE
    movwf    TMR0L
    bsf      T0CON,TMR0ON

```

Display:

```

D_1      movff  in_1, input
          goto   bcd_7seg_1
          movwf  PORTC
          clrf   PORTD

```

```

D_2      movff  in_2, input
          goto   bcd_7seg_2
          addlw  b'10000000'
          movwf  PORTC
          incf   PORTD

```

```

D_3      movff  in_3, input
          goto   bcd_7seg_3
          movwf  PORTC
          incf   PORTD

```

```

D_4      movff  in_4, input
          goto   bcd_7seg_4
          movwf  PORTC
          incf   PORTD
          GOTO   Display

```

Checkint:

```

    POP
    btfss    INTCON,INT0IF
    goto     inc4
    btfss    T0CON,TMR0ON
    goto     Resume
    btfsc    INTCON3,INT1IF
    goto     Restart
    goto     Stop

```

Stop:

```

    bcf      T0CON,TMR0ON
    bcf      INTCON,INT0IF
    bsf      INTCON,GIE

```

goto Display

Resume:

bcf INTCON,INT0IF

bsf INTCON,GIE

goto Again

Restart:

bcf T0CON,TMR0ON

clrf in\_1

clrf in\_2

clrf in\_3

clrf in\_4

bcf INTCON3,INT1IF

bsf INTCON,GIE

goto Display

ORG 0x300

inc4: bcf T0CON,TMR0ON

incf in\_4,w

DAW

andlw b'00001111'

movwf in\_4

BZ inc3

bcf INTCON,TMR0IF

bsf INTCON,GIE

goto Again

inc3: incf in\_3

decf cs6

bz inc2

bcf INTCON,TMR0IF

bsf INTCON,GIE

goto Again

inc2: movlw d'6'

movwf cs6

clrf in\_3

incf in\_2,w

DAW

ANDLW b'00001111'

movwf in\_2



```
BZ      inc1
bcf     INTCON,TMR0IF
bsf     INTCON,GIE
goto    Again
```

```
inc1:   incf     in_1
        decf     cm6
        btfsc    STATUS,Z
        goto     Main
        bcf     INTCON,TMR0IF
        bsf     INTCON,GIE
        goto     Again
```

```
bcd_7seg_1:
        MOVLW    low bcd_table
        MOVWF    TBLPTRL
        MOVLW    high bcd_table
        MOVWF    TBLPTRH
        MOVLW    upper bcd_table
        MOVWF    TBLPTRU
        MOVF     input, W
        ADDWF    TBLPTRL, F
        MOVLW    0
        ADDWFC   TBLPTRH
        ADDWFC   TBLPTRU
        TBLRD*
        MOVF     TABLAT, W
        goto     D_1
```

```
bcd_7seg_2:
        MOVLW    low bcd_table
        MOVWF    TBLPTRL
        MOVLW    high bcd_table
        MOVWF    TBLPTRH
        MOVLW    upper bcd_table
        MOVWF    TBLPTRU
        MOVF     input, W
        ADDWF    TBLPTRL, F
        MOVLW    0
        ADDWFC   TBLPTRH
        ADDWFC   TBLPTRU
        TBLRD*
```

```

        MOVF     TABLAT, W
        goto     D_2
bcd_7seg_3:
        MOVLW    low bcd_table
        MOVWF    TBLPTRL
        MOVLW    high bcd_table
        MOVWF    TBLPTRH
        MOVLW    upper bcd_table
        MOVWF    TBLPTRU
        MOVF     input, W
        ADDWF    TBLPTRL, F
        MOVLW    0
        ADDWFC   TBLPTRH
        ADDWFC   TBLPTRU
        TBLRD*
        MOVF     TABLAT, W
        goto     D_3
bcd_7seg_4:
        MOVLW    low bcd_table
        MOVWF    TBLPTRL
        MOVLW    high bcd_table
        MOVWF    TBLPTRH
        MOVLW    upper bcd_table
        MOVWF    TBLPTRU
        MOVF     input, W
        ADDWF    TBLPTRL, F
        MOVLW    0
        ADDWFC   TBLPTRH
        ADDWFC   TBLPTRU
        TBLRD*
        MOVF     TABLAT, W
        goto     D_4
bcd_table  ORG 0x500
db 0x3F,0x06,0x5B,0x4F,0x66,0x6D,0x7D,0x07,0x7F,0x6F
End

```