#### CS 271 WEEK 3 - Readings -

## The Stack

- like a stack of plates
- LIFO (Last In, First Oct)
- Runtine Stack is supported by hadware within the CPU
- Runfine Stack Uses ESP (stack pointer or stack pointer reg)
- -ESP points to the her offset of the most recently (topmost) pushed value
- -In 32-bit mode, each stack loc. has 32-bits of space

,	BEFORE MUSI	7	(VISUAL)
OX 1000 OX OFFC	0000006	0×1000 0×0FF8	ESP ESP
OXOFF8 OXOFF0		oxoff o	昌

Popping from the BEFORE POP	he stad	erer (VISUAL)
6×1000 12	OXFFC	~
OXOFF8	OXFF8	

# Uses for the Stack

- temporary storage for registers when their values are needed later, but need to be changed short-term.
- when a CALL is called, the CPU saves return addr on stack
- Stack is how you pass arguments fo a subroutine.

# PUSH and POP opudes

- -PUSH copies the source operand into the stack, "scrementing ESP by (2 if 16-bit op. | 4 if 32-bit op)
- -POP copies the wrient value
  pointed to by ESP into the
  "source" (dest) operand, then
  increments ESP by (2 If 16-61+)
  4 If 32-61+).

#### PUSHFD, POPFD

- -these opendes pushand pop the EFLAGS to /from the Stack
- -Pushed is essentially "backing up" the flags, and Popfol essentially "restores from backup".
- -it is best to use pushfd, then
  Pop to a variable, then inverse
  to restore.

### PUSHAD, POPAD, PUSHA, POPA

- ProhAD and POPAD pop these registers on to loft of the Stack:

### EAX EXX EDX EBX ESP\* EBP EST EDT \* before PUSHAD EXECUTION

- PUSHA and POPA are only for 16-bit purograming. they push/pop these registers to / from the stack:

## AX CX DX BX SP BP SI DI

-PUSHAD and POPAD can be used to "back up" all the registers, but bewase of any overwritten values that will be lost in the process of POPAD!!

Procedures
-AKA Methods, functions, subroutires
-Must have a PROC and END P
directive:

ret temp-proc ENDP

-Labels can only be accessed within their own procedure, unless they are defined as global labels: label-name: Thore double colon

- Should dowment Procedures with

T: TASK (goal of proc)

R: Redeves (inputs, pie-worditions)

R: Retrins (outputs, Post-con.)

R: Requires (Necessary settings/valves)

Stack when Calling Procedures

| PROC\_Main Proc | Address | Procedure-1

| CALL Procedure-1 | 0x20 | 0x40 | mov ebx, eax
| mov cax, elox | 0x25 | 0x45 | ret
| Proc\_main ENDP

I] Call Function moves 0x25 onto the stack, and moves 0x40 into EIP 2] when providere\_1 calls ret, 0x25 15 popped off the stack, into EIP

Arguments for Procedures
- usually, we use general-purpose
registers for this

USES Operator

-when defining a process, the uses operator will "manage" all the pushing and popping for the registers listed as to ensure that their values are the same once the proc has competed, as when the proc started

-Ex: temp PROC USES ecx, esi

ret temp END P

will put a "push" for each
register at the top of "temp"
and a "pop" at the end,
thus keeping the values of
ecx, esi the same post-proc.

Link Libraries and Whatnot
- a link library is a file that
has many proledures in machine

code within it

Source Asm Assen ORJ Fre Grantes

For Linking

- Source code must contain a
  PROTO to be able to have a
  fice linked with the library
  derived from that source
  be able to use Eall openie.
- When assembling, assembler leaves Call's for the linker (sometimes...), and linker copies Mielevant Gode and addresses.