

Exercício 01.2

Aula 01

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Moderate ♩ = 120

[illegible]

Diagram illustrating a 4x16 grid structure, likely representing a memory layout or a data structure. The grid is divided into four 4x4 quadrants. The columns are numbered 1 to 16 at the top. The rows are labeled with '0' at the top of each column. The grid is divided into four 4x4 quadrants. Below the grid, there are 16 small squares, each containing a '0'.

[illegible]

The diagram illustrates a 17-bit bus system. At the top, a horizontal line is labeled '17' at its left end. Below this line, four 4-bit components are shown, each consisting of a 2x2 grid of squares. The top row of each grid contains a '0' in the first three squares and a blank space in the fourth. The bottom row contains a '0' in the first square and blank spaces in the remaining three. Below each 2x2 grid, there are four small, empty square boxes, one for each column of the grid above it.

Figure 1 illustrates a sequence of 21 data points, labeled 1 through 21, arranged in a grid. The top row contains 21 data points, with the first point labeled '21'. Below this, there are four rows of data points, each with 21 columns. The data points are arranged in a pattern that suggests a sequence of operations or a data flow. The bottom row shows a sequence of 21 data points, each represented by a small square with a dot inside.