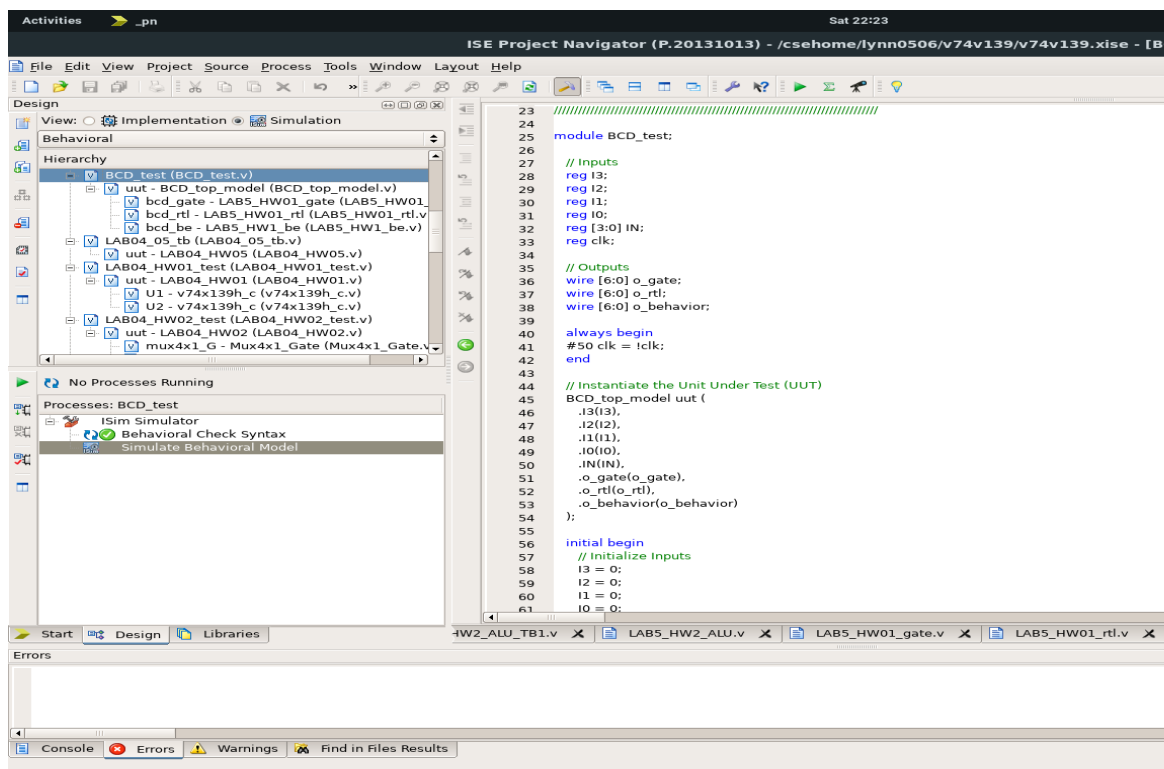


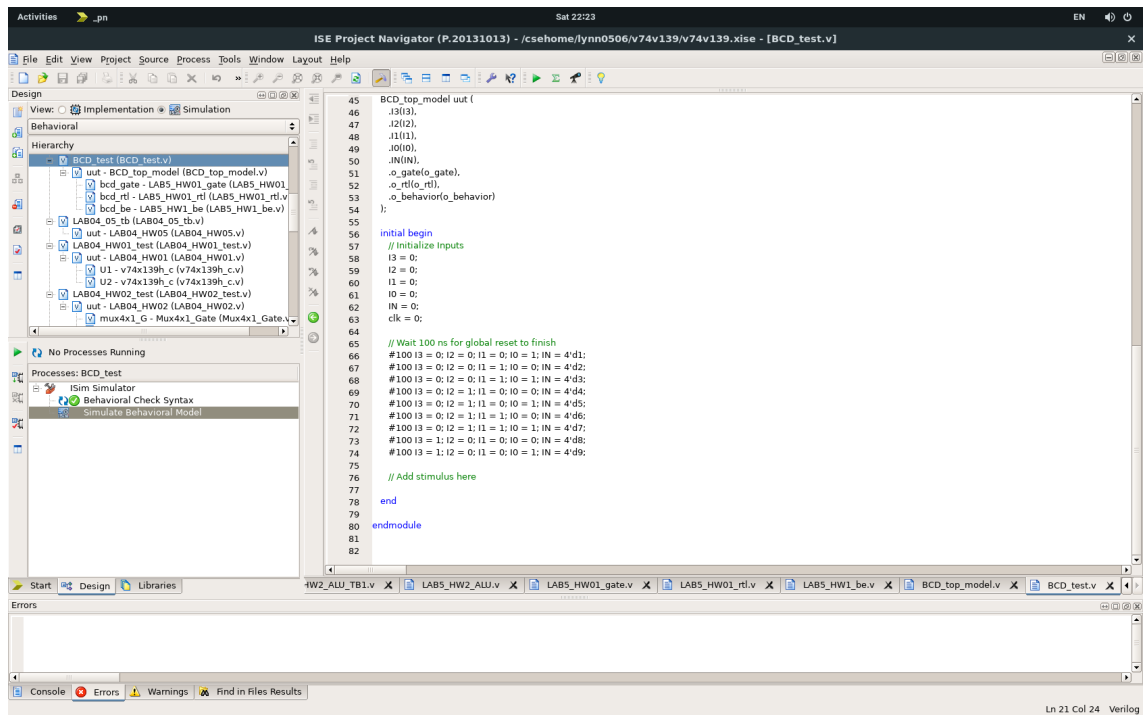
논리설계 LAB05
정은주 2014-19498
독어교육과

HW1

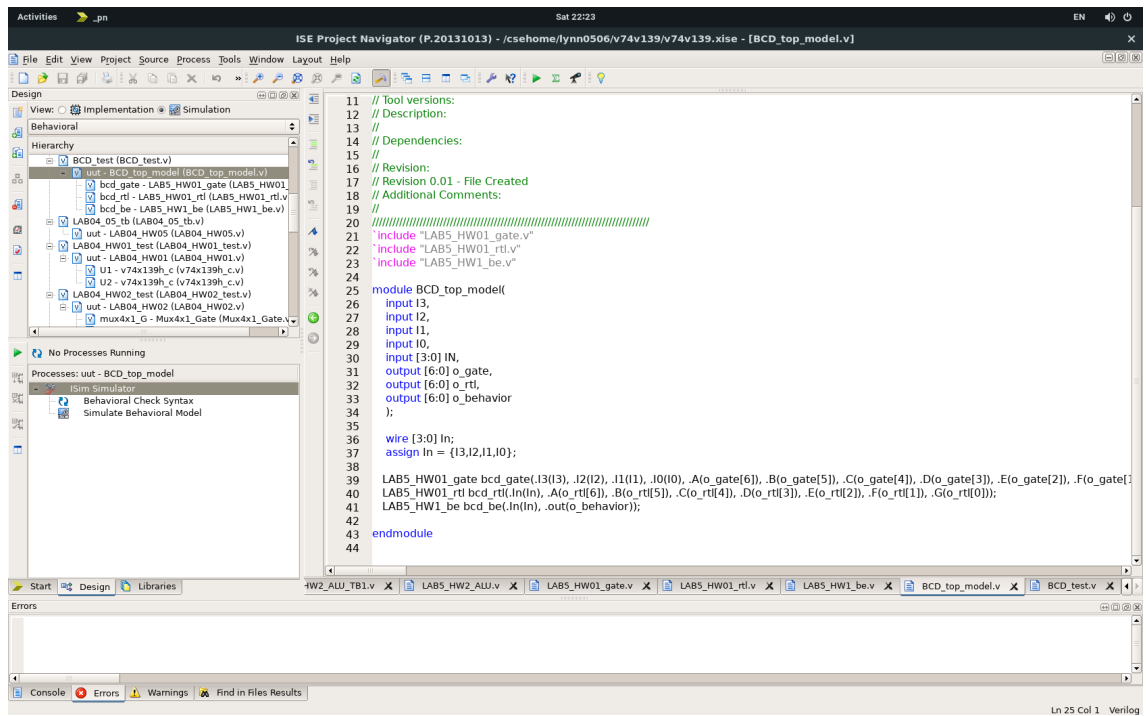
I3	I2	I1	I0	A	B	C	D	E	F	G
0	0	0	0	1	1	1	1	1	1	0
0	0	0	1	1	0	0	0	0	0	0
0	0	1	0	1	0	0	0	0	0	1
0	0	1	1	1	0	0	1	0	0	1
0	1	0	0	0	1	0	0	0	1	1
0	1	0	1	0	0	1	1	1	0	1
0	1	1	0	0	1	0	0	1	0	1
0	1	1	1	0	0	1	0	0	1	1
1	0	0	0	0	1	1	0	1	1	0
1	0	0	1	0	1	1	0	1	1	1



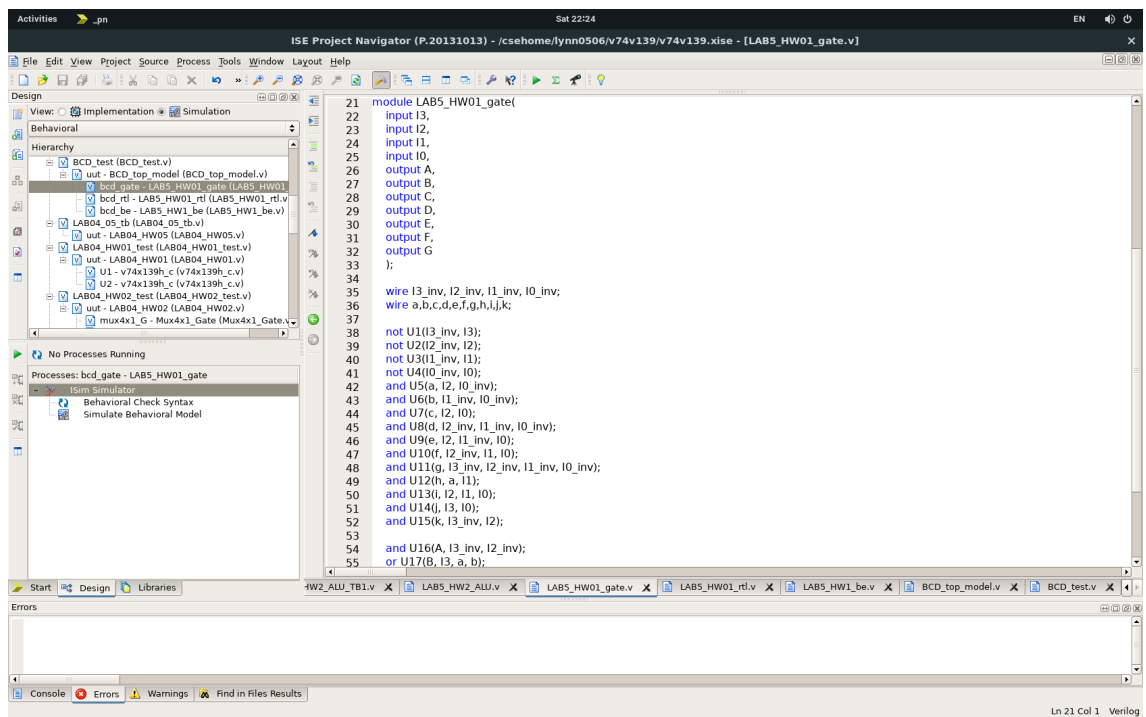
[BCD_TESTBENCH]



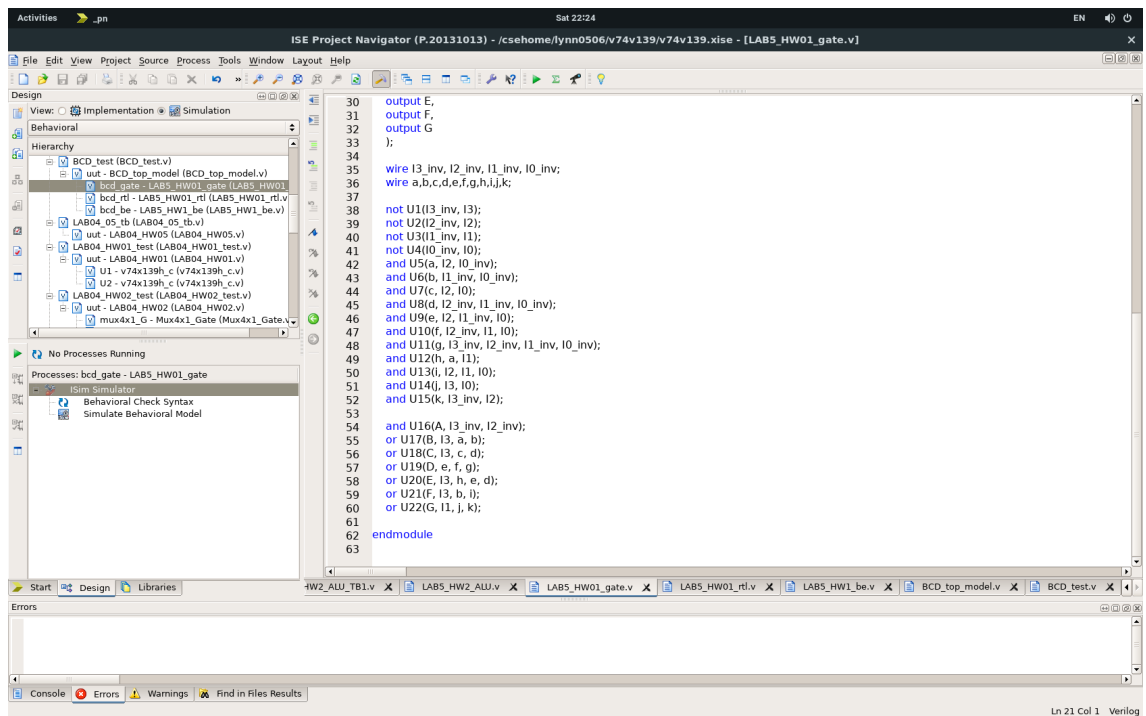
[BCD_TESTBENCH2]



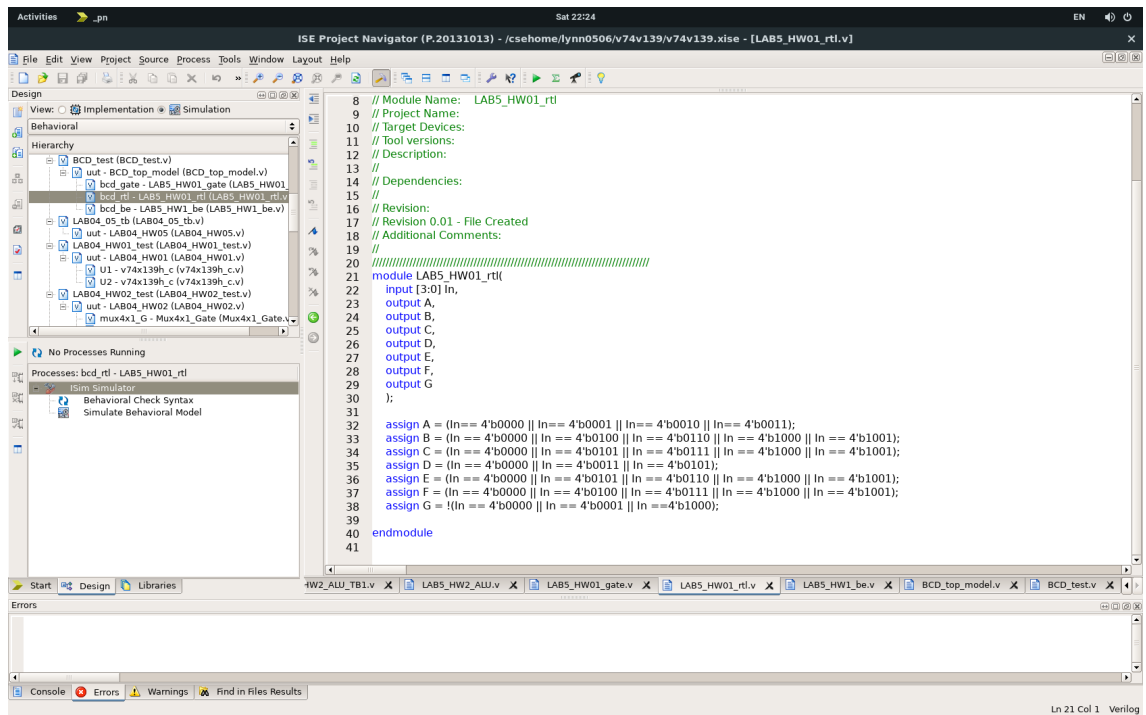
[BCD_Top_model]



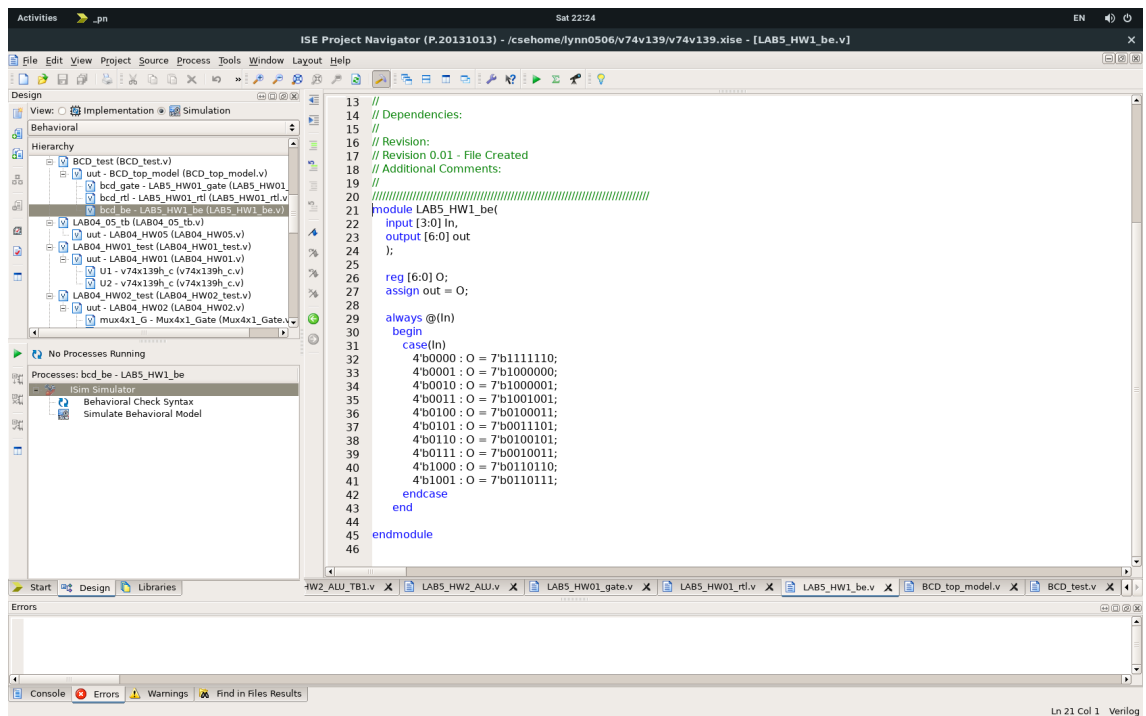
[BCD_GATE_LEVEL_1]



[BCD_GATE_LEVEL_2]

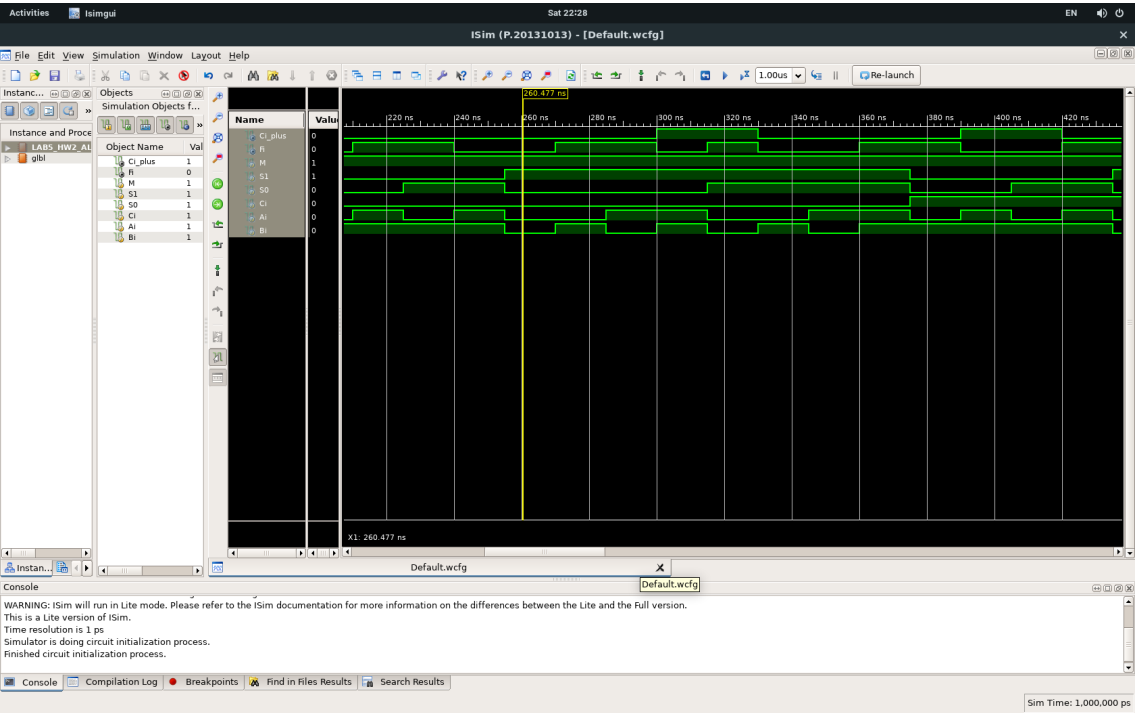


[BCD_RTL_LEVEL]

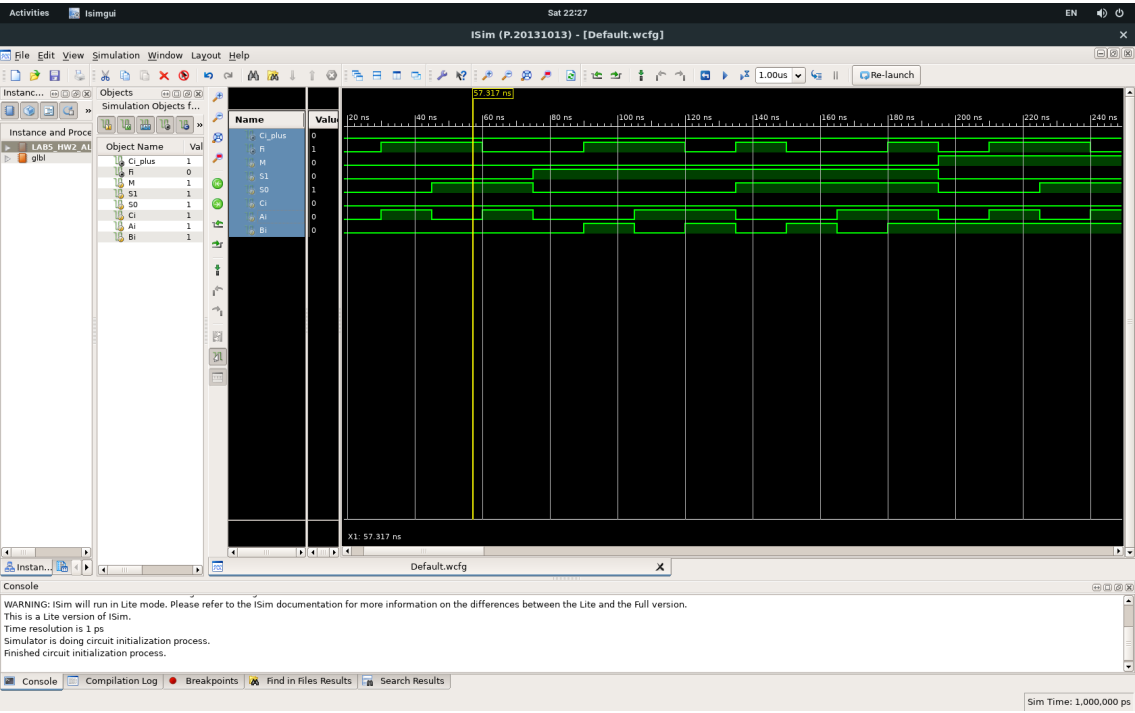


[BCD_BEHAVIOR]

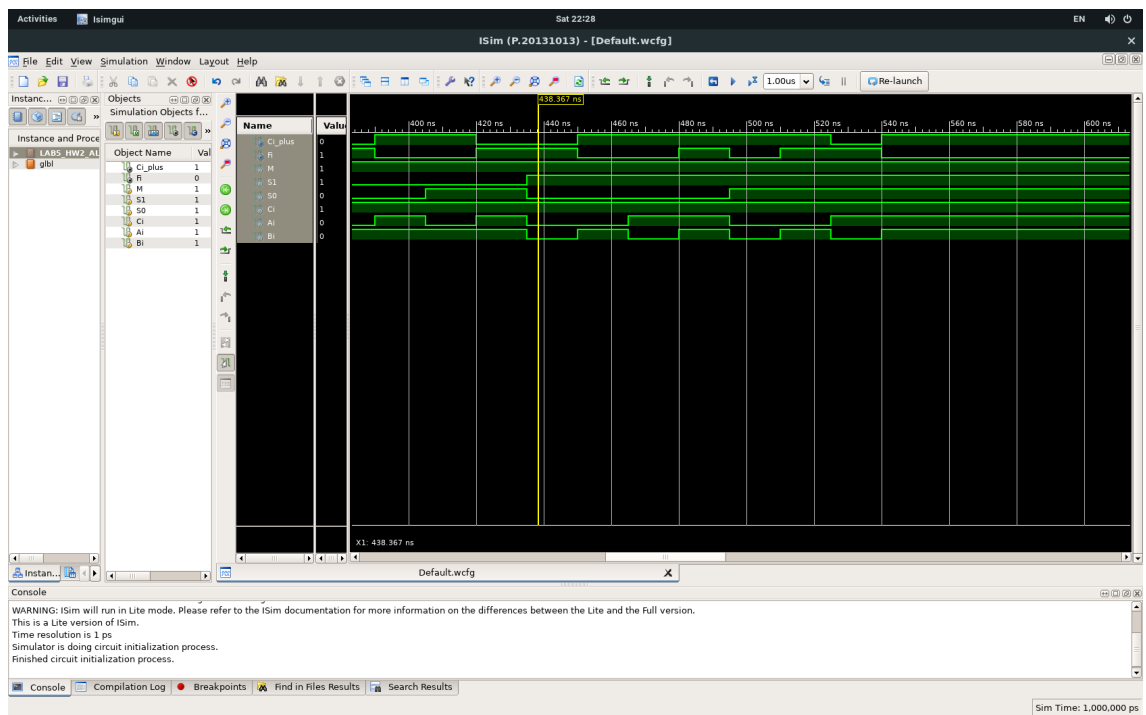
HW2



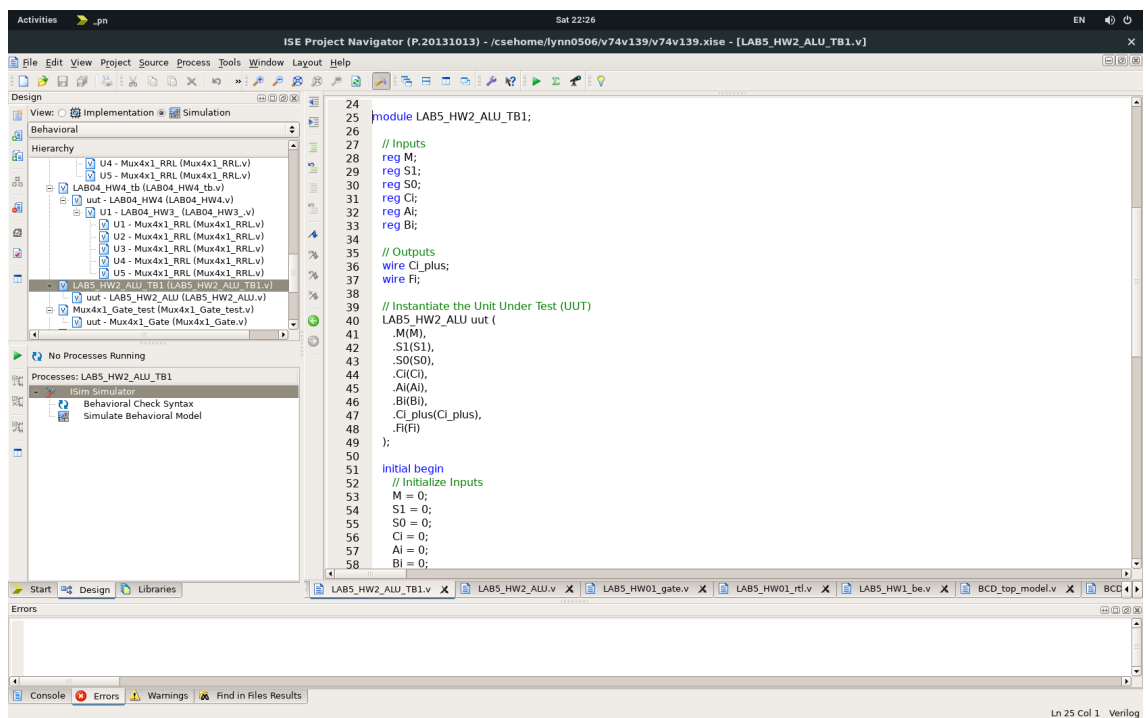
[ALU_SIMULATION1]



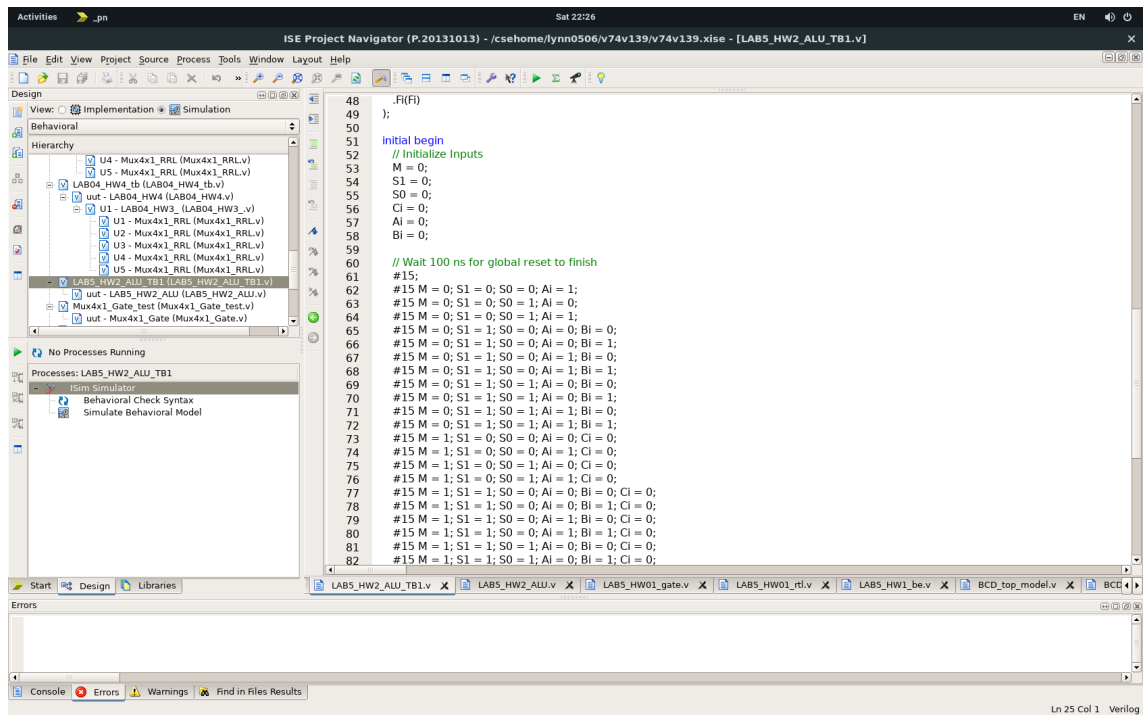
[ALU_SIMULATION2]



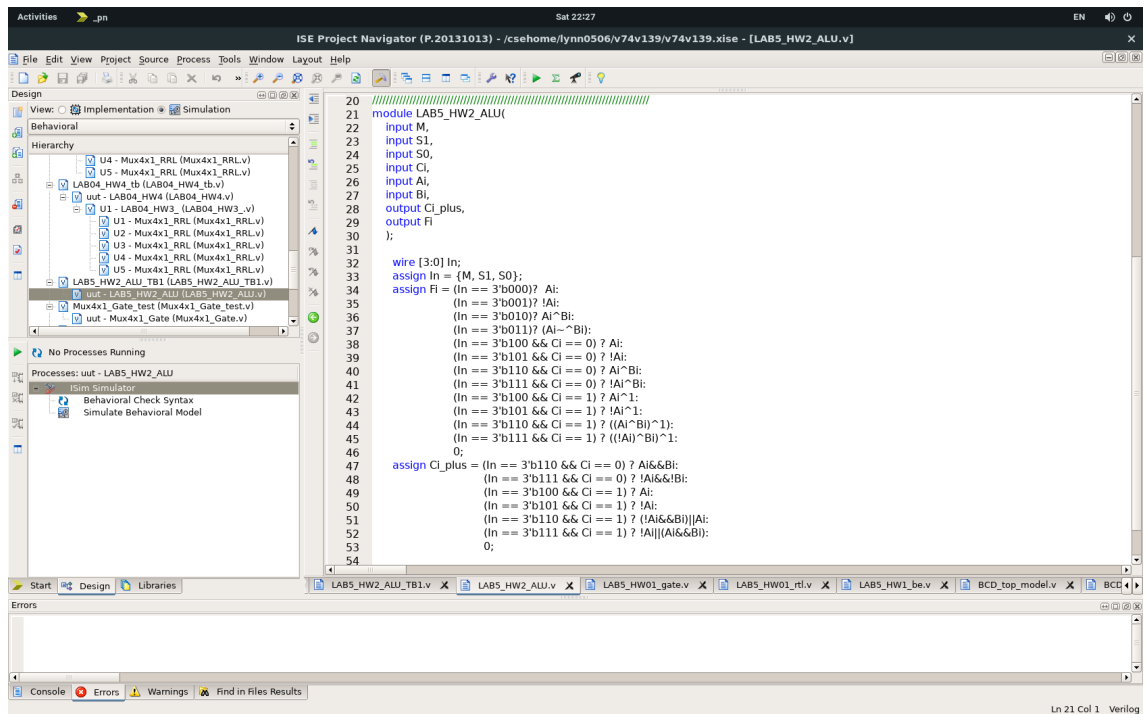
[ALU_SIMULATION3]



[ALU_TESTBENCH]



[ALU_TESTBENCH2]



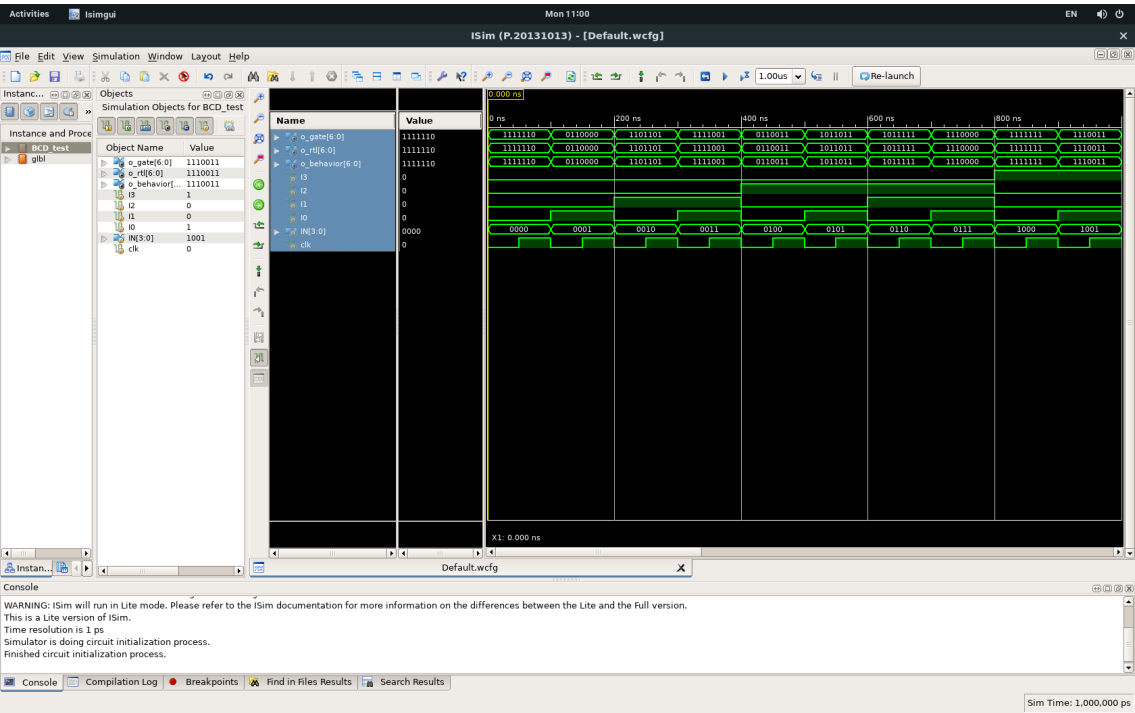
[ALU_CODE]

LAB5 report

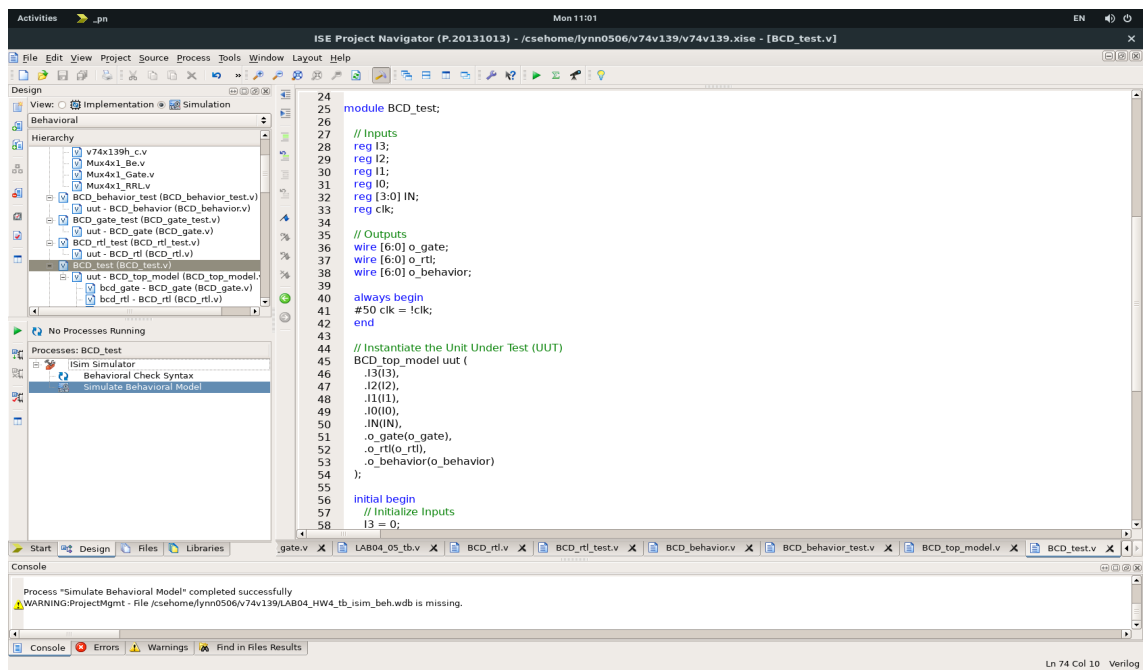
이번 5번째 랩에서는 BCD 7 segment를 이용하여, decoder를 구현해 보았다. 먼저 truth table을 만든 다음에 각각의 minimized sum of product를 만든다. 그 후에 3개의 각각 다른 gate level, rtl, 그리고 behavior model을 구현한다. 각각 시뮬레이션과 test bench 그리고 각각에 해당하는 코드를 위에 사진으로 첨부하였다.

I3	I2	I1	I0	A	B	C	D	E	F	G
0	0	0	0	1	1	1	1	1	1	0
0	0	0	1	0	1	1	0	0	0	0
0	0	1	0	1	1	0	1	1	0	1
0	0	1	1	1	1	1	1	0	0	1
0	1	0	0	0	1	1	0	0	1	1
0	1	0	1	1	0	1	1	0	1	1
0	1	1	0	1	0	1	1	1	1	1
0	1	1	1	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1	1	1	1
1	0	0	1	1	1	1	0	0	1	1

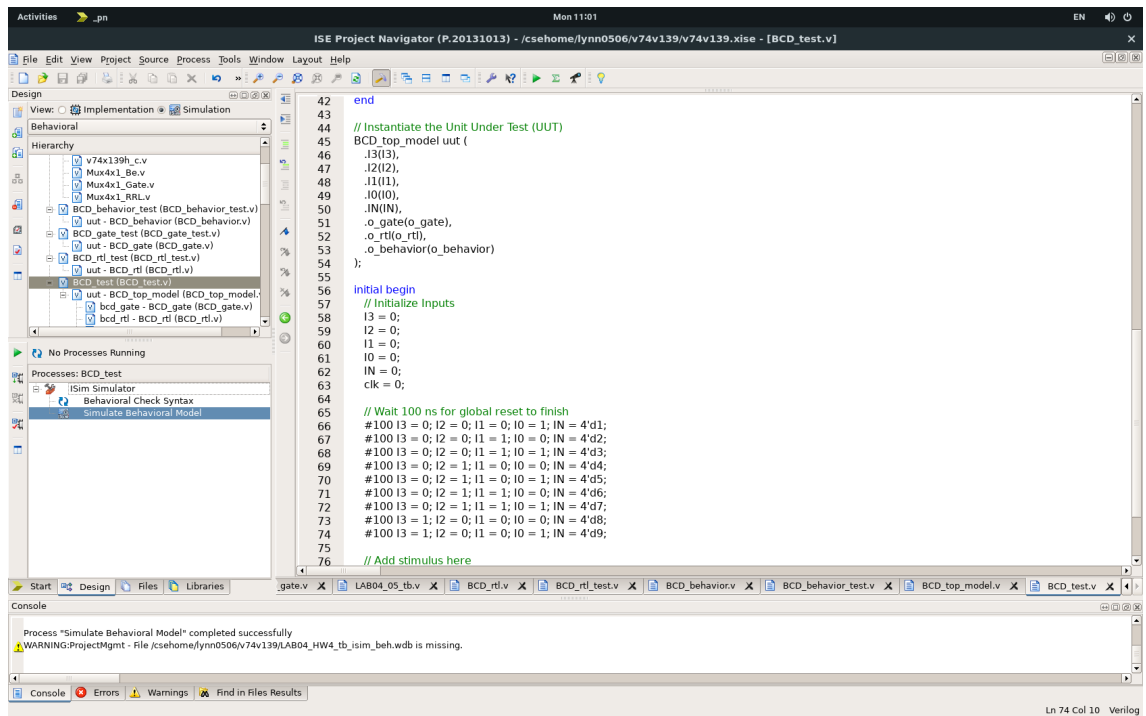
[BCD_to_7-segment_Decoder_truth_table]



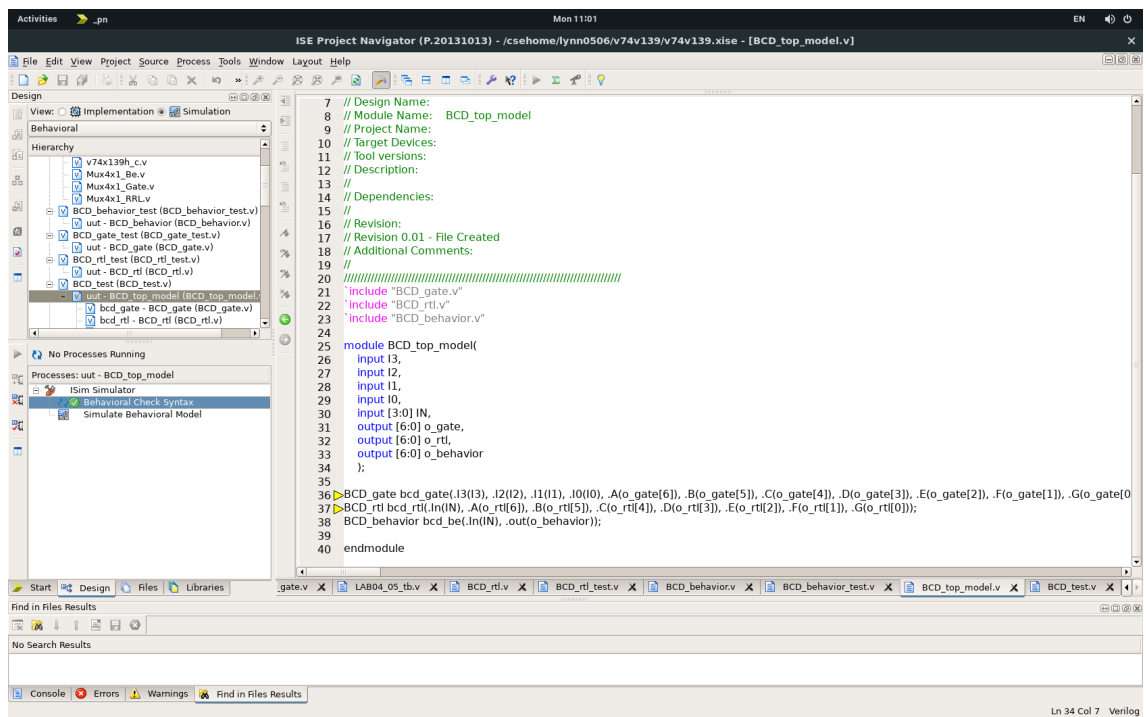
[BCD_SIMULATION]



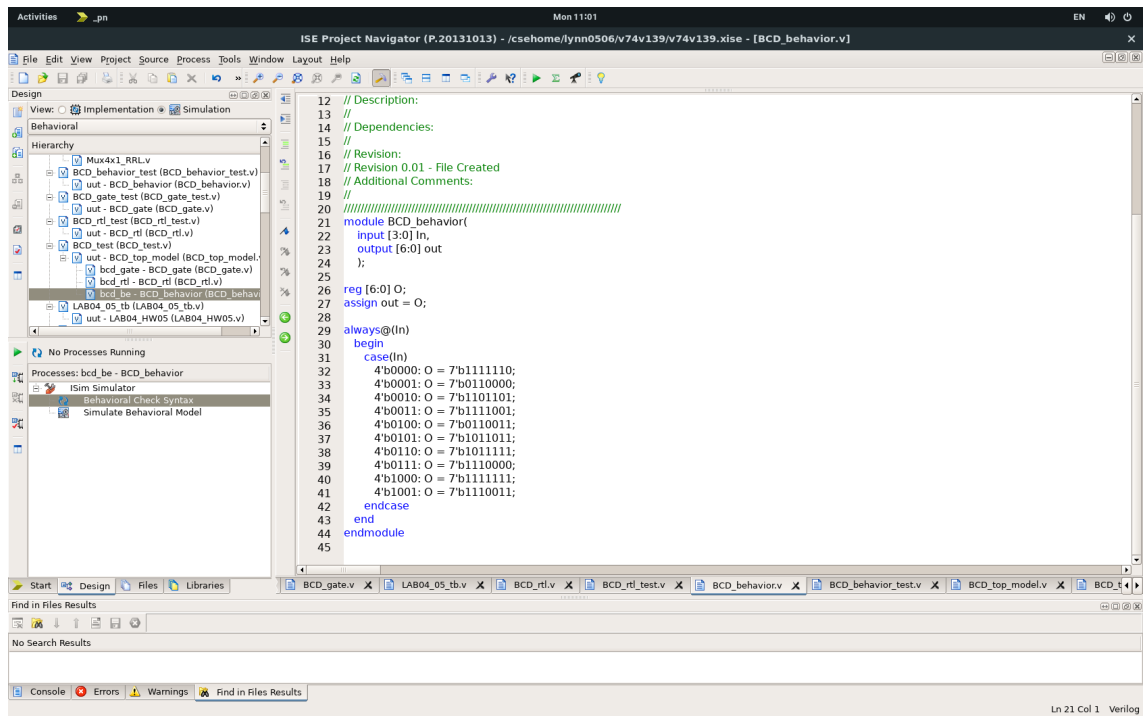
[BCD_TESTBENCH]



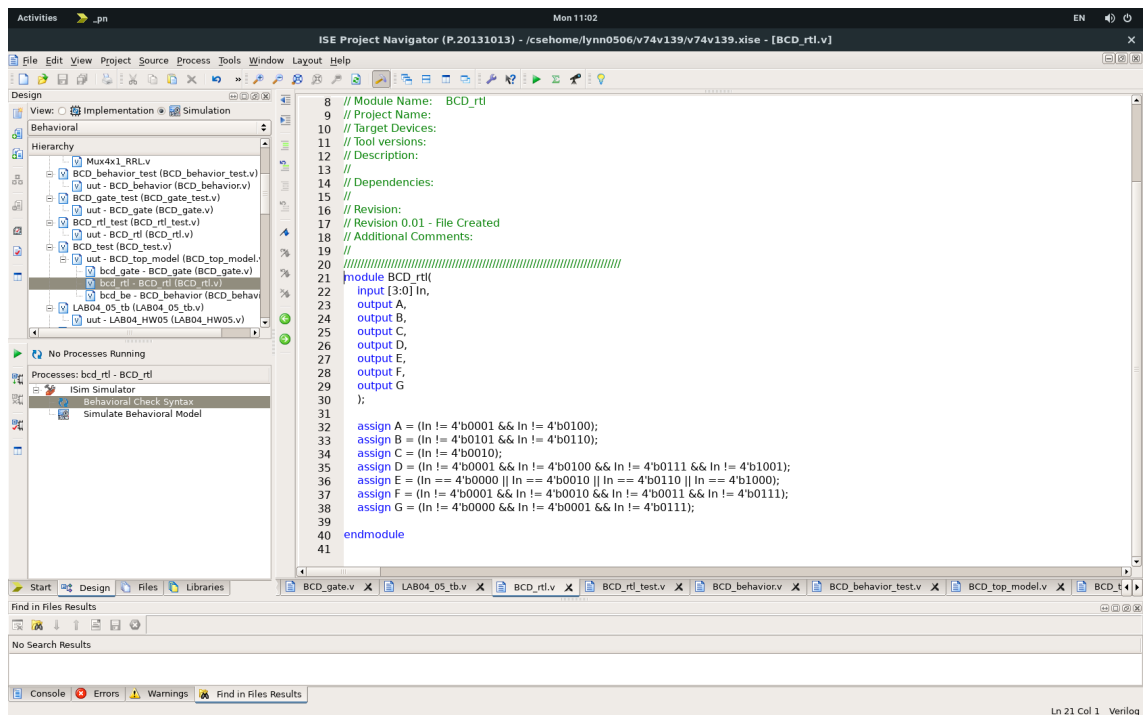
[BCD_TESTBENCH2]



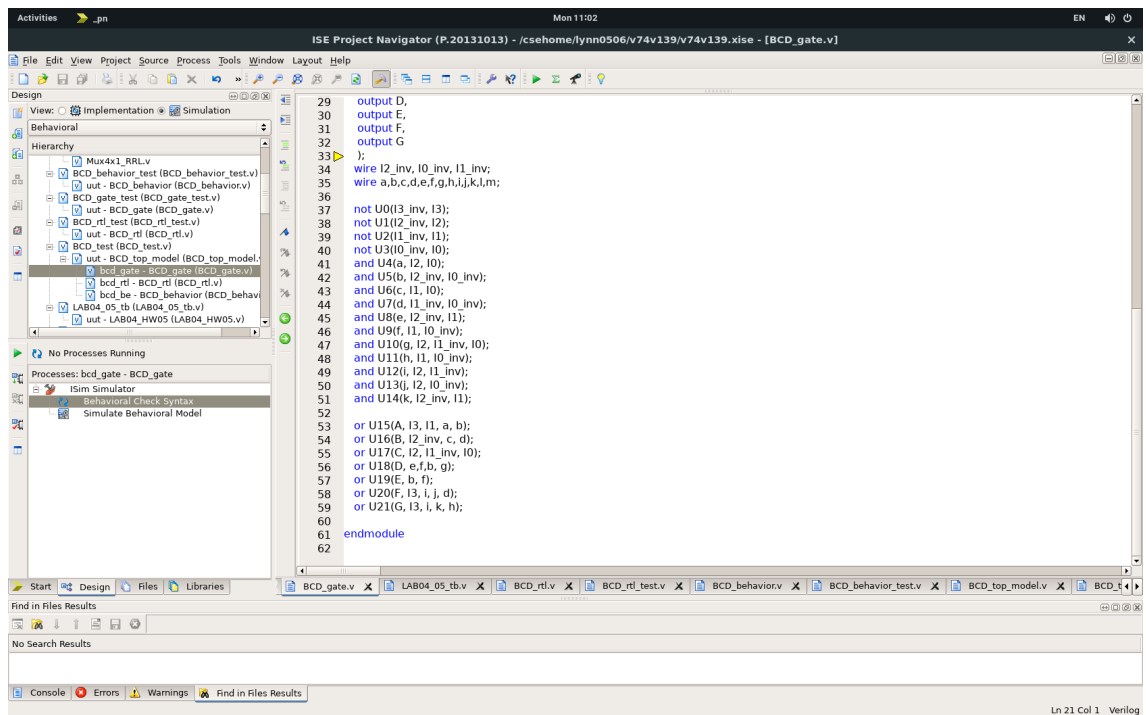
[BCD_TOP_MODEL]



[BCD_BEHAVIOR]



[BCD_RTL]



[BCD_GATE_LEVEL]