

KOLEJ SYNERGY NO 32, 34 JALAN PERAI JAYA 4, BANDAR PERAI JAYA, 13600 PULAU PINANG.

PENILAIAN PENGETAHUAN

(KNOWLEDGE ASSESSMENT)

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KOD UNIT KOMPETENSI/COMPETENCY UNIT CODE	IT-010-4:2013-C01	
TAJUK UNIT KOMPETENSI/COMPETENCY UNIT TITLE NO. KOD/ CODE NO. NAMACALON/CANDIDATE'S NAME	APPLICATION SYSTEMS PROGRAMMING IT-010-4:2013 – C01/KA(1/1)	
NO KADPENGENALAN/NRIC NUMBER		
MASA/TIME	2.00PM - 3.30PM (1 HOUR 30 MINUTES)	
TARIKH/DATE		
KEPUTUSANPENILAIAN/ASSESSMENT RESULT		
MARKAH/SCORE: %	TERAMPIL/BELUMTERAMPIL COMPETENT / NOT COMPETENT	
ULASAN PEGAWAI PENILAI/ASSESSOR COMMENT :		
Tandatangan Pegawai Penilai/Assessor's Signature Nama/Name :	Tarikh/ <i>Date :</i>	

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Instruction to candidate:

Write down the name and numbers of your ID card in the space provided.

- 1. Candidates are not allowed to open the paper until allowed.
- 2. Please answer all questions.
- 3. Not allowed to bring any notes and reference materials except allowed.
- 4. candidates are prohibited copying during evaluation.
- 5. Prohibited brings out the question paper

KERTASPENILAIAN PENGETAHUANINIMENGANDUNGI8MUKASURATBERCETAK.

This Knowledge assessment contains 8 printed pages

SECTION A: MULTIPLE CHOICE QUESTION

This section contains fourteen (10)questions. You must answerall questions by circling the correct answer, if you want tochange your answer, please cross the previous answer. Mark your answers on the answer sheet provided.

- 1. The part of machine level instruction, which tells the central processor what has to be done, is?
 - A. Operation code
 - B. Address
 - C. Locator
 - D. Flip-Flop
- 2. Which of the following refers to the associative memory?
 - A. the address of the data is generated by the CPU
 - B. the address of the data is supplied by the users
 - C. there is no need for an address i.e. the data is used as an address
 - D. the data are accessed sequentially
- 3. To avoid the race condition, the number of processes that may be simultaneously inside their critical section is?
 - A. 8
 - B. 1
 - C. 16
 - D. 0
- 4. A system program that combines the separately compiled modules of a program into a form suitable for execution?
 - A. Assembler.
 - B. Linking loader.
 - C. Cross compiler.
 - D. Load and go.

- 5. Process is?
 - A. program in High level language kept on disk
 - B. contents of main memory
 - C. a program in execution
 - D. a job in secondary memory
- 6. Addressing structure?
 - A. defines the fundamental method of determining effective operand addresses
 - B. are variations in the use of fundamental addressing structures, or some associated actions which are related to addressing
 - C. performs indicated operations on two fast registers of the machine and leave the result in one of the registers
 - D. all of the above
- 7. The Memory Buffer Register (MBR)?
 - A. Is a hardware memory device which denotes the location of the current instruction being executed.
 - B. Is a group of electrical circuits (hardware), that performs the intent of instructions fetched from memory.
 - C. Contains the address of the memory location that is to be read from or stored into.
 - D. Contains a copy of the designated memory location specified by the MAR after a "read" or the new contents of the memory prior to a "write".
- 8. The strategy of allowing processes that are logically runnable to be temporarily suspended is called?
 - A. preemptive scheduling
 - B. non preemptive scheduling.
 - C. shortest job first.
 - D. first come first served.

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- 9. The Storage-to-Storage instructions?
 - A. have both their operands in the main store.
 - B. which perform an operation on a register operand and an operand which is located in the main store, generally leaving the result in the register, expect in the case of store operation when it is also written into the specified storage location
 - C. which perform indicated operations on two fast registers of the machine and have the result in one of the registers
 - D. all of the above
- 10. The LRU algorithm?
 - A. pages out pages that have been used recently
 - B. pages out pages that have not been used recently
 - C. pages out pages that have been least used recently
 - D. pages out the first page in a given area

(10 Marks)

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SECTION B: STRUCTURED QUESTIONS

This section contains five (5) questions. Pleasewrite your answers clearly. If you want to change your answer, pleaseline in the previous answer. Answer on the answer sheet provided.

1.	Describes 5 steps of programming paradigm:	
	a	
	b	
	C	
	d	
	e	
		(5 Marks)
2.	What is meant by Object Oriented Programming?	,
		(5Marks)
3.	What is meant by Encapsulation?	(0.11.6)
		(5 Marks)
4.	In Java, How to make an object completely encapsulated?	,
		(5 Marks)
5.	What is static variable and static method?	(3 3, 3,
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		(5 Marks)

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SECTION C: ESSAY QUESTIONS

This section containsone (2) question. Write your answersclearly. If you want tochange your answer, pleaseline in the previous answer. Answeronthe answer sheet provided.

. What is the difference between String and String Buffer?	
	_
	(20 Mark
What is meant by Inheritance explain more detail?	
,	
	(10 Mark



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TAJUK UNIT KOMPETENSI/ COMPETENCY UNIT TITLE	APPLICATION SYSTEMS PROGRAMMING
NO. KOD/CODE NO.	IT-010-4:2013 – C01/KA(1/1)

ASSESSMENT RESULT

SCORE: %	COMPETENT/ NOT COMPETENT
SECTION / QUESTION NO.	MARK OBTAINED
Α	/ 10
В	/ 25
С	/ 30
TOTAL	/ 65 (100%) =
	/100 (15%) =