

APA 102-2020-256-6A

◆Outline (L* W*H): 2.0*2.0*0.75mm

◆Good thermal dissipation & optical uniformity

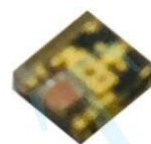


Table of Contents

Product Code Method-----	2
Maximum Rating-----	2
Electrical Characteristics -----	3
Optical characteristics -----	3
Range of Bins -----	4
Color Coordinate Comparison -----	4
Function description -----	5
Dimensions -----	6
PIN Configuration -----	7
Reflow Profile -----	8
Test Circuit and Handling Precautions -----	9
Packing-----	10
Precautions-----	12
Test Items and Results of Reliability-----	13

Features

- RoHS2.0 Compliant
- Packaged in 12mm tape on 7" diameter reels
- EIA STD package
- Compatible with automatic placement equipment and infrared reflow solder process
- Preconditioning: accelerate to JEDEC level 3
- Serial data transmission signal by (DATA CLK) two line

Applications

- Telecommunication, office automation, home appliances, industrial equipment
- Status indicator
- Signal and symbol luminaire
- Front panel backlighting
- Full-color strip.
- Indoor decorative lighting / curtain display

■ Product Code Method

APA- 102 - 2020 - 256 - 6A

① ② ③ ④ ⑤ ⑥

①	②	③	④	⑤
Brand Name	Model Number	Lead Frame Size	Gray Scale	Pin Number
Company Prefix	Model: 102	2020: 2.0*2.0mm	256 gray scales	6 Pins type led

■ **Maximum Rating(Ta=25°C)**

Parameter	Symbol	Rating	Unit
IC Power Supply Voltage	VDD	-0.4~+5.5	V
Logic input voltage	VI	-0.4~VDD +0.4	V
Rate of data signal	FCLK	15	MHZ
ESD pressure	VESD	2000	V
The max led output Current	IOMAX	18	mA
Soldering Temperature*1	TSD	260	°C
Operating Temperature Range	-40°C to +85°C		
Storage Temperature Range	-40°C to +105°C		

Notes 1: The maximum of soldering time is 5 seconds in TSD

■ **Typic Electrical Characteristics (Ta=25°C ; VDD=5V)**

Characteristics	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	Vdd			5.0	5.5	V
Rate of data signal	FCLK			5	-	MHZ
Input high voltage	VIH		0.7 VDD	-	-	V
Input low voltage	VIL		-	-	0.3 VDD	V
The clock high level widch	TCLKH		30	-	-	ns
The clock low level widch	TCLKL		30	-	-	ns
Data set up time	TSETUP		10	-	-	ns
Data hold time	THOLD		5	-	-	ns
The frequency of PWM	FPWM			20		KHz
Static power consumpton	IDD			0.7		mA

■ **Optical Characteristics(Ta=25°C;VDD=5V)**

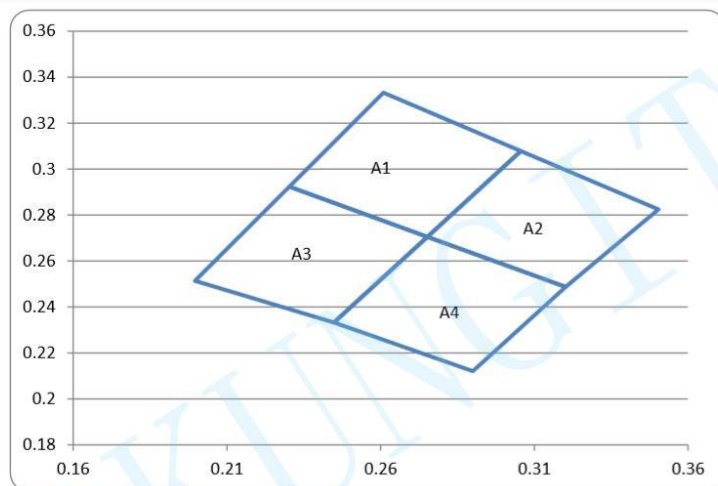
Characteristics	Symbol		Min.	Typ.	Max.	Unit	Test condition
Luminous Intensity	I _v	R	-	360	-	mcd	I _F =18mA
		G	-	510	-		
		B	-	100	-		
		W	400	800	-		
Dominant Wavelength	λ _d	R	615	-	630	nm	I _F =18mA
		G	520	-	535		
		B	460	-	475		
Color Coordinate	x			0.2752			I _F =18mA
	y			0.2705			
View Angle	2θ _{1/2}		-	120	-	deg	I _F =18mA

■ **Range of Bins**

1) **Luminous Intensity-White (I_F = 18mA)**

Bin Code	Min. IV (mcd)	Max. IV (mcd)
P	400	500
Q	500	630
R	630	800
S	800	1000

■ **Color Coordinate Comparison-White**



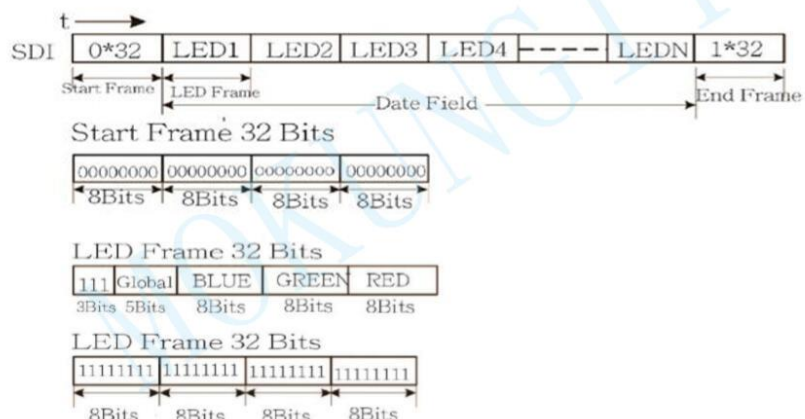
Color Rank

BIN 碼	X	Y	X	Y	X	Y	X	Y
A1	0.2609	0.3332	0.3056	0.3078	0.2752	0.2705	0.2303	0.2923
A2	0.3056	0.3078	0.3504	0.2824	0.3202	0.2487	0.2752	0.2705
A3	0.2303	0.2923	0.2752	0.2705	0.2448	0.2332	0.1996	0.2513
A4	0.2752	0.2705	0.3202	0.2487	0.29	0.212	0.2448	0.2332

■ Function description

(1) Series data structure

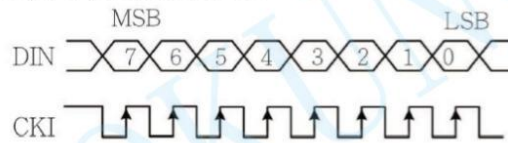
Tandem N-LED



(2) 5-Bit (level 32) brightness adjustment (simultaneous control of OUTR\OUTG\OUTB three port current)

DATA MSB↔LSB	Driving Current
00000	0/31
00001	1/31
00010	2/31
...	
11110	30/31
11111	31/31(max)

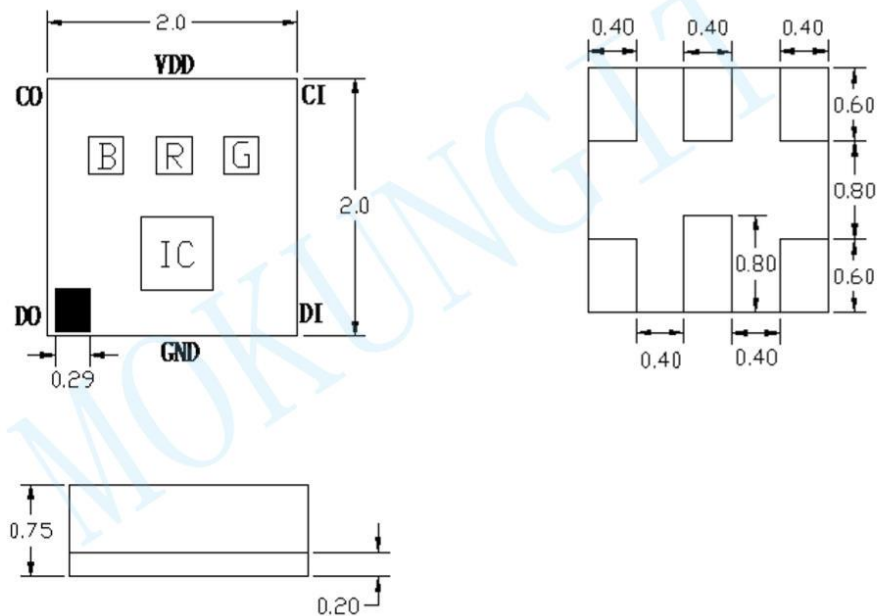
(3) PWM input/output signals relations



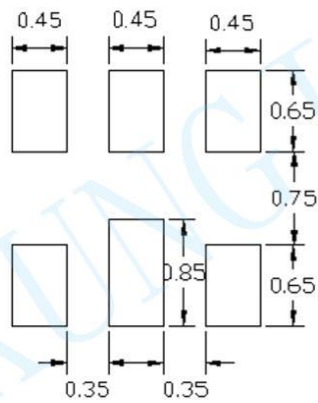
Data MSB—	Duty Cycle
00000000	0/256(min)
00000001	1/256
00000010	2/256
...	
11111101	253/256
11111110	254/256
11111111	255/256(max)

Maximum serial input data clock
PWM :20KHz,frequency 30MHZ (lighting
application When the clock frequency is
controlled below 2MHz)

■ Dimensions

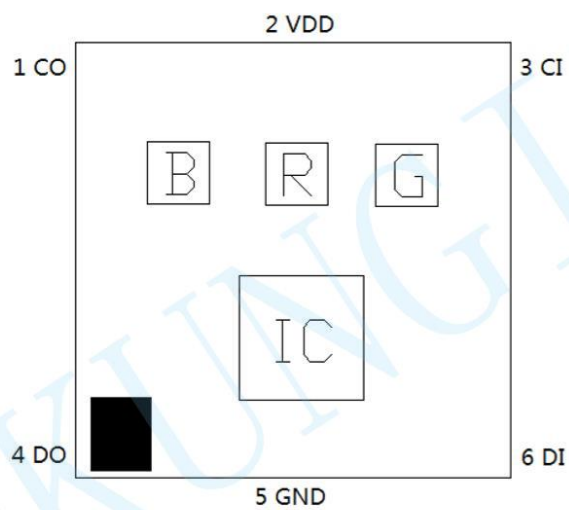


RECOMMEND PAD LAYOUT



- § All dimensions are in millimeters.
- § Tolerance is ± 0.1 mm unless otherwise specified
- § Specifications are subject to change without notice

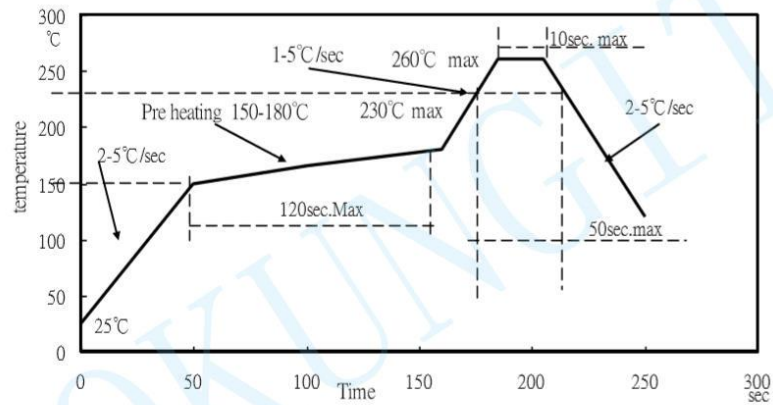
PIN Configuration



No.	Symbol	Function description
1	CO	Clock output
2	VDD	supply voltage
3	CI	Clock input
4	DO	Data output
5	GND	Ground
6	DI	Data input

■ Reflow Profile

1. I_R reflow soldering Profile for Lead Free solder

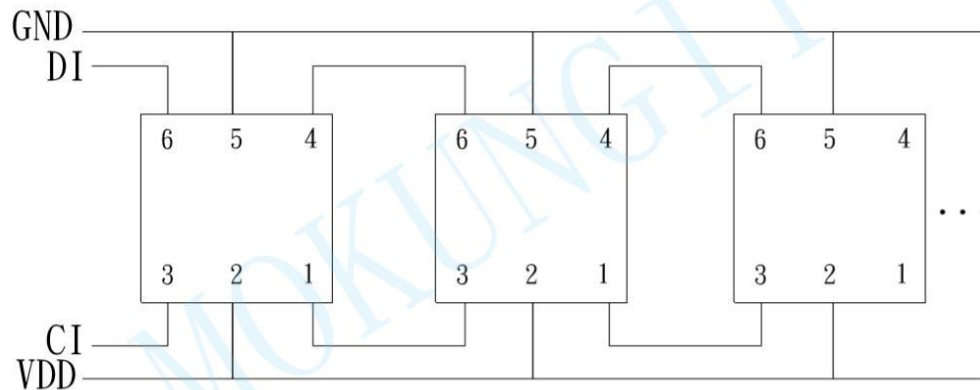


Notes:

1. We recommend the reflow temperature at 240°C ($\pm 5^\circ\text{C}$), and the maximum soldering temperature should be limited to 260°C.
2. Don't cause stress to the silicone resin while it is exposed to high temperature.
3. Number of reflow process shall not be more than 1 time.

■ Test Circuit and Precautions for Use

1. Typical application circuit



2. Precautions for Use

- 2.1. Over-current-proof

Customer must apply resistors for protection; otherwise slight voltage shift will cause big current change (Burn-out will happen).

2.2. Storage

- 1). To store the products is recommended with following conditions:

Humidity: 60% R.H. Max.

Temperature: $5^{\circ}\text{C}\sim 30^{\circ}\text{C}$ ($41^{\circ}\text{F}\sim 86^{\circ}\text{F}$)

- 2). Shelf life in sealed bag: 12 month at $<5^{\circ}\text{C}\sim 30^{\circ}\text{C}$ and $<60\%$ R.H. after the package is Opened, the products should be used within 1 week or they should be keeping to stored at $\leq 20\%$ R.H. with zip-lock sealed.

2.3. Baking

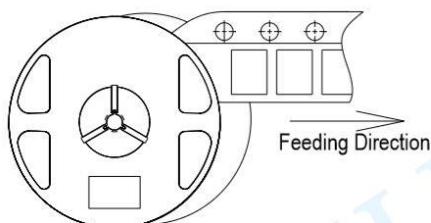
If the package has been opened for more than 1 week, it is recommended to bake the products with the following instruction:

- 1). $60\pm 3^{\circ}\text{C}$ X 6hrs and $<5\%$ RH, for reel
- 2). $125\pm 3^{\circ}\text{C}$ X 2hrs, for single LED

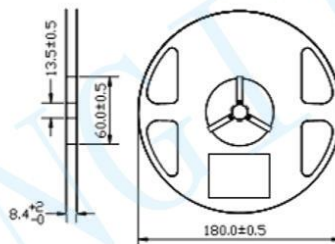
It shall be normal to see slight color fading of carrier (light yellow) after baking in process

■ Packing

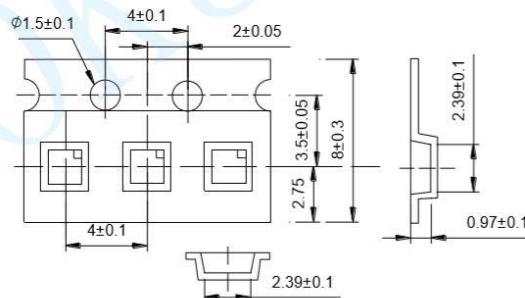
● Feeding Direction



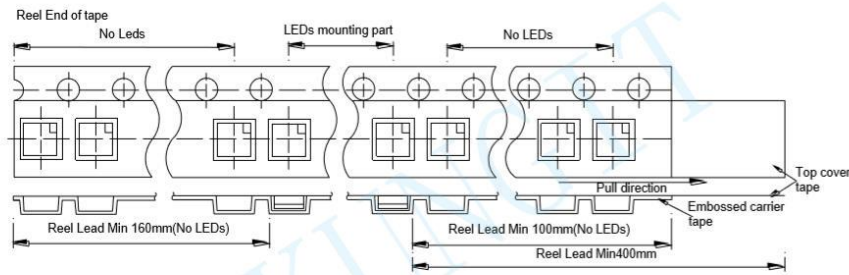
● Dimensions of Reel (Unit: mm)



● Dimensions of Tape (Unit: mm)



● Arrangement of Tape

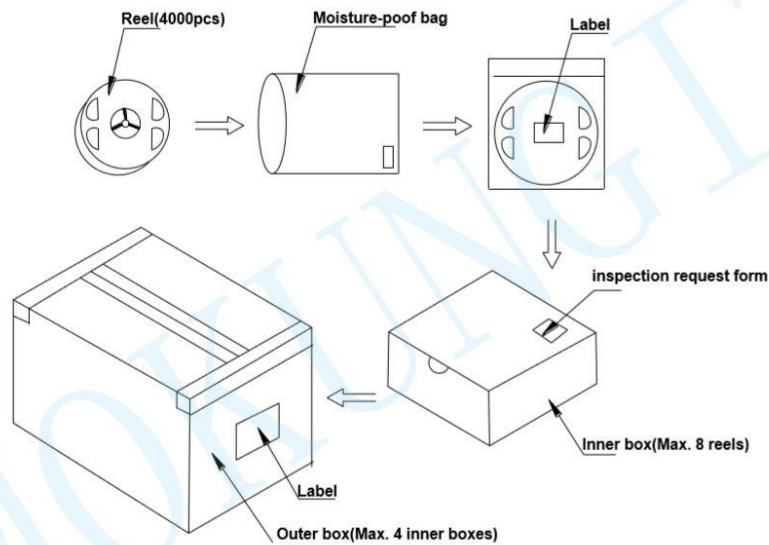


Notes:

1. Empty component pockets are sealed with top cover tape;
2. The max loss number of SMD is 2pcs;
3. The cathode is oriented towards the tape sprocket hole in accordance with ANSI/EIA RS-481 specifications;
4. 4000pcs per reel

■ Packing

● Packaging Specifications



Notes:

Reeled product (max.4000) is packed in a sealed moisture-proof bag. Seven bags are packed in an inner box (size: about 260 X 230 X 100 mm) and four inner boxes are in an outer box (size: about 480 X 275 X 215mm). On the label of moisture-proof bag, there should be the information of Part No., Lot No. and quantity number; also the total quantity number should be on inspection request form on outer box.

■ Precautions

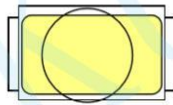
1. Abnormal situation caused by improper setting of collet

To choose the right collet is the key issue in improving the product's quality. LED is different from other electronic components, which is not only about electrical output but also for optical output. This characteristic made LED more fragile in the process of SMT. If the collet's lowering down height is not well set, it will bring damage to the gold wire at the time of collet's picking up and loading which will cause the LED fail to light up, light up now and then or other quality problems

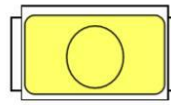
2. How to choose the collet

During SMT, please choose the collet that has larger outer diameter than the lighting area of lens, in case that improper position of collet will damage the gold wire inside the LED. Different collets fit for different products, please refer to the following pictures cross out

Outer diameter of collet should be larger than the lighting area



Picture 1(√)



Picture 2(X)

3. Other points for attention

- A. No pressure should be exerted to the epoxy shell of the SMD under high temperature.
- B. Do not scratch or wipe the lens since the lens and gold wire inside are rather fragile and cross out easy to break.
- C. LED should be used as soon as possible when being taken out of the original package, and should be stored in anti-moisture and anti-ESD package.

4. This usage and handling instruction is only for your reference.

■ Test Items and Results of Reliability

Test Item	Test Conditions	Duration/ Cycle	Ac/Re	Number of Damage	Reference
Normal Temperature Life	Ta=23°C(±5°C) If=10mA(5bit-level 15)	1008 hrs	0/1	0/22	JESD22 A-108
High Temperature Life	Ta=85°C(±5°C) If=10mA(5bit-level 15)	1008 hrs	0/1	0/22	JESD22 A-108
High Humidity Heat Life	Ta=85°C(±5°C) RH=85% If=10mA(5bit-level 15)	1008 hrs	0/1	0/22	JESD22 A-108
Thermal shock	-45°C/30min~105°C /30min (±5°C)	1008 hrs	0/1	0/22	JESD22 A-104
Electrostatic Discharge (ESD) Test	According to the SPEC	3 cycles	0/1	0/22	AEC Q101-001
Low Temperature Storage	Ta=-40°C	1008 hrs	0/1	0/22	JESD22-A103D
High Temperature Storage	Ta=105°C	1008 hrs	0/1	0/22	JESD22-A103D

*Criteria for Judging				
Item	Symbol	Condition	Criteria for Judgment of Pass	
			Min	Max
Luminous Intensity	Iv	If=10mA	LSL* ² ×0.7	-

[Note] LSL*²: Lower Specification Level