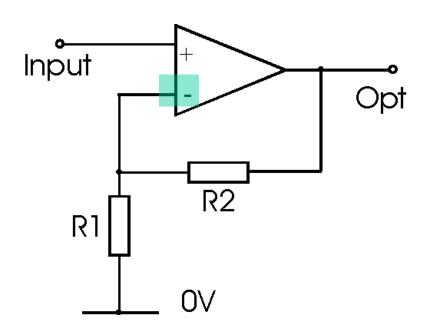
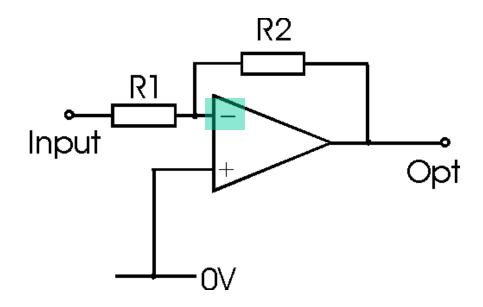
AE 242 Aerospace Measurements Laboratory





Integrated circuit many stages of matched transistor amplifiers

Very high gain differential amplifier

High input impedance and low output impedance

Initially designed for mathematical operations: multiplication, division, addition, subtraction etc

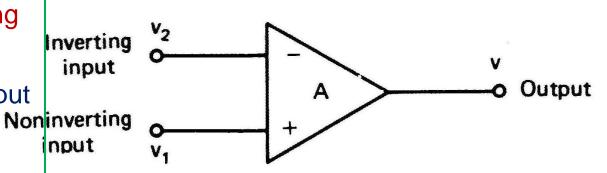
v₁ - voltage at non-inverting input

v₂ - voltage at inverting input

v - Output voltage

A - Open loop gain

All are measured wrt ground



$$V = A(V_1 - V_2)$$

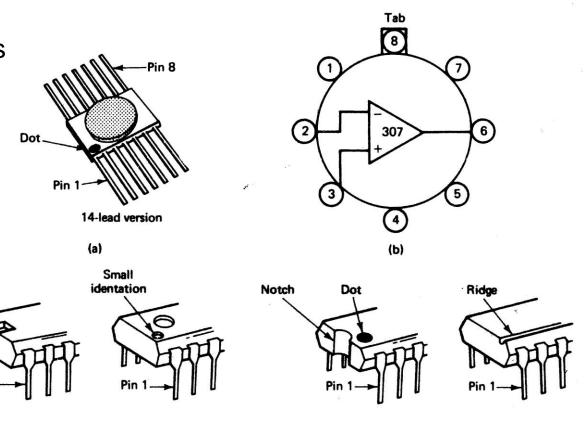
Output is amplification of difference at input terminals

Simple to use and understand as compared to transistors. Large number of applications. Most popular is 741 (8 pin dip).

Notch

Can be used in many modes

- Differential
- Single ended
- Open loop
- Feed back



Pin assignment

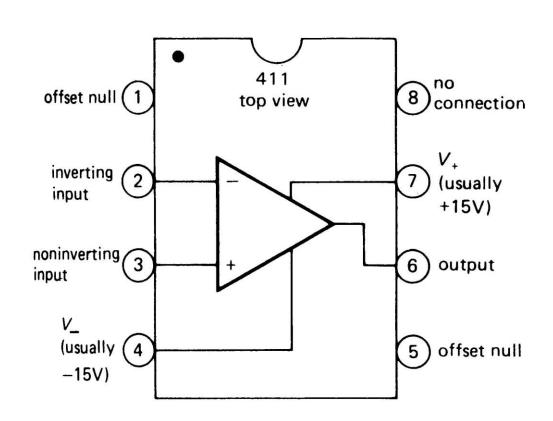
Generally in circuit diagram connection are pointed to pin number 2, 3, 4, 7 and 6.

Pin 2 & 3 are input

Pin 4 & 7 are power supply

Pin 6 is output

Pin 1 & 5 for offset null



Common mode rejection ratio (CMRR): Ratio of common mode differential voltage gain A_d to common mode voltage gain A_{cm} .

$$CMRR = \frac{A_d}{A_{cm}} \qquad \qquad \begin{array}{c} \text{Inverting input} & \text{$\frac{v_2}{v_1}$} \\ \text{Noninverting input} & \text{$\frac{v_2}{v_1}$} \end{array}$$

Generally A_d is very large compared to A_{cm} and CMRR is a very large number. Typically 90dB \sim 31.6 k Higher the number it is better. This quality is important when the OpAmp is used in differential mode. Helps in noise reduction. Generally noise is common to both the input terminals and the applied signal is voltage difference at the input terminals. For precision opamp it is as high as 120 dB.

$$dB = 20\log_{10} x$$

dB	Power ratio	Amplitude ratio	
100	10 000 000 000	100 000	
90	1 000 000 000	31 623	
80	100 000 000	10 000	
70	10 000 000	3 162	
60	1 000 000	1 000	
50	100 000	316 .2	
40	10 000	100	
30	1 000	31.62	
20	100	10	
10	10	3 .162	
6	3 .981	≈ 4 1.995 ≈ 2	
3	1 .995	≈ 2 1 .413 ≈ √2	
1	1.259	1.122	
0	1	1	
-1	0.794	0.891	
-3	0 .501	≈ ½ 0.708 ≈ √½	
-6	0 .251	≈ ½ 0.501 ≈ ½	
-10	0.1	0.3162	
-20	0.01	0.1	
-30	0.001	0 .031 62	
-40	0.000	1 0.01	
-50	0.000	0 .003 162	
-60	0.000	0.001	
-70	0.000	0.000 316 2	
-80	0.000	0.000 1	
-90	0.000	000 001 0 .000 031 62	
-100	0.000	000 000 1 0 .000 01	
An example scale showing power ratios x , amplitude ratios \sqrt{x} , and dB equivalents 10 $\log_{10} x$.			

 $dB = 20\log_{10} x$

dB values

<u>Large-signal voltage gain:</u> Ratio of output voltage to difference of input voltage. It is very high 20 k. It means that 0.05 mV differential voltage at input will give one volt output.

<u>Output voltage swing:</u> This is the saturation voltage of output in positive and negative range. This will always be less than supply voltage, generally 1 to 2 volts. Opamp with rail to rail output are also available.

<u>Output resistance</u>: Equivalent resistance at output i.e. measured between output terminal and ground. It is ~ 75 ohms.

Gain bandwidth product: The bandwidth of the op-amp when the voltage gain is one. This can be obtained from open loop voltage gain and frequency characteristics.

<u>Slew rate</u>: Maximum rate of change of output volt per second, important for high frequency signal. V/ μ S This is a measure of how rapidly output will change in response to input signal. Perfect square wave is not possible.

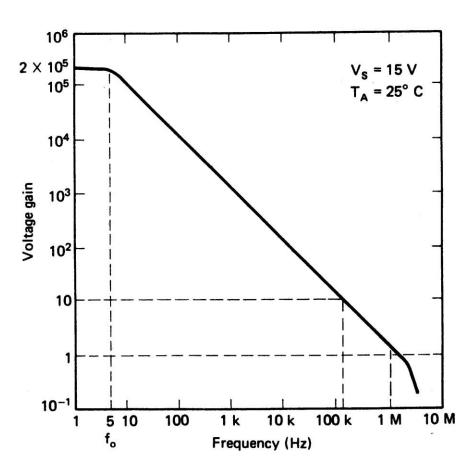
Bandwidth with feed back

Bandwidth of an amplifier is defined as the band of frequencies for which the gain remain constant

f₀ is break frequency (5 Hz)

$$f_F = f_0 (1 + AB)$$

f_F is bandwidth with feed back, B is feed back gain



Open loop gain versus frequency

Ideal op-amp

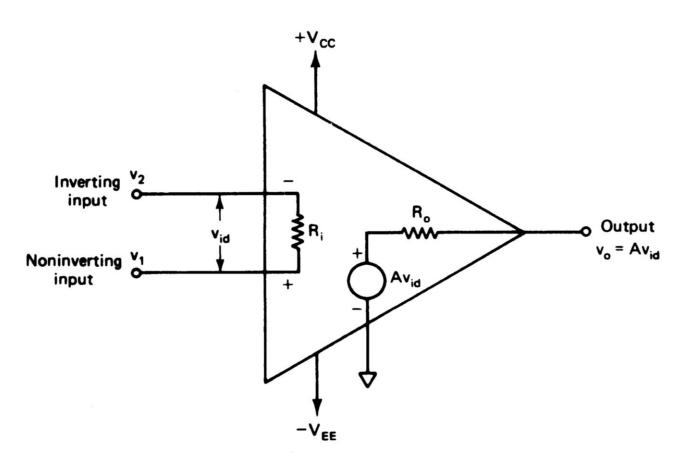
- Infinite voltage gain
- Infinite input resistance no loading on source
- Zero output resistance so that output can drive any number of devices
- Zero output voltage when input voltage is zero
- Infinite bandwidth
- Infinite CMRR
- Infinite slew rate

Characteristic	Ideal value	Typical real-world value
Open-loop gain A	00	100,000 V/V
Offset voltage V	0	±1 mV @ 25°C
Bias currents i_A , i_B	0	10 ⁻⁶ to 10 ⁻¹⁴ A
Input impedance Z _D	00	10^{5} to $10^{11} \Omega$
Output impedance Z	0 - 1	1 to 10 Ω

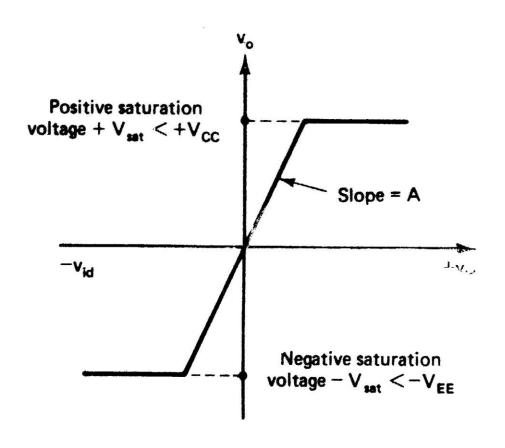
For practical opamps and practical use many of the characteristic can be approximated to above mentioned characteristics.

Equivalent circuit of an op-amp

Equivalent circuit is shown using input resistance, output resistance, and open loop gain. Input resistance will load the source and output resistance will drive the output.



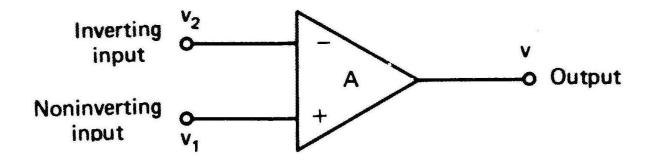
Ideal Voltage transfer curve: Output voltage cannot exceed the positive and negative saturation voltages. Offset voltage is zero. Slope is generally very large (almost vertical). Amplifies the difference in input voltages not the individual voltages.



Open-loop op-amp configuration

No connection between output and input

- 1 Differential amplifier
- 2 Inverting amplifier
- 3 Noninverting amplifier



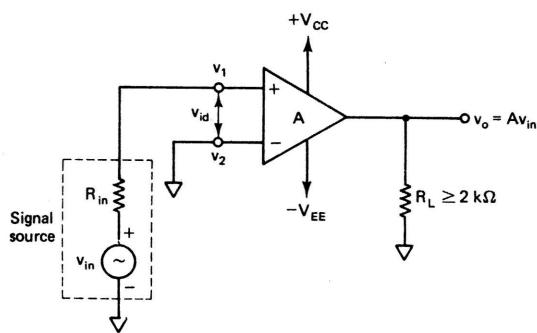
Non-inverting amplifier

Signal is applied to non-inverting (positive) terminal. Inverting terminal is grounded.

 $v_0 = A (v_1 - v_2) = A v_1$, where A is open loop gain

Output is in phase with input, it will have same polarity as input.

In open loop configurations any input signal of few milli volts will saturate the output due to high open loop gain



Difference input voltage ideally zero

Opamp equation

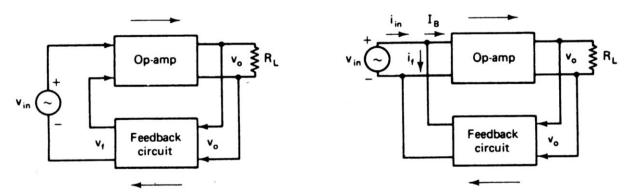
$$\mathbf{v}_{\mathrm{id}} = \frac{\mathbf{v}_{0}}{\mathbf{A}}$$

A is very large and ideally infinite. $V_{id} = 0$

 $v_1 = v_2$, inverting and non-inverting terminal are at same voltage

<u>Golden rule 1:</u> opamp will work in such a way that it will drive the two inputs to same level

Golden rule 2: No current can flow into opamp

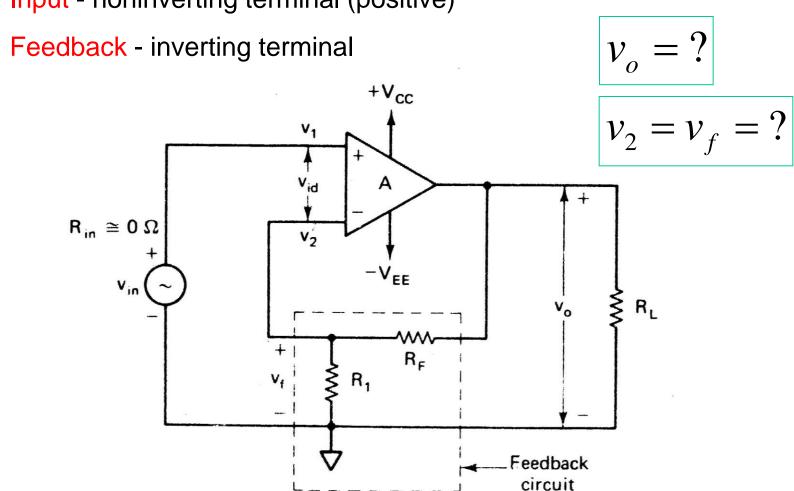


Using above two rules op-amp circuit analysis becomes easy.

Applicable for negative feedback

Voltage series feed back - Noninverting amplifier

Input - noninverting terminal (positive)



Closed loop voltage gain $\mathbf{A}_{\mathbf{F}} = \frac{\mathbf{V}_0}{\mathbf{V}_0}$

$$\mathbf{A}_{\mathbf{F}} = \frac{\mathbf{v}_{0}}{\mathbf{v}_{in}}$$

$$\mathbf{v}_{\mathbf{o}} = \mathbf{A}(\mathbf{v}_{1} - \mathbf{v}_{2})$$

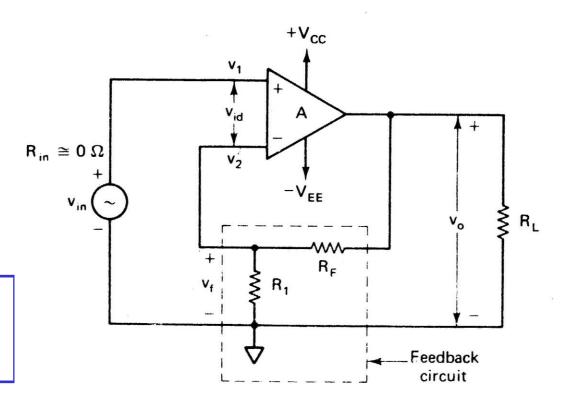
$$\mathbf{v}_{_1} = \mathbf{v}_{_{\mathbf{in}}}$$

$$\mathbf{v_o} = \mathbf{A}(\mathbf{v_1} - \mathbf{v_2})$$
 $\mathbf{v_1} = \mathbf{v_{in}}$ $\mathbf{v_2} = \mathbf{v_f} = \frac{\mathbf{R_1}\mathbf{v_o}}{\mathbf{R_1} + \mathbf{R_F}}$

$$\mathbf{v}_{o} = \frac{\mathbf{A}(\mathbf{R}_{1} + \mathbf{R}_{F})\mathbf{v}_{in}}{\mathbf{R}_{1} + \mathbf{R}_{F} + \mathbf{A}\mathbf{R}_{1}}$$

$$A_F = \frac{v_0}{v_{in}} = \frac{A(R_1 + R_F)}{R_1 + R_F + AR_1}$$

Exact gain, involves open loop gain and generally not known correctly



Closed loop voltage gain

$$A_F = \frac{v_0}{v_{in}} = \frac{A(R_1 + R_F)}{R_1 + R_F + AR_1}$$

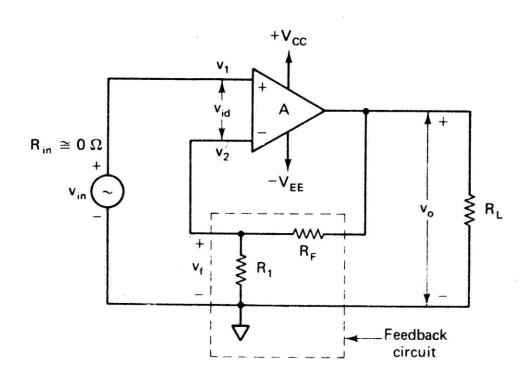
Exact

A is open loop gain $AR_1 >> R_1 + R_F$ $A_F = \frac{v_0}{} = 1 + \frac{R_F}{}$

$$A_F = \frac{v_0}{v_{in}} = 1 + \frac{R_F}{R_1}$$
 Ideal

Gain is decide by two resistance: R₁ and R₅ independent of open loop gain

As a general rule R₁ and R_F should be less than 1 M Ω



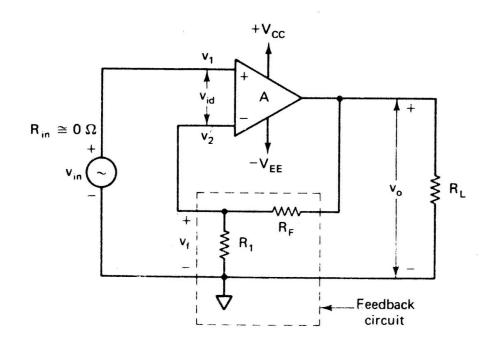
Feedback gain
$$\mathbf{B} = \frac{\mathbf{v_f}}{\mathbf{v_o}} = \frac{\mathbf{R_1}}{\mathbf{R_1} + \mathbf{R_F}}$$

$$\mathbf{A}_{\mathbf{F}} = \frac{1}{\mathbf{B}}$$

$$A_F = \frac{v_0}{v_{\rm in}} = \frac{A(R_1 + R_F)}{R_1 + R_F + AR_1} \quad A_F = \frac{v_0}{v_{\rm in}} = \frac{A(R_1 + R_F)/(R_1 + R_F)}{(R_1 + R_F)/(R_1 + R_F) + AR_1/(R_1 + R_F)}$$

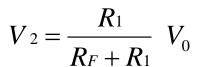
$$A_{\rm F} = \frac{A}{1 + AB}$$
 Exact

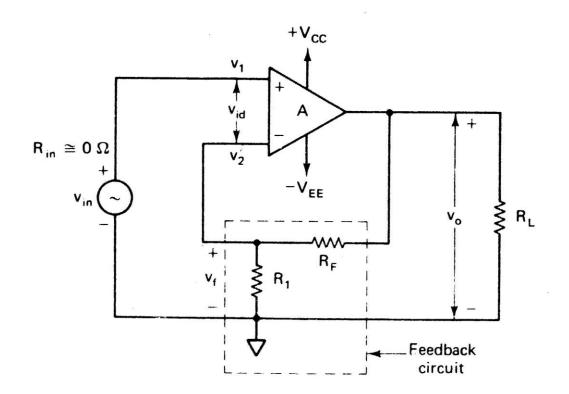
$$\mathbf{A}_{\mathbf{F}} = \frac{\mathbf{v}_{\scriptscriptstyle 0}}{\mathbf{v}_{\scriptscriptstyle \mathrm{in}}} = \frac{1}{\mathbf{B}}$$
 Ideal



Using Golden rule

a) $v_1 = v_2$ b) No current flows in to opamp





$$V_1 = V_{in} = V_2$$

$$\frac{V_0}{V_{in}} = 1 + \frac{R_F}{R_1}$$

Input resistance with feed back

$$R_{iF} = R_i (1+AB) \qquad B = 1/A_F$$

It means that input resistance in feed back is (1+AB) times than open loop input resistance R_i It means that input resistance increases.

Output resistance with feed back

$$R_{oF} = R_o / (1 + AB)$$

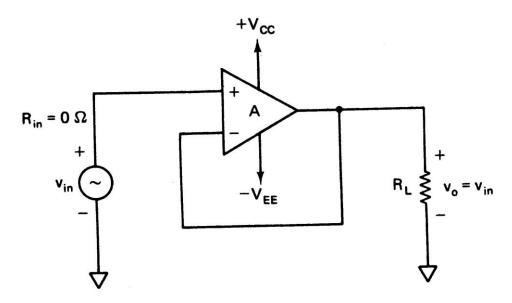
It means that output resistance in feed back is 1/(1+AB) times open loop output resistance R_o It means that output resistance in feed back is much smaller compared to open loop.

Voltage follower

Lowest gain that can be obtained by a non-inverting amplifier is one, this configuration is called voltage follower. Output will follow the input.

Output is exactly equal to input and input resistance is very high.

Used as *isolator*. Any system connected on right side will obtain power from OpAmp. Power drawn from input will be almost zero, depends on the input resistance of OpAmp.



Voltage shunt feedback amplifier

Non-inverting terminal is grounded. Signal to inverting terminal in series with resistance R₁

$$i_{in} = i_F + I_{B2}$$
 $I_{B2} \approx 0$ $I_{in} = \frac{v_{in} - v_2}{R_1} = \frac{v_2 - v_o}{R_E}$ $\mathbf{v}_1 - \mathbf{v}_2 = \frac{\mathbf{v}_0}{\mathbf{A}}$

$$\mathbf{A}\mathbf{F} = \frac{\mathbf{v}_0}{\mathbf{v}_{in}} = -\frac{\mathbf{A}\mathbf{R}\mathbf{F}}{\mathbf{R}_1 + \mathbf{R}\mathbf{F} + \mathbf{A}\mathbf{R}_1} \qquad \mathbf{v}_1 = 0 \qquad \mathbf{v}_2 = -\frac{\mathbf{v}_0}{\mathbf{A}}$$

$$A_F = \frac{V_0}{V_{in}} = -\frac{R_F}{R_1}$$

$$R_{in} \cong 0 \Omega$$

$$V_{in} \longrightarrow V_{in}$$

$$R_{in} \cong 0 \Omega$$

$$V_{in} \longrightarrow V_{in}$$

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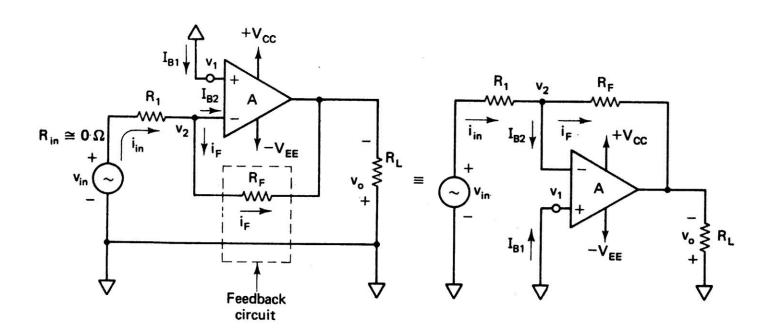
$$R_{in} \longrightarrow$$

Voltage shunt feedback amplifier

$$A_{\mathrm{F}} = rac{\mathrm{V}_{\mathrm{0}}}{\mathrm{V}_{\mathrm{in}}} = -rac{\mathrm{R}_{\mathrm{F}}}{\mathrm{R}_{\mathrm{1}}}$$

Gain less than one possible

Gain is set by two resistance R₁ and R_F and independent of A



Voltage shunt feedback amplifier

Using Golden rule

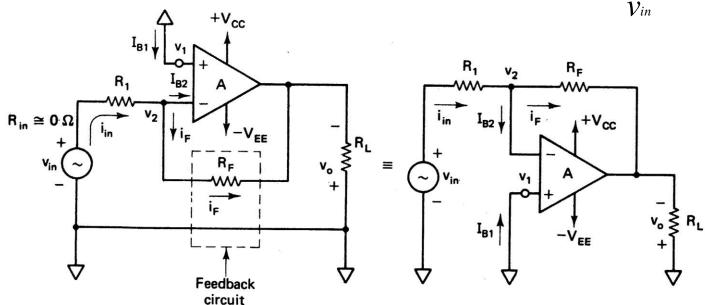
a) $v_1 = v_2$ b) No current flows in to opamp

$$A_F = \frac{V_0}{V_{in}} = -\frac{R_F}{R_1}$$

$$\frac{V_{in} - V_2}{R_1} = \frac{V_2 - V_0}{R_F}$$

$$V_1 = 0 = V_2$$

$$\frac{v_0}{v_{in}} = -\frac{R_F}{R_1}$$



Input resistance with feed back

$$R_{iF} = R_1$$

It means that input resistance in feed back is ~ resistance R₁ much less compared to open loop input resistance R_i Input resistance reduces considerably.

Output resistance with feed back

$$R_{oF} = R_o / (1 + AB)$$

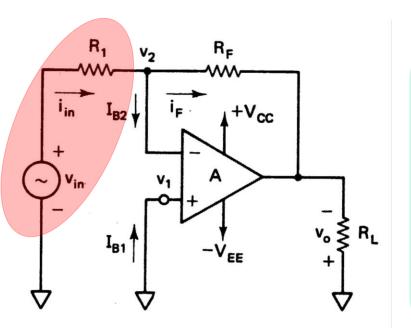
It means that output resistance in feed back is 1/(1+AB) times open loop output resistance R_o Output resistance in feed back is much smaller compared to open loop. Same as non-inverting configuration.

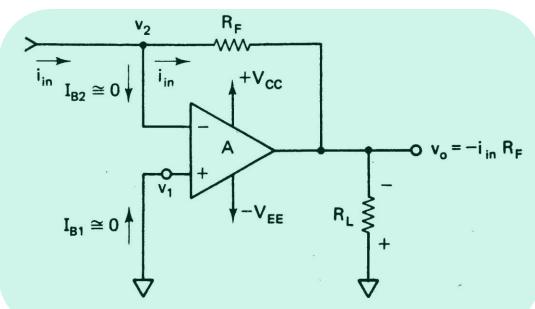
Current to voltage converter

$$\mathbf{v}_{\scriptscriptstyle 0} = -\frac{\mathbf{v}_{\scriptscriptstyle \mathrm{in}}}{\mathbf{R}_{\scriptscriptstyle \mathrm{I}}}\,\mathbf{R}_{\scriptscriptstyle \mathrm{F}}$$

If replace v_{in} and R₁ combination by a current source, the output voltage will be proportional to current

Sensing current from photodiodes





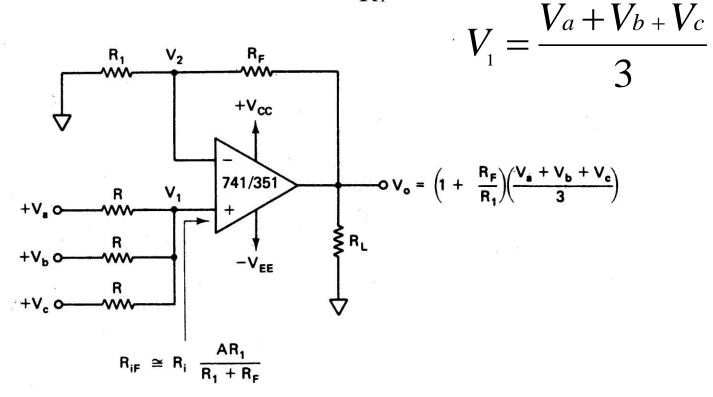
Summing amplifier - noninverting mode

Averaging of input signal

$$V_o = (1 + \frac{R_F}{R_1}) \frac{V_a + V_{b+} V_c}{3}$$

Summing, number of inputs =

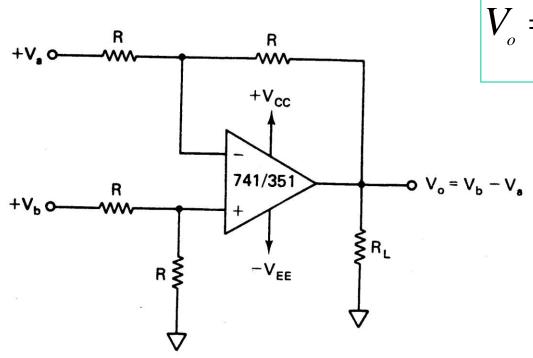
$$(1+\frac{R_F}{R_1})$$



Subtractor

Gain in inverting mode is 1

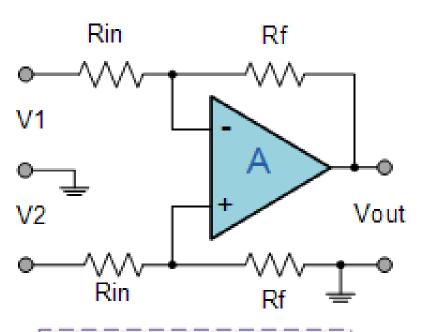
Gain in noninverting mode is 2



$$V_o = \frac{V_b}{2} \left(1 + \frac{R}{R} \right) - V_a \frac{R}{R}$$

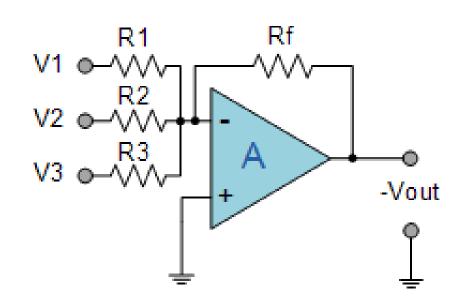
Output, $V_o = V_b - V_a$

Differential Op-amp



$$Vout = \frac{Rf}{Rin}(V2 - V1)$$

Summing Op-amp



$$Vout = -\left(\frac{Rf}{R1}V1 + \frac{Rf}{R2}V2 + \frac{Rf}{R3}V3\right)$$

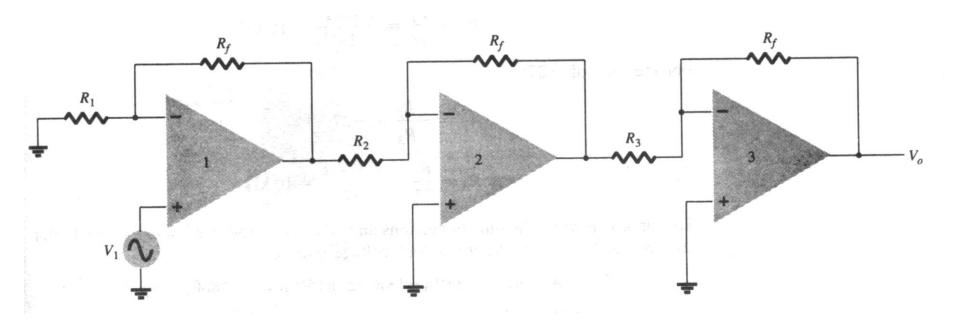
Multi stage amplifier

 $v_0 = A V_1$ where $A = A_1 A_2 A_3$

 A_1 = Amplification of first amplifier

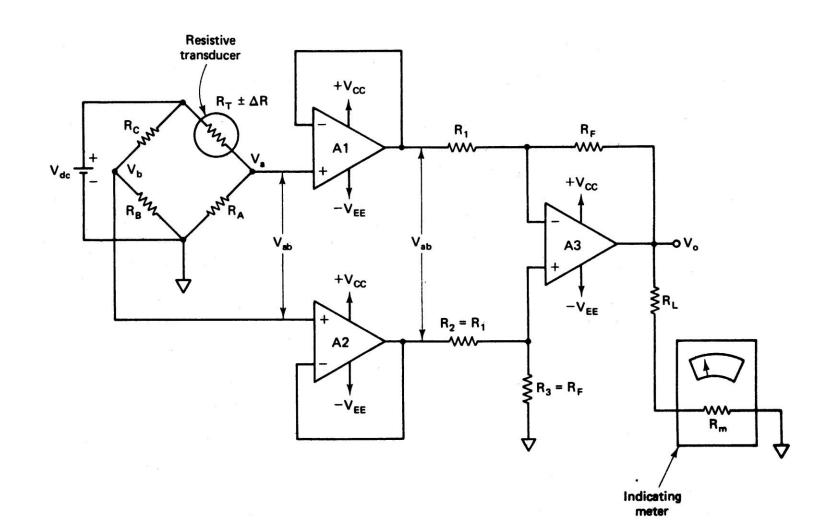
 A_2 = Amplification of second amplifier

 A_3 = Amplification of third amplifier



Instrumentation amplifier

Two voltage follower output given to a differential amplifier

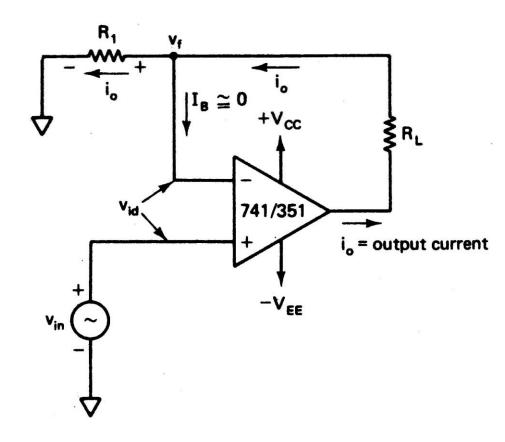


Voltage to current converter

$$V_{id} = 0$$
; $V_f = V_{in}$

$$\mathbf{i}_{o} = \frac{\mathbf{v}_{f}}{\mathbf{R}_{1}}$$
 $i_{o} = \frac{V_{in}}{R_{1}}$

Current is proportional to input voltage



The integrator

The output is integral of the input signal is called integrator amplifier.

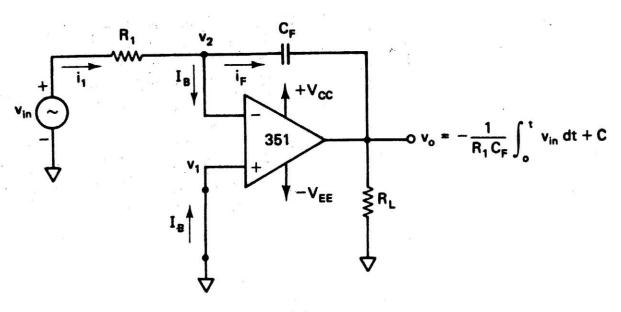
$$i_c = C \frac{dv_c}{dt}$$

$$\frac{V_{in} - V_2}{R_1} = C_F \frac{d}{dt} (V_2 - V_0) \qquad V_2 = V_1 = 0$$

$$V_2 = V_1 = 0$$

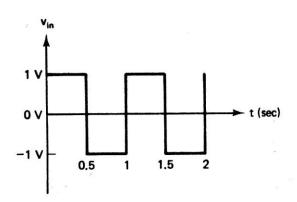
$$\frac{V_{in}}{R_1} = C_F \frac{dV_0}{dt}$$

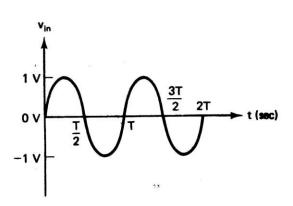
$$V_0 = -\frac{1}{C_F R_1} \int_0^t v_{in} dt + C$$

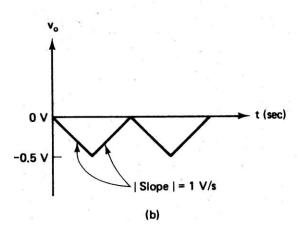


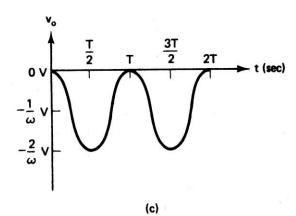
The integrator

These are ideal waveforms integration errors due to constant bias is very common, which will increase linearly with time









The differentiator

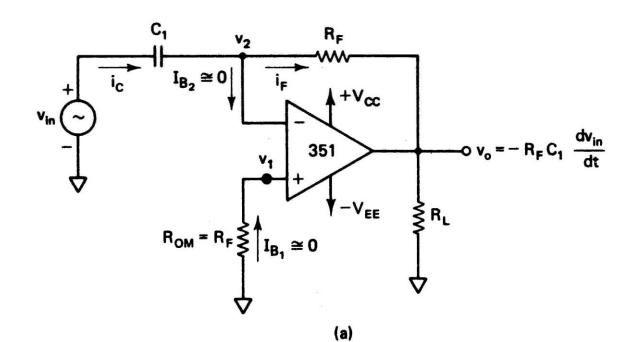
The output is differentiation of the input signal is called differentiator amplifier.

$$i_c = C \frac{dv_c}{dt}$$

$$C_F \frac{d}{dt} (V_{in} - V_2) = \frac{V_2 - V_0}{R_F}$$
 $V_2 = V_1 = 0$

$$V_2 = V_1 = 0$$

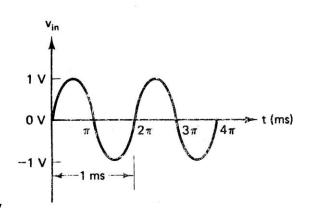
$$V_0 = -R_F C_1 \frac{dV_{in}}{dt}$$

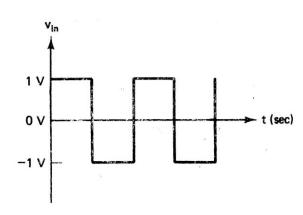


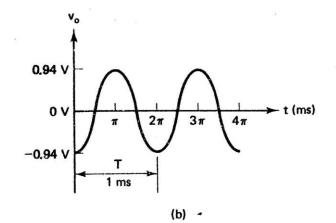
The differentiator

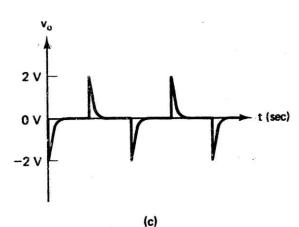
Sine wave into a cosine wave

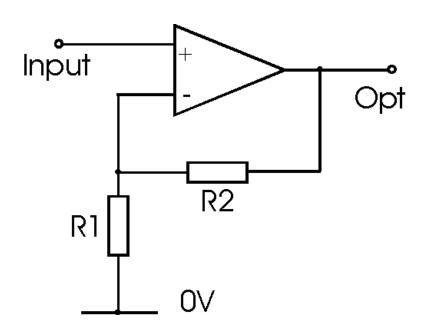
Spikes (high frequency noise) in signal is generally a problem in such circuits

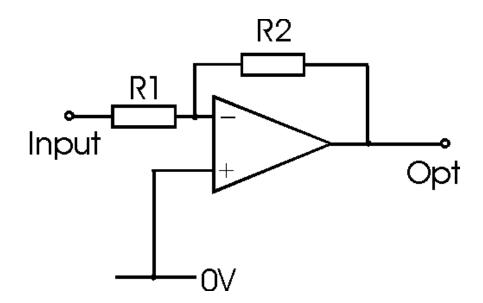












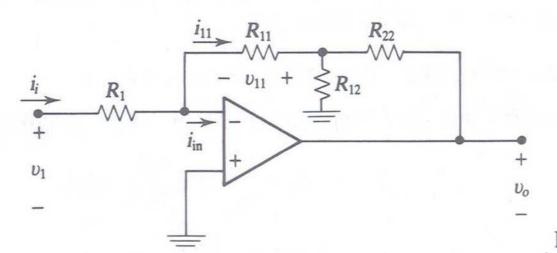


Fig. 13-23

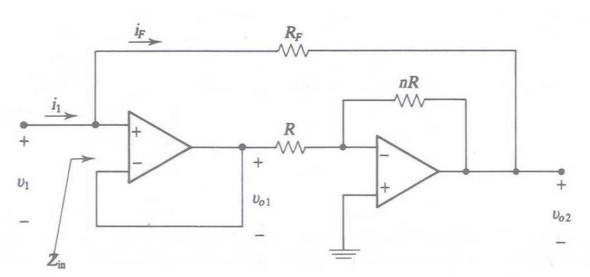


Fig. 13-25