```
(let-cells ((power (bias-voltage-source))
            (vin (signal-voltage-source))
            (vout (open-circuit))
            (amp (ce-amplifier)))
 (let-cells ((gnd
               (node (the t2 power)
                     (the t2 vin) (the t2 vout)
                     (the -rail amp)))
              (+V (node (the t1 power) (the +rail amp)))
              (in (node (the t1 vin) (the sigin amp)))
              (out (node (the t1 vout) (the sigout amp))))
   ((constant 0) (the potential gnd))))
(assume! (the strength power amp) (& 15. volt))
(assume! (the resistance rc amp) (& 5000. ohm))
(assume! (the resistance re amp) (& 1000. ohm))
```

(assume! (the resistance rb1 amp) (& 51000. ohm)) (assume! (the resistance rb2 amp) (& 10000. ohm))

(assume! (the vthreshold q amp bias) (& +0.7 volt))

(assume! (the IO q amp) (& 1e-15 ampere))

(assume! (the beta q amp) 100)