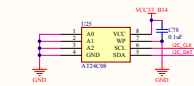
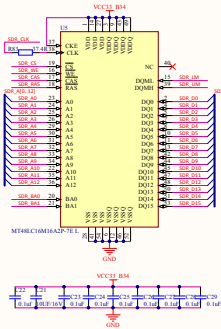


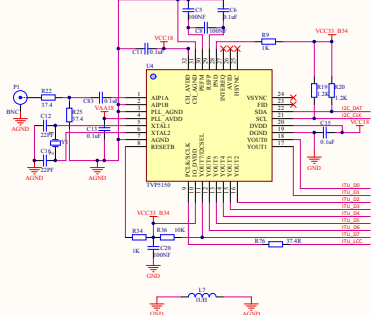
Memory(EEPROM)



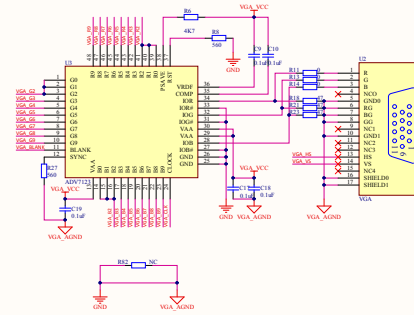
Memory(SDRAM)



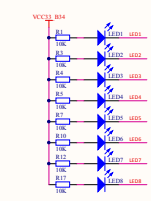
Video A/D



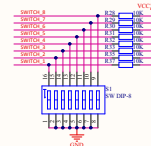
Video D/A(VGA)



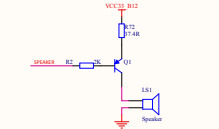
LEDS



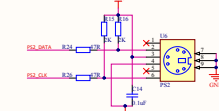
Switch



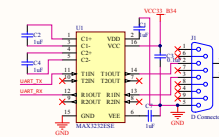
Speaker



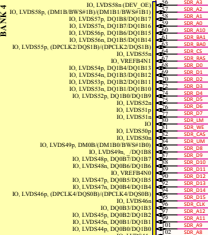
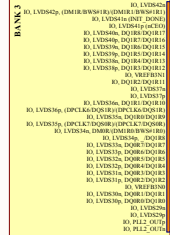
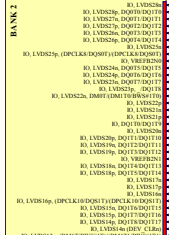
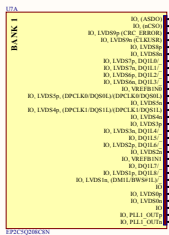
PS/2



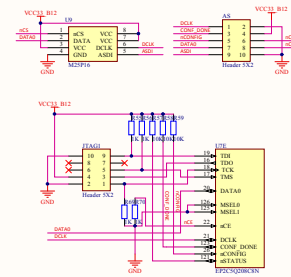
UART



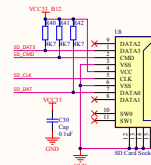
FPGA_10_BANK



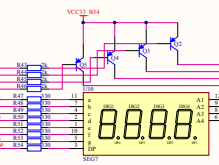
Config



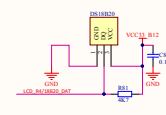
SD card



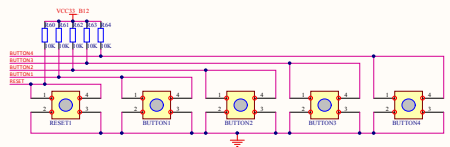
SEG7



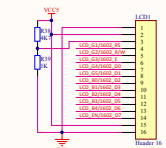
DS18B20



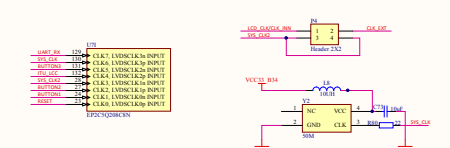
Buttons



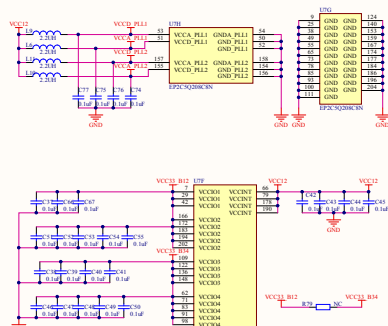
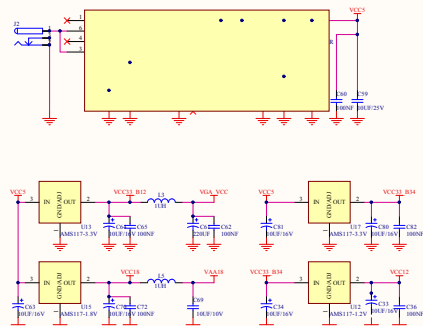
LCD1602



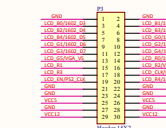
Clock



POWER



Header



Action® VIP board

Rev	Number	Revision
A1	001	1.0
File	F:\Action_VIP_Board_Action_VIP_v1.0\001.dwg	