

SGND与PGND单点连接

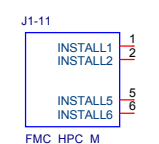
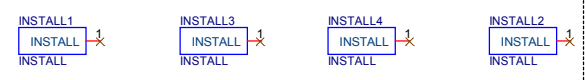


备注:
1、本页为电源部分，除地属性的线外都视作电源线；
2、+8V电压一部分给PHA-13HLN供电，电流约250mA。另一部分继续转5V给检波电路、DGA等其他电路供电。
3、DGND和GND通过R925单点相连。

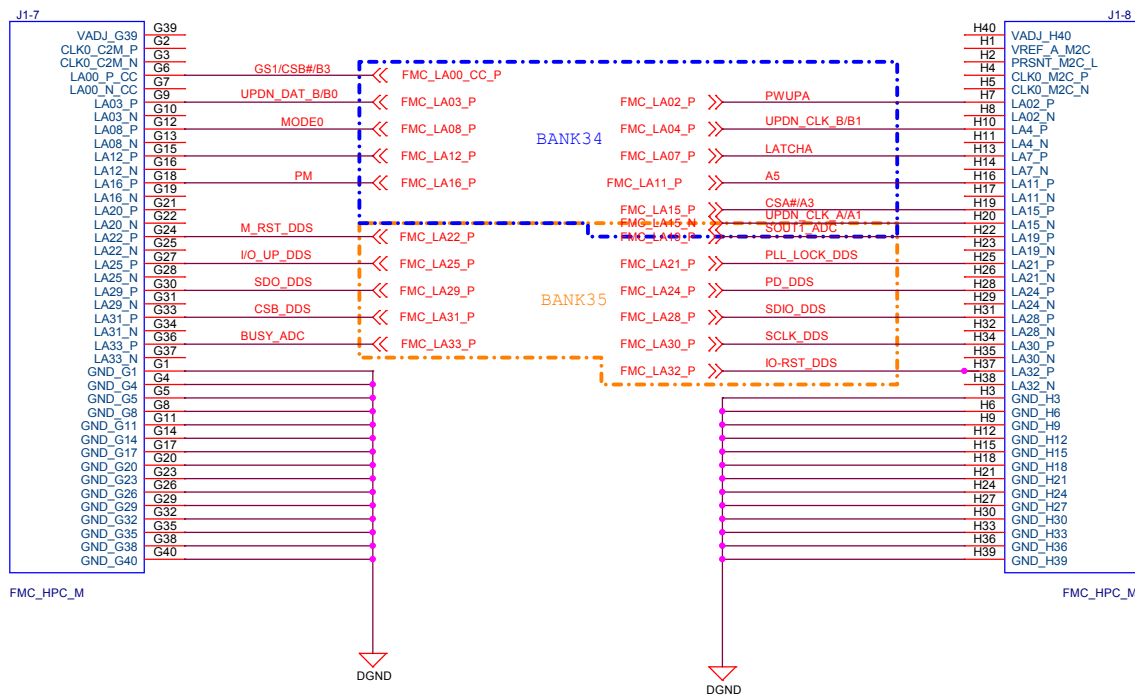
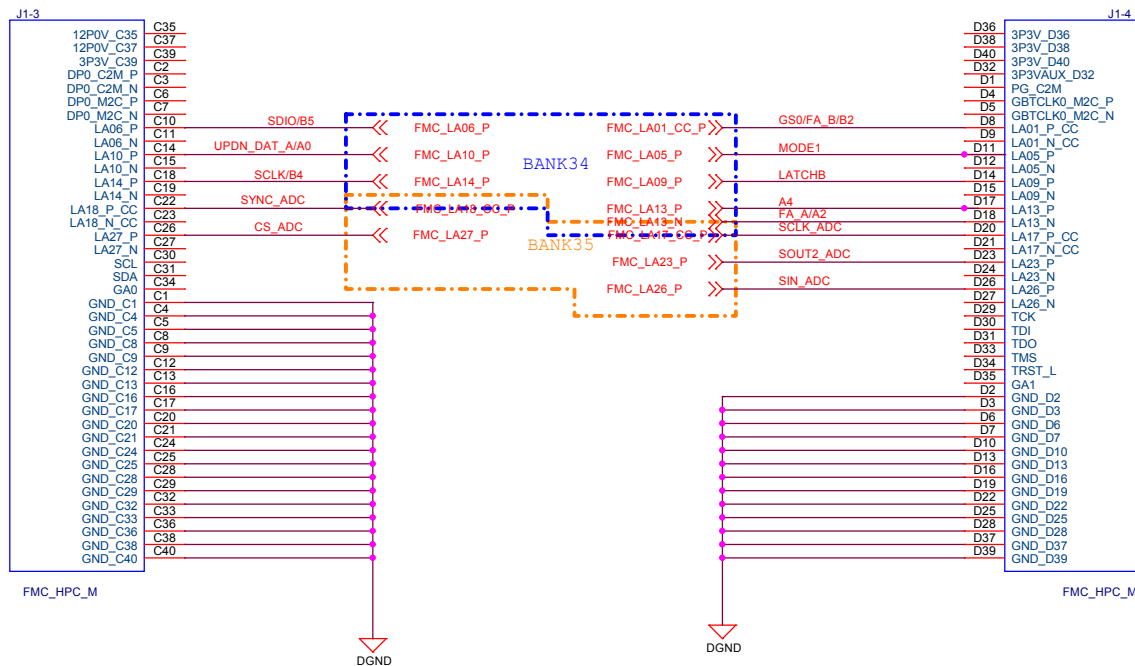
J1-2	RES0 B40 RES1 B1 DP9_M2C_P B4 DP9_M2C_N B5 DP8_M2C_P B8 DP8_M2C_N B9 DP7_M2C_P B13 DP7_M2C_N B16 DP6_M2C_P B17 DP6_M2C_N B20 GBTCLK1_M2C_P B21 GBTCLK1_M2C_N B22 DP9_C2M_P B28 DP9_C2M_N B29 DP8_C2M_P B32 DP8_C2M_N B33 DP7_C2M_P B36 DP7_C2M_N B37 DP6_C2M_P B2 DP6_C2M_N B3 GND_B3 B6 GND_B6 B7 GND_B7 B10 GND_B10 B11 GND_B11 B14 GND_B14 B15 GND_B15 B18 GND_B18 B19 GND_B19 B22 GND_B22 B23 GND_B23 B26 GND_B26 B27 GND_B27 B30 GND_B30 B31 GND_B31 B34 GND_B34 B35 GND_B35 B38 GND_B38 B39	B40 B1 B4 B5 B8 B9 B13 B16 B17 B20 B21 B22 B28 B29 B32 B33 B36 B37 B2 B3 B6 B7 B10 B11 B14 B15 B18 B19 B22 B23 B26 B27 B30 B31 B34 B35 B38 B39
J1-5	VADJ_E39 E39 HA01_P_CC E3 HA01_N_CC E6 HA05_P E7 HA05_N E10 HA09_P E12 HA09_N E13 HA13_P E15 HA13_N E16 HA16_P E18 HA16_N E19 HA20_P E22 HA20_N E24 HB03_P E25 HB03_N E27 HB05_P E28 HB05_N E30 HB13_P E31 HB13_N E33 HB19_P E34 HB19_N E36 HB21_P E37 HB21_N E1 GND_E1 E2 GND_E2 E5 GND_E5 E8 GND_E8 E11 GND_E11 E14 GND_E14 E17 GND_E17 E20 GND_E20 E23 GND_E23 E26 GND_E26 E29 GND_E29 E32 GND_E32 E35 GND_E35 E38 GND_E38 E40	E39 E3 E6 E7 E10 E12 E13 E15 E16 E18 E19 E22 E24 E25 E27 E28 E30 E31 E33 E34 E36 E37 E1 E2 E5 E8 E11 E14 E17 E20 E23 E26 E29 E32 E35 E38 E40
J1-6	VADJ_F40 F40 PG_M2C F4 HA00_P_CC F5 HA00_N_CC F7 HA04_P F8 HA04_N F10 HA08_P F11 HA08_N F13 HA12_P F14 HA12_N F16 HA15_P F17 HA15_N F20 HA19_P F22 HB02_P F23 HB02_N F25 HB04_P F26 HB04_N F28 HB08_P F31 HB08_N F32 HB12_P F34 HB12_N F35 HB16_P F36 HB16_N F38 HB20_P F39 GND_F39 F39	F40 F4 F5 F7 F8 F10 F11 F13 F14 F16 F17 F20 F22 F23 F25 F26 F28 F31 F32 F34 F35 F36 F38 F39


FMC_HPC_M	J1-1	A2 A3 A6 A7 A10 A11 A14 A15 A18 A19 A23 A27 A28 A30 A31 A34 A35 A38 A39 A1 A4 A5 A8 A9 A12 A13 A16 A17 A20 A21 A24 A25 A28 A29 A32 A33 A36 A37 A40	A2 A3 A6 A7 A10 A11 A14 A15 A18 A19 A23 A27 A28 A30 A31 A34 A35 A38 A39 A1 A4 A5 A8 A9 A12 A13 A16 A17 A20 A21 A24 A25 A28 A29 A32 A33 A36 A37 A40
FMC_HPC_M	J1-9	J40 J38 J35 J32 J29 J26 J23 J20 J17 J14 J11 J8 J5 J4 J1 J37 J36 J33 J31 J30 J28 J27 J25 J24 J22 J19 J18 J16 J15 J13 J12 J10 J9 J7 J6 J2 J39	J40 J38 J35 J32 J29 J26 J23 J20 J17 J14 J11 J8 J5 J4 J1 J37 J36 J33 J31 J30 J28 J27 J25 J24 J22 J19 J18 J16 J15 J13 J12 J10 J9 J7 J6 J2 J39
FMC_HPC_M	J1-10	K39 K38 K33 K30 K27 K24 K21 K18 K15 K12 K9 K6 K3 K2 K38 K37 K35 K34 K31 K29 K28 K26 K25 K23 K20 K19 K17 K16 K14 K13 K11 K10 K8 K7 K5 K4 K1 K40	K39 K38 K33 K30 K27 K24 K21 K18 K15 K12 K9 K6 K3 K2 K38 K37 K35 K34 K31 K29 K28 K26 K25 K23 K20 K19 K17 K16 K14 K13 K11 K10 K8 K7 K5 K4 K1 K40

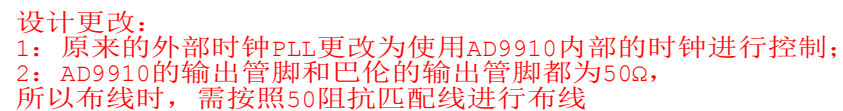
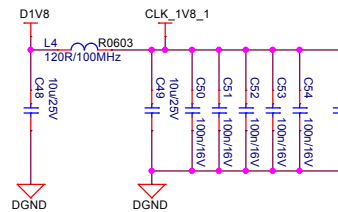
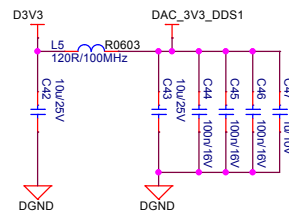
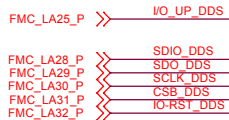
INSTALL PCB固定孔M3带金属焊盘




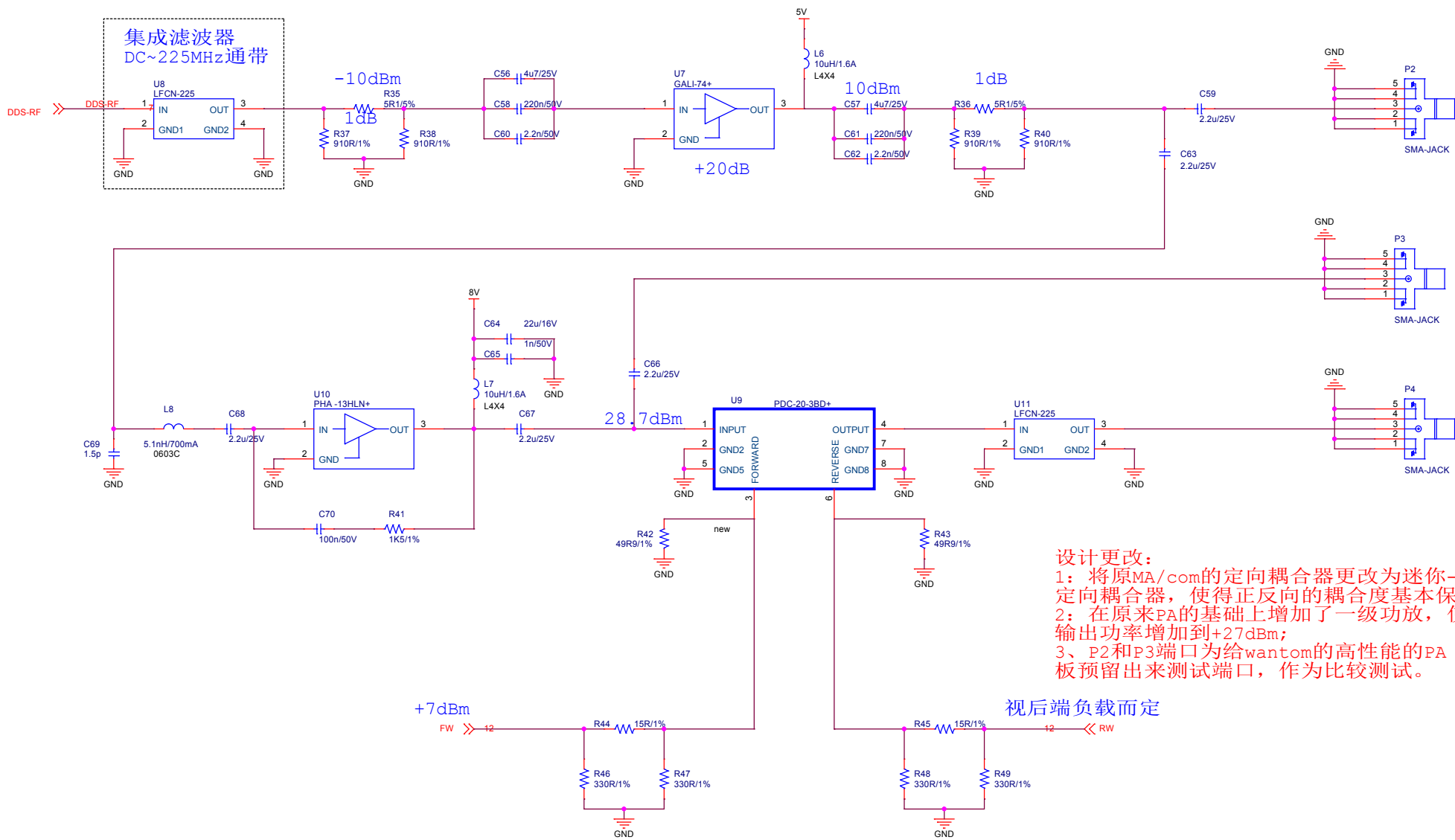
Date		Rectify content	
Design NO.		Sheet Name	
Size		Confirm / Date	
A3		Name3/Date	
Date: Friday, May 04, 2018		Sheet 1 of 6	



 恒润科技 HIRAIN TECHNOLOGIES				
		Date	Rectify content	Signer
Design NO.		Sheet Name		Rev
		Cover_Sheet		V1.0
Size	Designer / Date	Check / Date	Confirm / Date	
A3		Name2 / Date	Name3/Date	
Date:	Friday, May 04, 2018		Sheet	2 of 6



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		Date	Rectify content	Signer
Design NO. SCH_EH5010-SIG_V1_0		Sheet Name DDS		Rev V1.0
Size A3	Designer / Date	Check / Date Name2 / Date	Confirm / Date Name3/Date	
Date:	Friday, May 04, 2018	Sheet	3	of 6



设计更改:
 1: 将原MA/com的定向耦合器更改为迷你-circuit的定向耦合器, 使得正反向的耦合度基本保持一致;
 2: 在原来PA的基础上增加了一级功放, 使输出功率增加到+27dBm;
 3: P2和P3端口为给wantom的高性能的PA demo板预留出来测试端口, 作为比较测试。


视后端负载而定

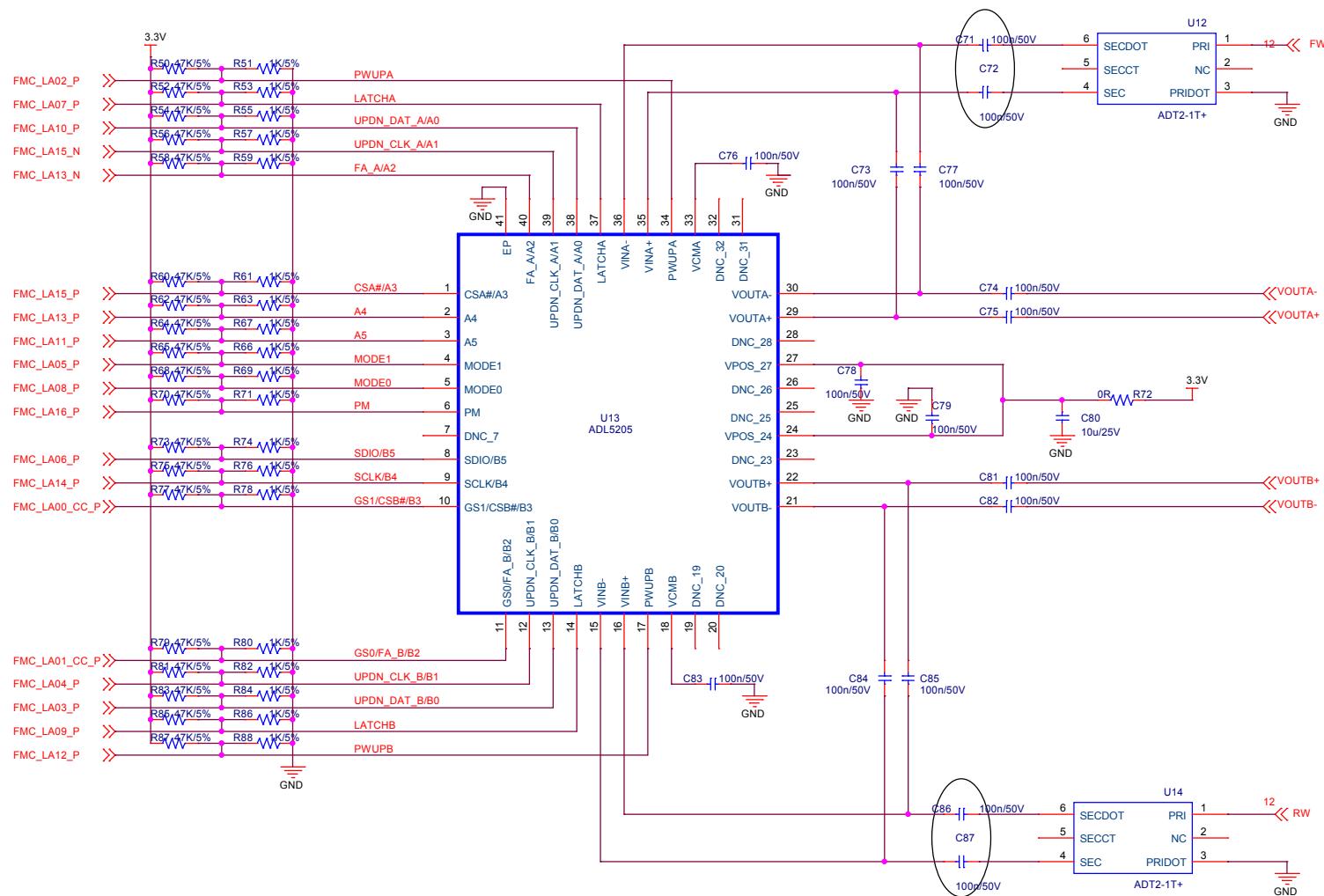
布线要求:

1、GALI-74+为功放器件, 所有涉及功放的电路(后面电路图中还有很多)要保证从前端的π型衰减器到输出的π型衰减器之间的布板满足: 信号线的走线过程中尽量直, 不要有拐弯, 更不允许走回头线。


2、本页中功放后端又有耦合器和balun, 所以直线部分尽量保证到balun后端的π型衰减器, 如果不能做到, 至少保证到balun前端的衰减器

3、第11、12两页的电路图走线除明显的地和电源线以外, 都为信号线, 应满足信号线布局要求: 走50欧姆阻抗微带线(建议走共面波导线, 线宽20mil, 线与地边距之间的间隔为20mil。)

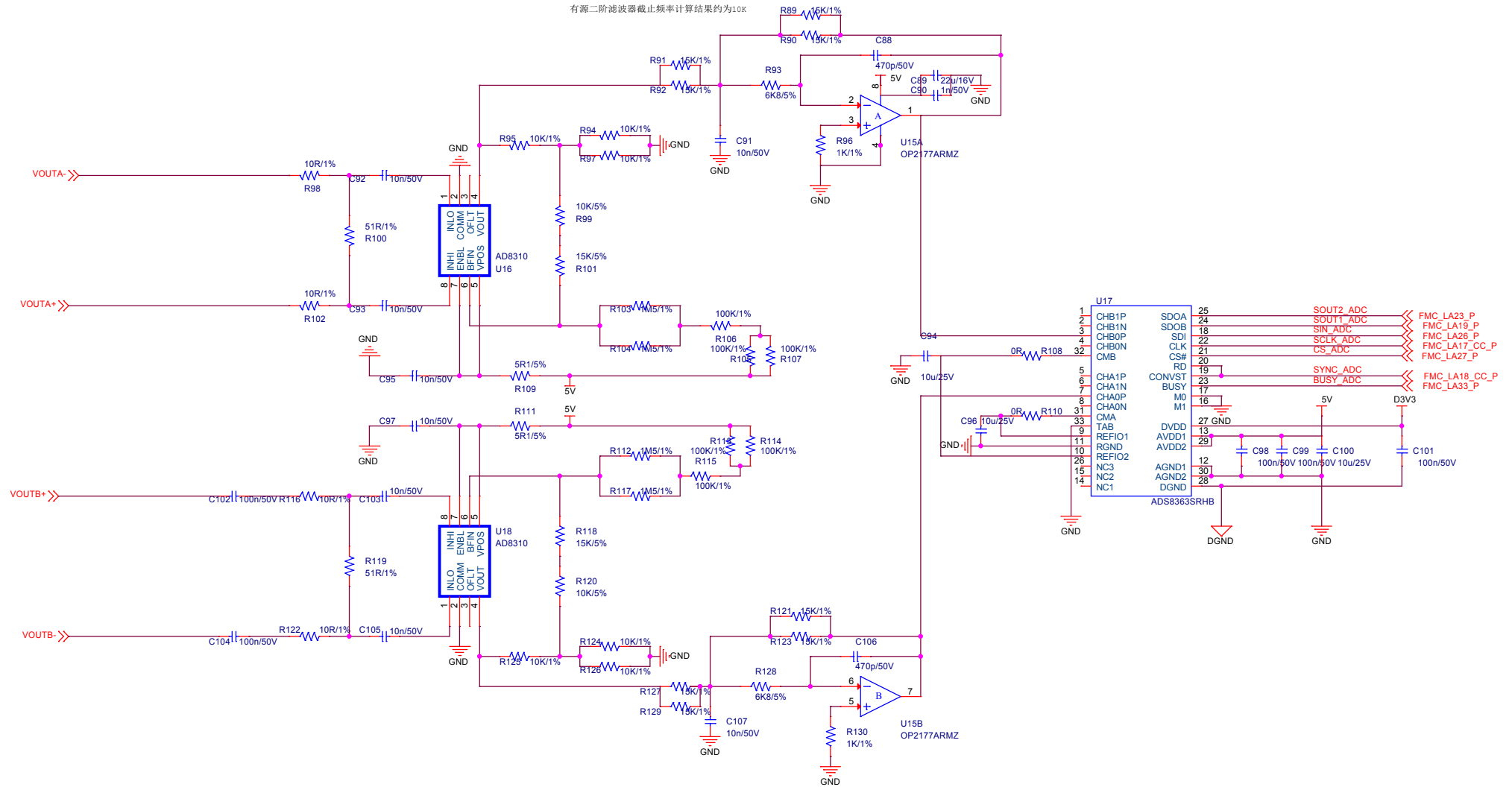
		Date		Rectify content		Signer	
		Sheet Name				Rev V1.0	
		Check / Date		Confirm / Date			
		Name2 / Date		Name3/Date			
Design NO.							
Size A3	Designer / Date						
Date: Friday, May 04, 2018				Sheet 4 of 6			




C81、C82、C91、C92、R75的作用：
考虑ADL5205调试、实现其功能和性能时间可能较长
且整机调试时需根据接入负载的实际情况，对比测试和调整DGA性能参数；
故在此加入辅助调试电路，DGA电路性能达到预定设计后，上述阻、容器件无需焊接。

					
		Date		Rectify content	
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				Rev V1.0	
Size A3	Designer / Date	Check / Date		Confirm / Date	
Date: Friday, May 04, 2018		Sheet 5 of 6		Name3/Date	

有源二阶滤波器截止频率计算结果约为10K



				
Design NO.		Date		Signer
Size A3		Sheet Name		Rev V1.0
Designer / Date		Check / Date		Confirm / Date
Date: Friday, May 04, 2018		Sheet 6 of 6		Name3/Date