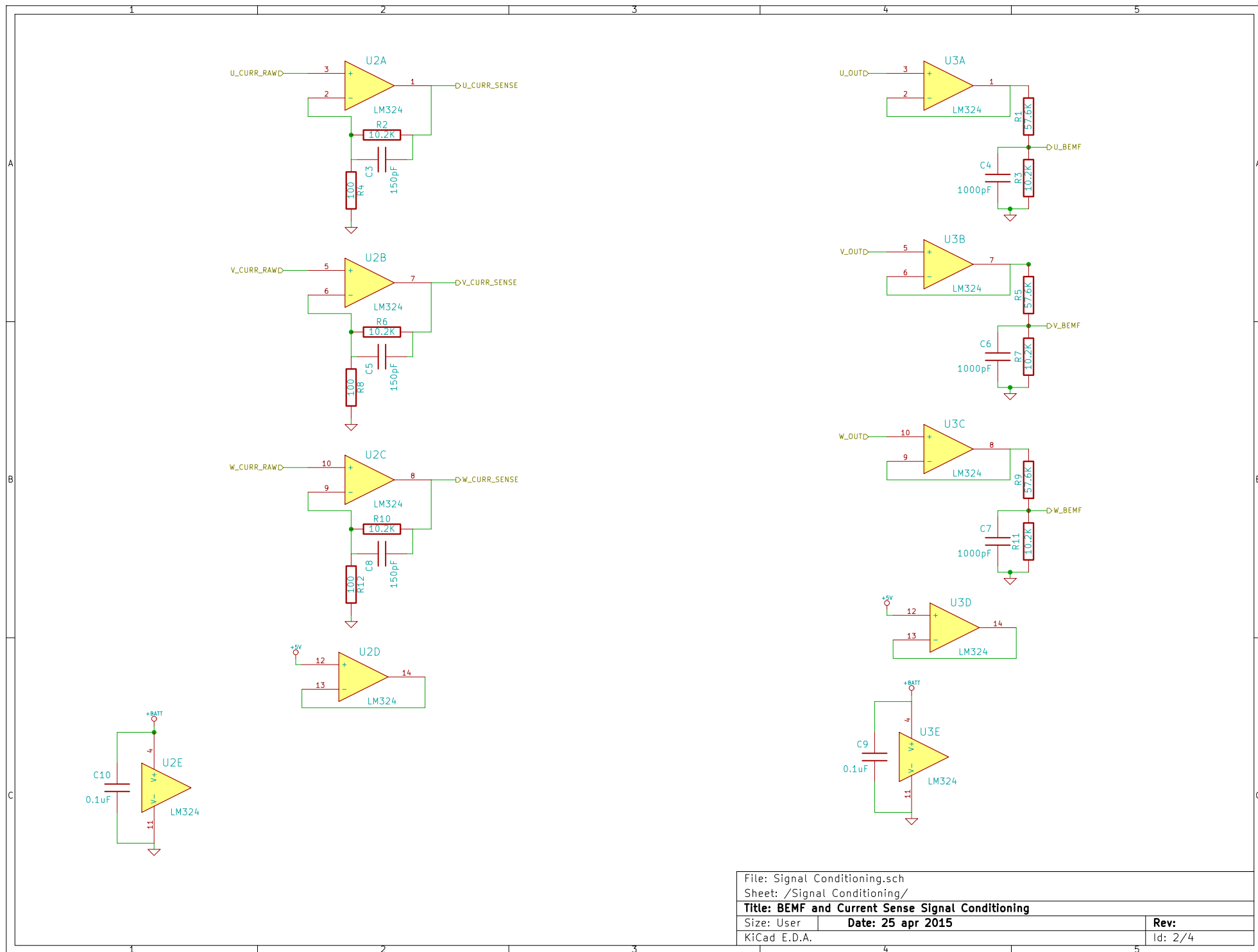


U\_OUT J3

V\_OUT J4

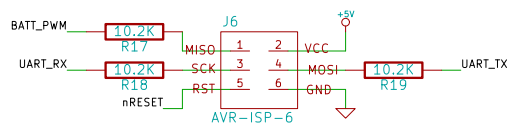
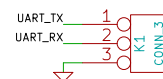
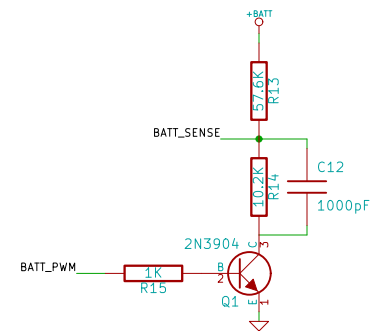
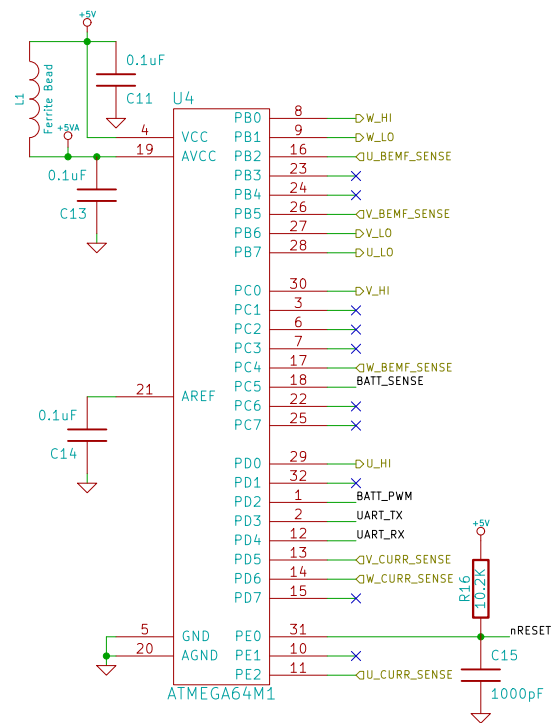
W\_OUT J5

File: bldc.sch		
Sheet: /		
Title: BLDC Controller		
Size: User	Date: 25 apr 2015	Rev: 1
KiCad E.D.A.		Id: 1/4

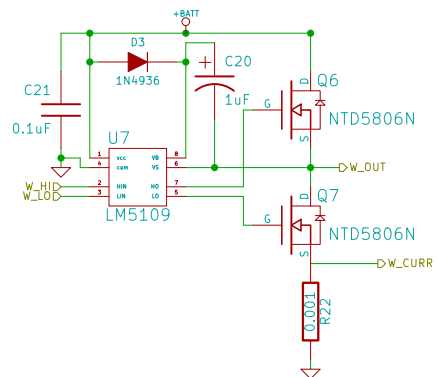
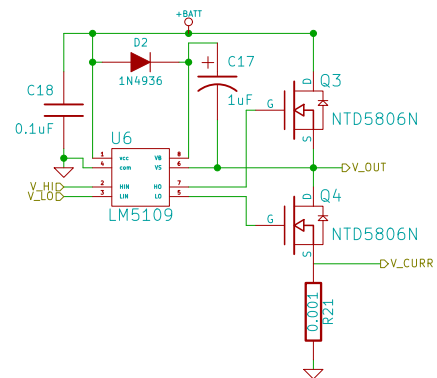
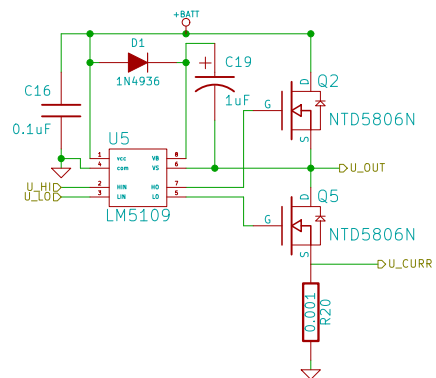


File: Signal Conditioning.sch  
 Sheet: /Signal Conditioning/  
**Title: BEMF and Current Sense Signal Conditioning**  
 Size: User      Date: 25 apr 2015  
 KiCad E.D.A.

Rev:  
 Id: 2/4



File: Microcontroller.sch		
Sheet: /Microcontroller/		
<b>Title: Microcontroller and Support</b>		
Size: User	Date: 25 apr 2015	
KiCad E.D.A.	Rev:	Id: 3/4



File: 3 Phase Inverter.sch		
Sheet: /3 Phase Inverter/		
<b>Title: 3 Phase Inverter</b>		
Size: User	Date: 25 apr 2015	
KiCad E.D.A.	Rev:	
	Id: 4/4	