

# Logic Synthesis

## 1 Variables

Given the number of input signals  $n$ , the depth  $d$ , and the specification (truth table) of the logical circuit, I defined the following variables:

- $\mathbf{c}_{i,j} :=$  “Code of the node  $(i,j)$ ”, where
  - $i \in \{0, 1, \dots, d\}$
  - $j \in \{0, 1, \dots, 2^i - 1\}$
- $\mathbf{b}_{i,j}^{(t)} :=$  “Boolean value of the node  $(i,j)$  for the row  $t$  of the truth table”, where
  - $i \in \{0, 1, \dots, d\}$
  - $j \in \{0, 1, \dots, 2^i - 2\}$
  - $t \in \{0, 1, \dots, 2^n - 1\}$

For example, for a NOR-circuit that implements the functionality of an AND gate (see figure 1), with  $n = d = 2$ , one possible solution for the variables  $c_{i,j}$  and  $b_{i,j}^{(0)}$ , with  $i \in \{0, 1, \dots, 6\}$ , is shown in figures 2b and 2c, respectively.

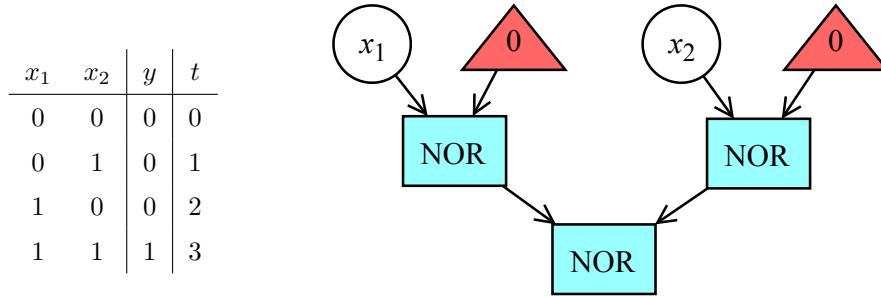


Figure 1: Truth table of  $y = \text{AND}(x_1, x_2)$  and NOR-circuit implementing it.

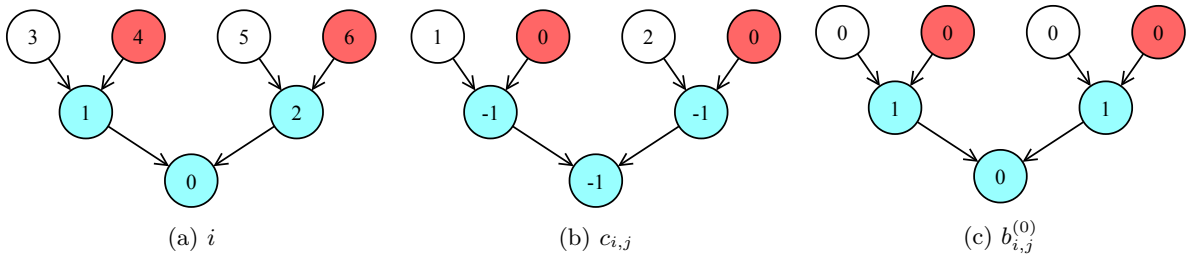


Figure 2: Visual representation of  $(i, j)$  and both variables.

## 2 Constraints

In order to simplify the definition of the constraints, I define three functions. Given the variable  $v_{i,j}$ , with  $v_{i,j} = c_{i,j}$  or  $b_{i,j}^{(t)}$ ,

- **left**( $v_{i,j}$ ) := “Variable corresponding to the one on the left of  $v_{i,j}$ ”
- **right**( $v_{i,j}$ ) := “Variable corresponding to the one on the right of  $v_{i,j}$ ”
- **bit-up**( $\mathbf{k}, \mathbf{t}$ ) := “Boolean value of  $x_k$  in the  $t$  row of the truth table”

So, the constraints are the following:

1.  $c_{d,j} \geq 0$
2.  $c_{i,j} \geq 0 \Rightarrow (\mathbf{left}(c_{i,j}) = 0 \wedge \mathbf{right}(c_{i,j}) = 0)$
3.  $c_{i,j} = -1 \Rightarrow (\mathbf{left}(c_{i,j}) \geq \mathbf{right}(c_{i,j}))$
4.  $(c_{i,j} = -1 \wedge (\mathbf{left}(c_{i,j}) > 0 \vee \mathbf{right}(c_{i,j}) > 0)) \Rightarrow (\mathbf{left}(c_{i,j}) \geq \mathbf{right}(c_{i,j}))$
5.  $c_{i,j} = -1 \Rightarrow (b_{i,j}^{(t)} = \neg(\mathbf{left}(b_{i,j}^{(t)}) \vee \mathbf{right}(b_{i,j}^{(t)})))$
6.  $c_{i,j} = 0 \Rightarrow \neg b_{i,j}^{(t)}$
7.  $c_{i,j} = k \Rightarrow b_{i,j}^{(t)}$
8.  $c_{i,j} = k \Rightarrow \neg b_{i,j}^{(t)}$