

```

      ⋮
      ldi r18, 16
Loop1: ld r17, X
      rcall Sbox
      st X+, r17
      dec r18
      brne Loop1
      clr r18
      ret
Sbox: ldi ZL, lo8(Sbox_Table)
      ldi ZH, hi8(Sbox_Table)
      add ZL, r17
      clr r17
      adc ZH, r17
      lpm r17, Z
      ret
      ⋮

```

(b) Assembly code segment.

⋮
ldi r18, 16 <b>Loop1:</b> ld r17, X rcall Sbox
st X+, r17 dec r18 brne Loop1
clr r18 ret
<b>Sbox:</b> ldi ZL, lo8(Sbox_Table) ldi ZH, hi8(Sbox_Table) add ZL, r17 clr r17 adc ZH, r17 lpm r17, Z ret
⋮

(b) The basic blocks of code segment.