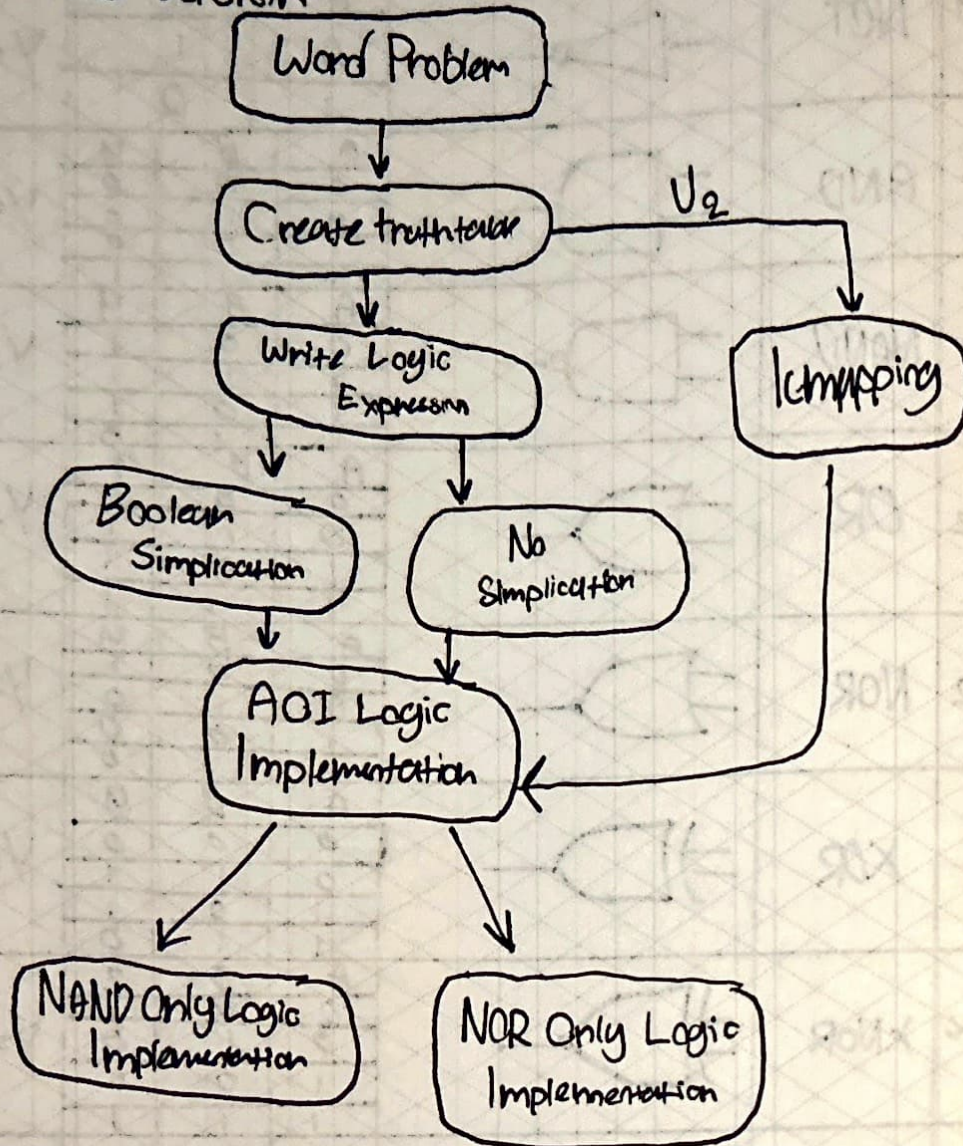


# Circuit Design Process

1. Word Problem
2. Identify Circuit Types Required
3. Truth Table
4. Hand Calculations
5. Simulation
6. Breadboard

## Combinational Logic Design Process Version 1



Signature: *Umar Juma*

Date: *4/26/24*

Team Members:

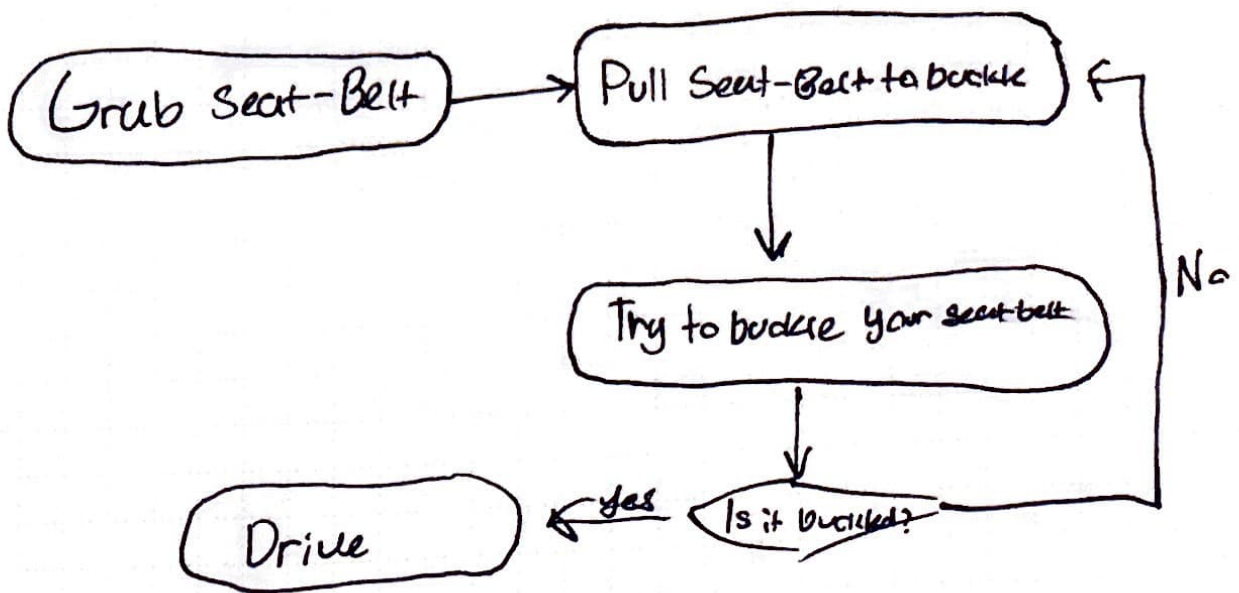
Witness:

Date:

Continued From Page #

Continued On Page #

19.



*lv*

Signature: *Munster Jank*

Date: *9/26*

Team Members: \_\_\_\_\_

Witness:

Date:

Continued From Page # *18*

Continued On Page # \_\_\_\_\_