Title: 1.1.6 Digital Component Identification	h 13
Transistor to Gate	
Transistors	
An electronic device that is used to control the flow	of electricity in
Expired with extense 3 electrodes 80.	therecement Voltage controls
Can act as an complifier &	
Can act as a switch	
Completely off or completely on Vin Collector	
Gate Builter	Andrew Property African
Transistors and resistors can be enranged to one	oute desired outputs
and steeling inputs us a logic gove.	
Transistans have only 2 stortes (on or off) so the	binary number system
These inport to outpute resultanships can be shown in truth tou	outs toautputs of these goutes.
741 e 00N 711 a 00N	
74LSOBN 74LSOBN 74LSOAN condigue argute norgete	
Gute to Integrated Circuits (IC)	
Integrated Circuit	
An electronic circuit having many components diodes, resistors, and capacitors in a single pack	s, Such as transistors,
Transistors -> Gate -> Integrate	d Circuit
Integrated Circuits (IC) and Socket	Service Contract
1. DIP: Ducy Inline Package 2. 80IC: Small Outline Integrated Circuit	IN X INY OUT Z
3. PLCC: Plastic Leaded Chip Corrier	0 0 ?
Gottes and Truth Take	0 1 2
Truth factive 16 a list of our possible input vowbules to a algitum c	
listed in ascending binery order, and the autout for each import compin	cutron.
Inputs X \$ Y could be a button or switch Output Z could be a buzzar on LEO	
For 2 inputs there can only by the possible accommendation inputs	
Signature Jude Date: 1/8/24 Team Memb	pers:
Witness: Date:	Minosis
Continued From Page # Continued C	On Page #
	14

14			11 11 11 11 11 11	
	Title: 1.1.6 Digital a	Comparant Iden	tification	
inter	Mali + II	411		- Count:
The	Structure of a trut		1- on being celice +	o court in
	ora trut	n table depen	nas oil	
Tine	lry which is the base-2	number system	m. (1.2):	U. hineune
	discending now 4.	Louth trade was	usent a count of (4-3) 11	n the David
Part of the latest of the late	'VU SURTOM	A STATE OF THE REAL PROPERTY.		
Truth	Textles and Binar			XXXX
All	Tobles and Binar	y	Con use	resolved bircomy order
	ossible in our values	to a digital o	crownt one lister	
	The thirth Table.	A SA ASSA		45.21.20
integ	rated Circuits			
All	logic gates are availbe	us on Interned	Circuit (IC)	
Ic	s are categorized in	3 different well		
	Underlying Technolo	Dell'		
	· Transistor - Transis · Complementary Me		(CMOS)	
	Scale of Integration	who oxide semic	COLOUS COLO	
	· Small Scale Inte	egration (SSI)		
	Irredium Soule Int	earction (MSI)		
	Large Scale integ	notion (USI)		
ASKY.	very Large Scale	Integration (VISI		
X	Package Style			
$\times$	· Through - Hole tec · Duay Inline Paskage	chnology (THT)		
X	· Surface-Mont tel	(SMT)		
$\times () \times$	Small Outline 16	(Soto)		
	· Plastic Leaded (V	nip Corner (PCCS)		XXX
X 741	· Ovad Flor Pack (RF		AND MEDICAL STREET	
11	us CMos	<b>1</b> 8.25 <b>7</b> 5.4		* OKEN
$\times$	ransister-transister Ir	rtegration (TTL)		
X	Constructed from Bife Fewer than ones but uses	olar Junction Tra	insisters (BJT)	W W W
*()*		O to I feller (	CMV61	
×()C	constructed from Mutal Oxide	Similar Company	Field-Effect	WAS ASK
X X	constructed from Mujor C	ALOC .	to electrodicatio - chackenge	
KINK	Uses less pour, side		A SAME AND A COMPANY	
Signatu	re: Chouttun Jell	Date 0/18/29	Team Members:	and Commercial
Witness		Date:	250	
		Comme	Continued On Page #	
Continu	ied From Page # 2		1C	

10 Density of Integrousion

Density of Integrousion	Cours per 10
3s‡	10
Msi	10-100
L8#	100-10000
VLSI	10000-100000
ULSI	100000 - 1000 000
CSI	>1000 000

Through-Hole Technology
THE Components are easier to hand assemble leepower they ear much larger.

Longer, proto-boerds, most high-end electronis companents Surface Mount Technology 8Mt Components are mounted on the october of the PCB.

Smuller, Expensive, contact party proto-boards

TIL Logic Sub-termities off 691

Infix Example Comments Stendard TIL Staunet, High power 7404 nohe Low Power Less pour trea Stordard 74L04 Schotky 5 74804 Puster, high power Low Pauer Schottley 74 LS04 faster, lour pour Advanced Schottley AS 74AS04 Very Fast, high cour Advanced Law-Power Schoffky ALS 74ALS04 fortest, good power

Manufacteur Declasheets

A menutarities or Obeforduet for a logic grove contains the following information:

General Description

Connection (pin-out diagrams) Drayrown

Function Table

Operanting Conditions

Electerous annihilations

Switching disportantions

Physical Dimensions

Signature:	Javel	Date:	Team Members:
Witness:		Date:	

Continued From Page #

Continued On Page #