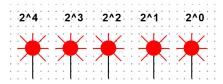
W10: DE 1.2.4 C Binary Counting Connection to Sequential Logic

Make copies of the probes and show which indicators would be "ON" for each of the following numbers. What would be the pattern for the probes for each of the cases below?



	2 ⁵	24	2 ³	22	2 ¹	20
0						Ф.
1						**
2					*	
3					*	**
4				*		Ф.
5				*		**
6				*	*	Ф.
7				**	**	**

8		0 0 0 0 1	***			
9			**			*
10		,	*		*	
11		<u>.</u>	**		**	*
12	Ф.	,	**	**		
13			*	*		*
14	О		**	**	**	
15			*	*	*	*
16		**				
17		**				**
18		**	 	 	**	
19		**	. O. O.	 	*	*
20		**		***		(O)

Build the following FIGURE 2:Binary Counter Example Circuit on Multisim.

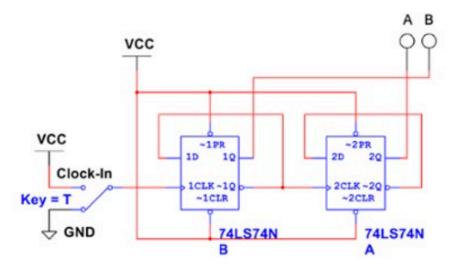
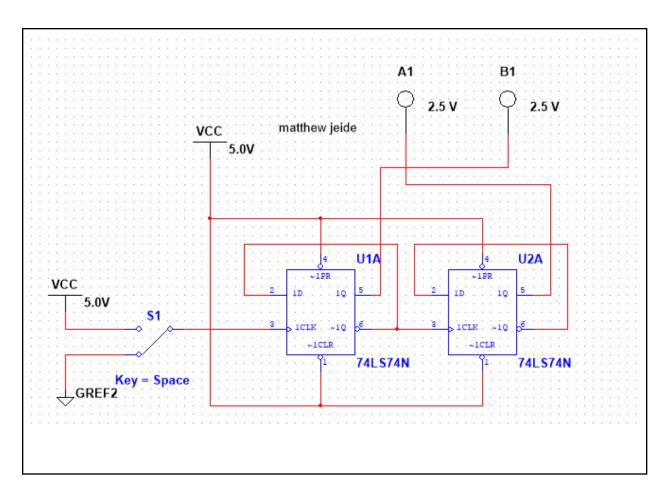


Figure 2. Binary Counter Example Circuit



How many numbers does this counter count up to? How many 74LS74N chips will I need for it to count until 20?

This counter will count up to 7, you would need 4 74LS74N chips to count up to 20.

How would you design a circuit to count up to the number 7? Design it on MultiSim.

