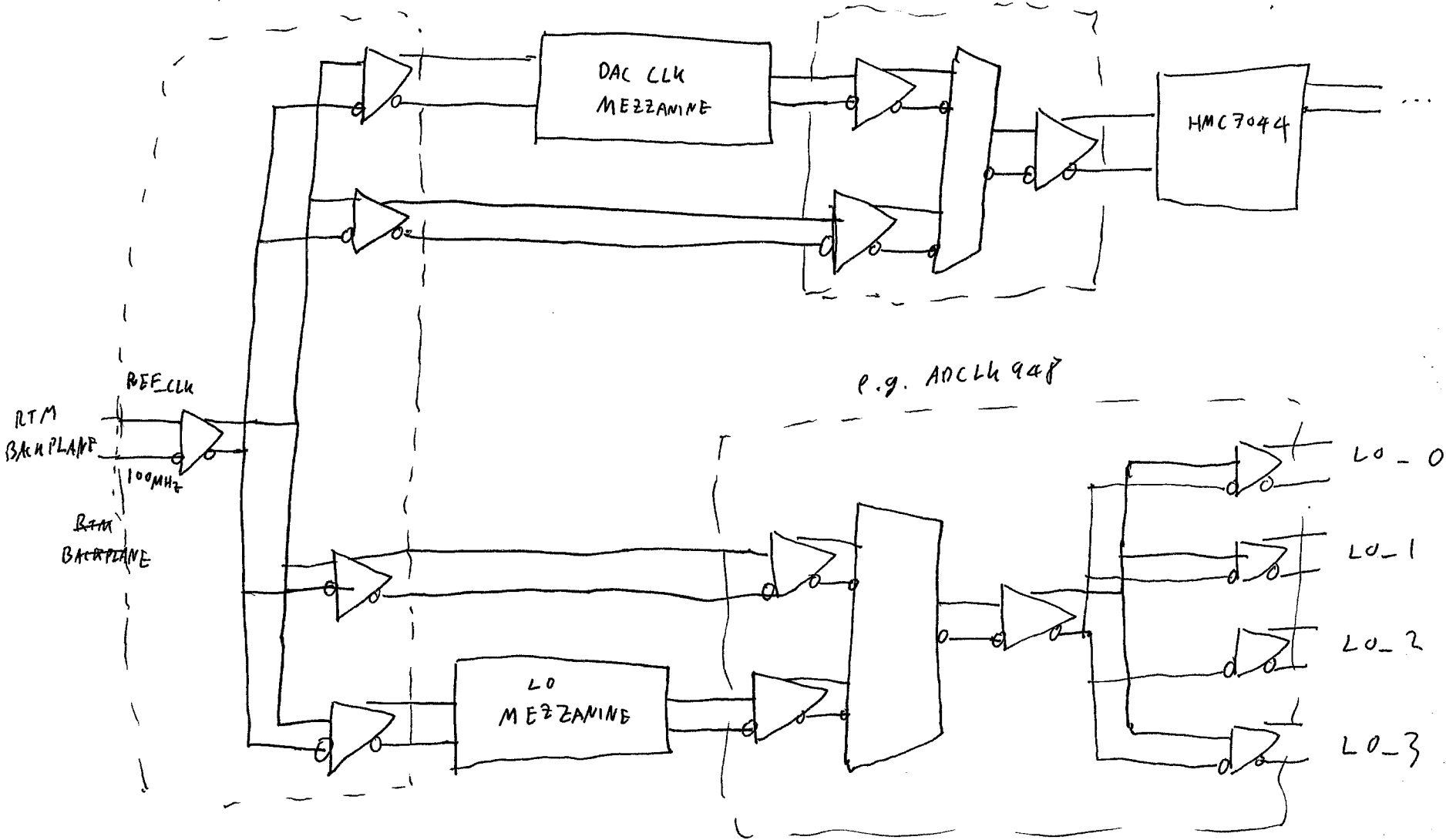


e.g.
ADCLK948

e.g.
ADCLK948



- MUX/FAN-OUT ARE E.G. ADCLK948

- ALL SIGNALS ARE 3V3 PECL

- ASSUME 100MHz REF_CLK MEETS PHASE NOISE SPEC UP TO \approx 100kHz,