

Evaluation of Heuristic Task-to-Thread Mapping Using Static and Dynamic Approaches

Presenter:

Mohammad Samadi

Co-authors:

Tiago Carvalho, Luis Miguel Pinho, Sara Royuela

Polytechnic Institute of Porto & INESC TEC, Porto, Portugal

Barcelona Supercomputing Center, Barcelona, Spain

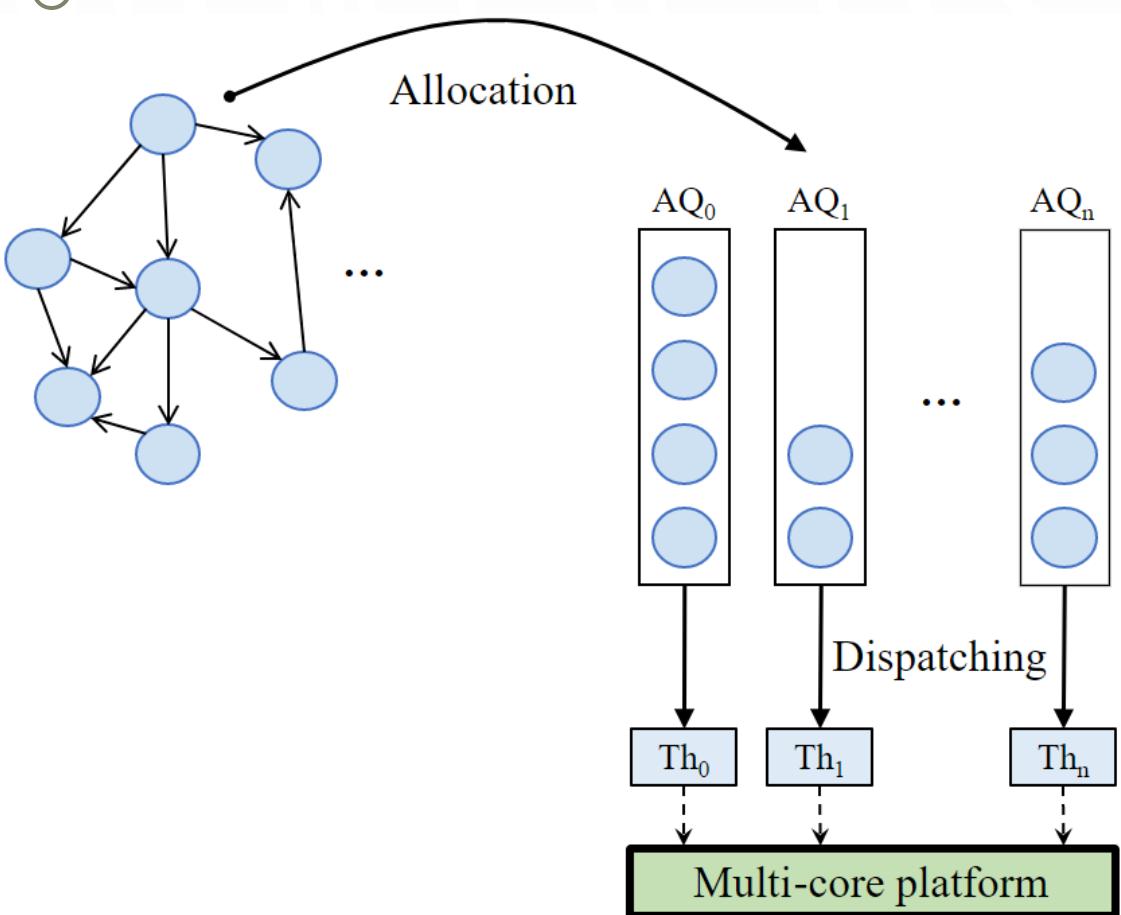
Universitat Politècnica de Catalunya, Barcelona, Spain

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Introduction

- Increase the need for high-performance computing (HPC) capabilities in complex cyber-physical systems
- Use OpenMP in real-time HPC applications running on shared-memory platforms
- Apply dynamic process in OpenMP runtime implementations
- Lack of time predictability in dynamic mapping algorithm
- Need time-predictable task-to-thread mapping in OpenMP

Heuristic Task-to-Thread Mapping



Allocation phase

- Pick each task-part from OpenMP DAG by threads
- Select an allocation queue
- Allocate the task-part to the queue

Dispatching phase

- Select a task-part from each allocation queue
- Dispatch the task-part to the thread, and execute it
- Rearrange the queue
- Update the system info

Heuristic Task-to-Thread Mapping

The allocation heuristics to select threads:

- MNTP: Minimum number of task-parts
- NT: Next thread
- MRIT: Most recent idle time
- MTET: Minimum total execution time
- MTRT: Maximum total response time
- TMCD: Total multi-criteria decision based on MNTP, MRIT and MTET

The dispatching heuristics to select task-parts:

- MET: Minimum execution time
- MRT: Maximum response time
- MCD: Multi-criteria decision based on MET and MRT

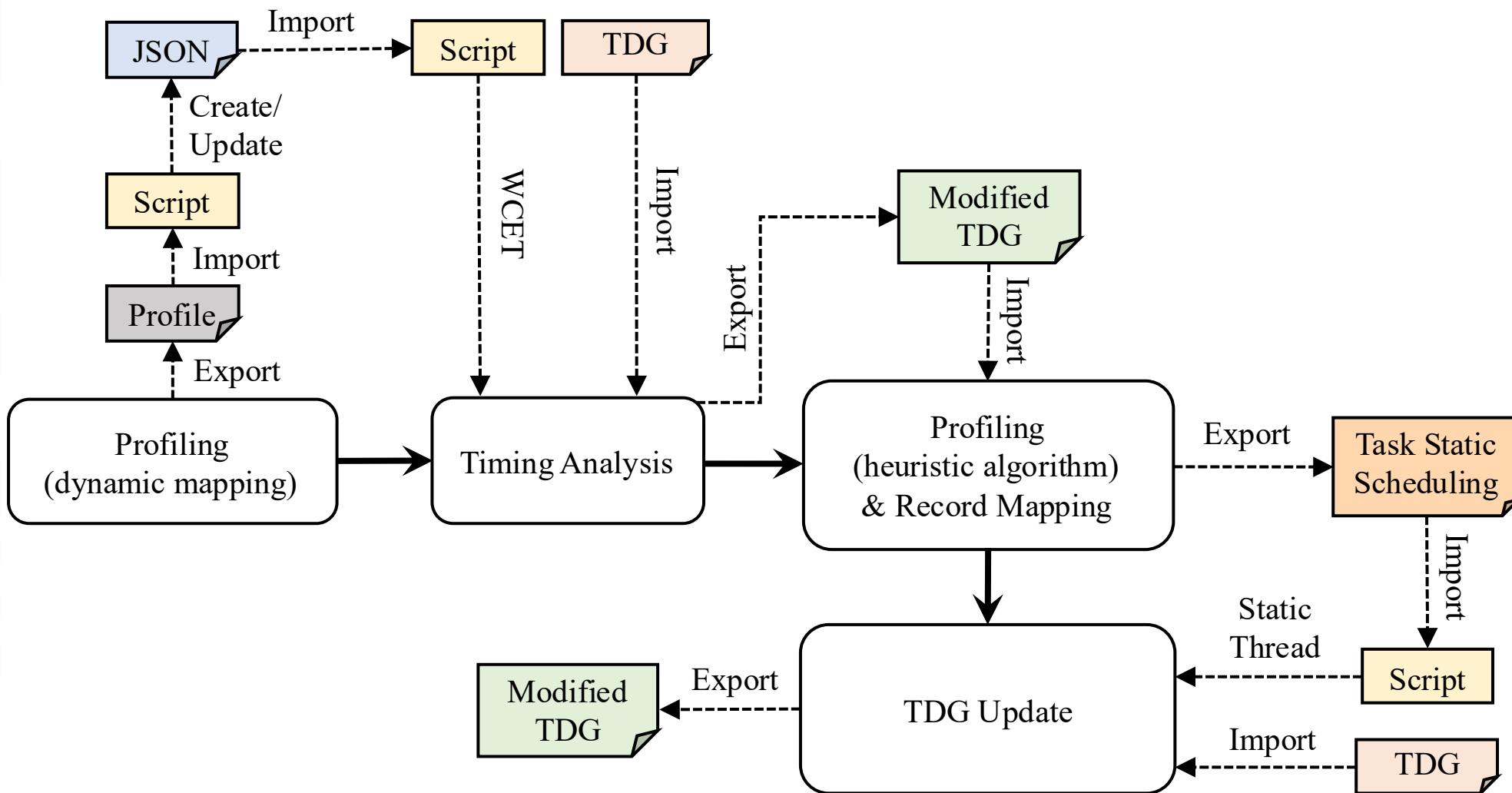
Problems With Heuristic Mapping

- Implementation of the MTET heuristic in LLVM is not efficient.
- MRT heuristic needs more information of the system.
- Current evaluations did not consider embarrassingly parallel applications.
- Experiments were carried out based on the WCET, without considering the variability of results.

Contributions

- Compare execution of the heuristics considering decisions made during execution, against static enforcement of the mapping.
- Determine static mapping by recording the execution in real platform instead by simulation.
- Reduce overhead by changing the implementation of one of the heuristics.
- Implement a new heuristic in the dispatching phase.

Methodology for Static Mapping



LLVM Extensions

- Improve the MTET heuristic by materializing the time expended in execution for each thread to reduce mapping overhead.
- Propose the highest execution time (HET) heuristic algorithm to reduce the workload of each queue and increase its chance of getting new tasks.

Applications

- Axpy: A Level 1 operation in the Basic Linear Algebra Subprograms (BLAS) package that combines scalar multiplication and vector addition.
- Heat: A simulator of heat diffusion based on the Gauss-Seidel method implementing a stencil computation.
- SparseLU: A matrix decomposition implementing a diamond-like form of irregular parallelism.

Applications

```
1 void saxpy(double *x, double *y) {
2   for (int i=0; i<Iters; i++) {
3     #pragma omp parallel
4     #pragma omp single
5     for (int i = 0; i < N; i+=BS1) {
6       #pragma omp task
7       for(int j = 0; j < BS1; j++)
8         y[i+j] += a * x[i+j];
9     }
10  }
11 }
```

Axpy

```
1 void heat_propagation (void) {
2 ...
3   for (int i=0; i<Iters; i++) {
4     #pragma omp parallel
5     #pragma omp single
6     for (int ii=0; ii<NB; ii++) {
7       for (int jj=0; jj<NB; jj++) {
8         int inf_i = 1 + ii * bx;
9         int sup_i = ((inf_i + bx) < sizex - 1) ? inf_i + bx : sizex - 1;
10        int inf_j = 1 + jj * by;
11        int sup_j = ((inf_j + by) < sizey - 1) ? inf_j + by : sizey - 1;
12        #pragma omp task depend(in: u[inf_i-bx][inf_j], u[sup_i][inf_j], \
13                                u[inf_i][inf_j-by], u[inf_i][sup_j]) \
14                                depend(out: u[inf_i][inf_j])
15        for (int i = inf_i; i < sup_i; ++i)
16          for (int j = inf_j; j < sup_j; ++j)
17            u[i][j] = 0.25 * (u[i][j-1] + u[i][j+1] + u[i-1][j] + u[i+1][j]);
18      }
19    // Implicit barrier
20  }
21 ...
22 }
```

Heat

```
1 void sparselu(float **M) {
2   for (int i=0; i<Iters; i++) {
3     #pragma omp parallel
4     #pragma omp single
5     for (int kk=0; kk<S; kk++) {
6       #pragma omp task firstprivate(kk) shared(M) depend(inout: M[kk*S+kk])
7       lu0(M[kk*S+kk]);
8
9       for (int jj=kk+1; jj<S; jj++) {
10         #pragma omp task firstprivate(kk, jj) shared(M) \
11           depend(in: M[kk*S+kk]) depend(inout: M[kk*S+jj])
12         if (M[kk*S+jj] != NULL) fwd(M[kk*S+kk], M[kk*S+jj]);
13       }
14
15       for (int ii=kk+1; ii<S; ii++) {
16         #pragma omp task firstprivate(kk, ii) shared(M) \
17           depend(in: M[kk*S+kk]) depend(inout: M[ii*S+kk])
18         if (M[ii*S+kk] != NULL) bdiv (M[kk*S+kk], M[ii*S+kk]);
19       }
20       for (int ii=kk+1; ii<S; ii++) {
21         for (int jj=kk+1; jj<S; jj++) {
22           #pragma omp task firstprivate(kk, jj, ii) shared(M) \
23             depend(in: M[ii*S+kk], M[kk*S+jj]) depend(inout: M[ii*S+jj])
24           if ((M[ii*S+kk] != NULL) && (M[kk*S+jj] != NULL))
25             if (M[ii*S+jj]==NULL) M[ii*S+jj] = allocate_clean_block();
26             bmod(M[ii*S+kk], M[kk*S+jj], M[ii*S+jj]);
27         }
28       }
29     }
30 }
```

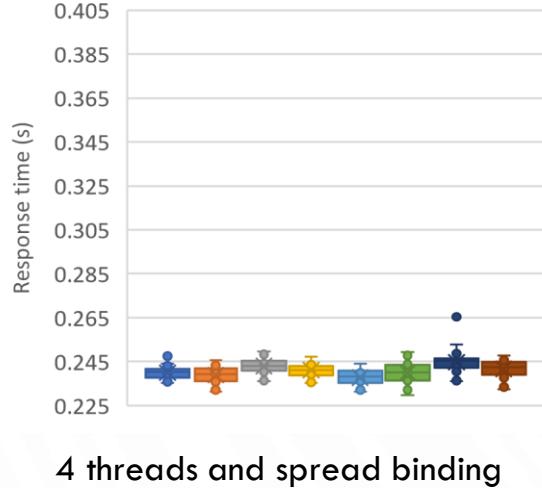
SparseLU

Experimental Setup

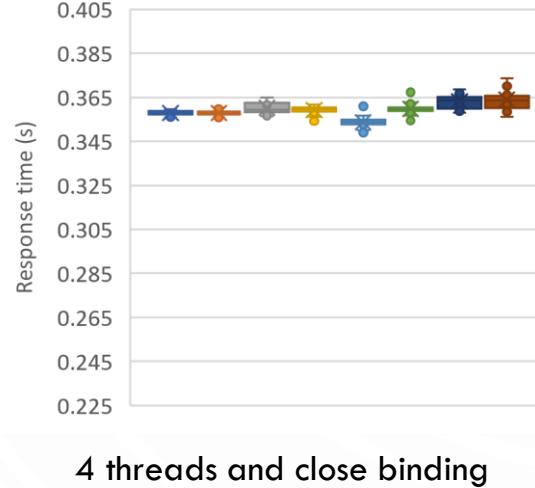
- Hardware: NVIDIA Jetson AGX Xavier with 8-core CPU
- Operating system: Linux OS with the highest real-time priority
- Compiler and runtime system: An extended version of LLVM/Clang 17.0.0, supporting OpenMP 5.0 and Taskgraph
- Configurations: Different number of threads and binding options
- Execution process: Running the applications in 50 iterations and removing the first 10% of results as a warm-up phase

Experiments (Axpy)

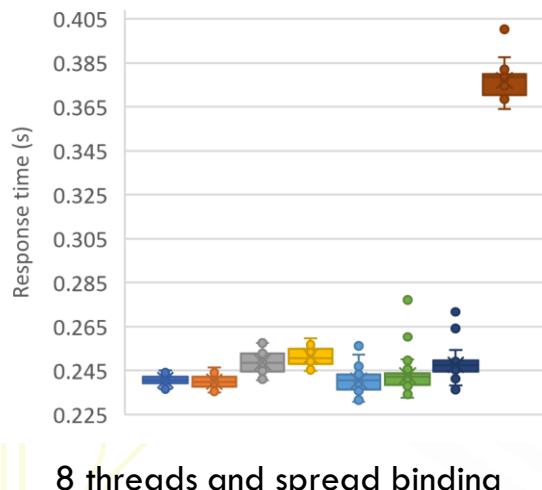
MNTP-MET_DY MNTP-HET_DY MTET-MET_DY MTET-HET_DY
MNTP-MET_ST MNTP-HET_ST MTET-MET_ST MTET-HET_ST



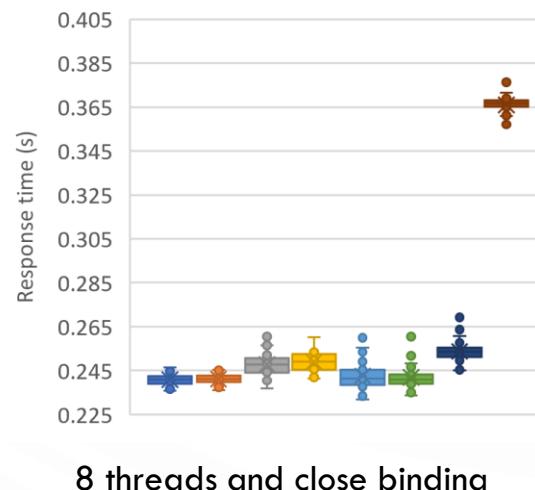
4 threads and spread binding



4 threads and close binding



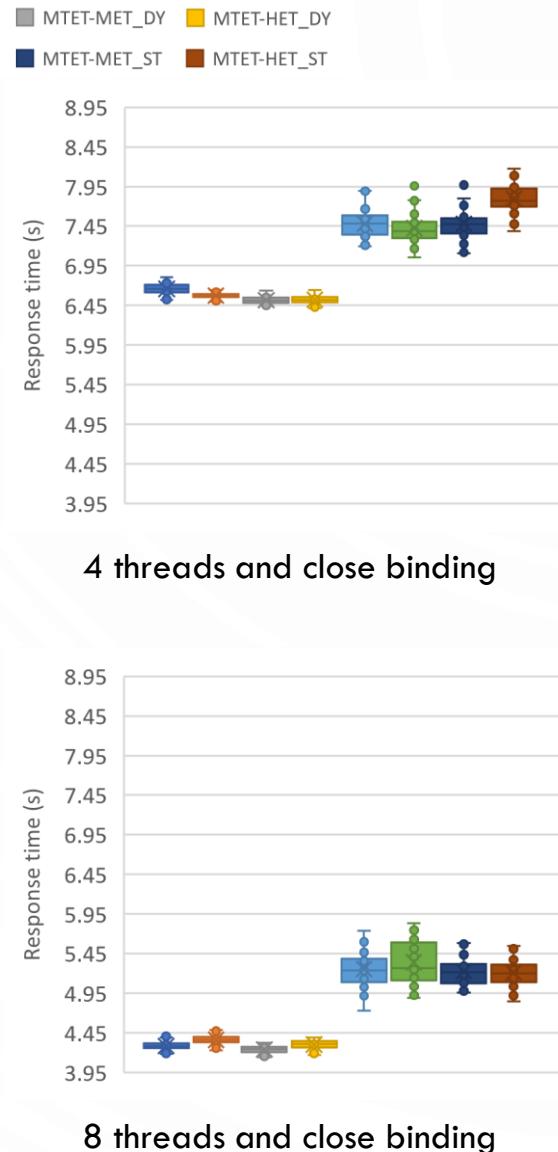
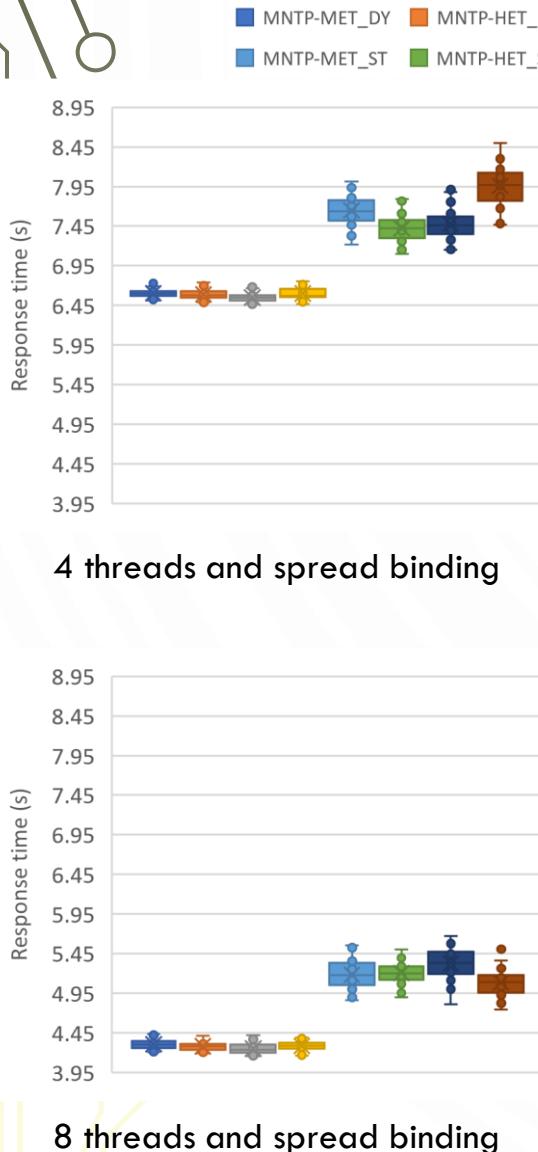
8 threads and spread binding



8 threads and close binding

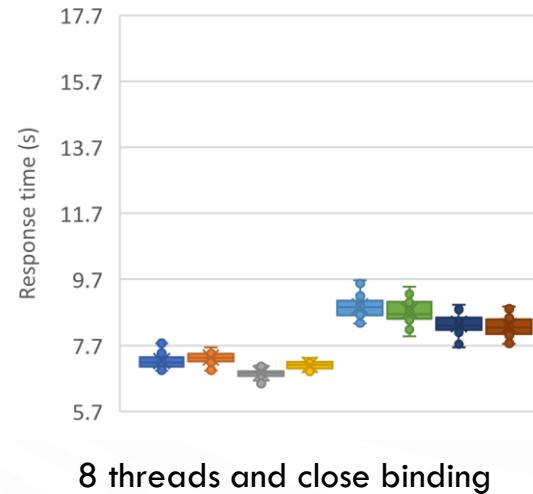
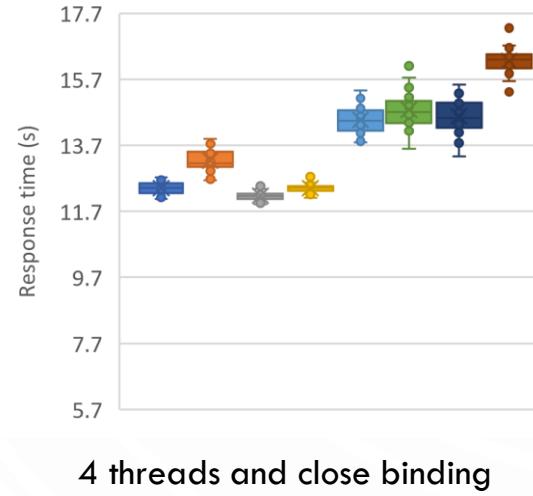
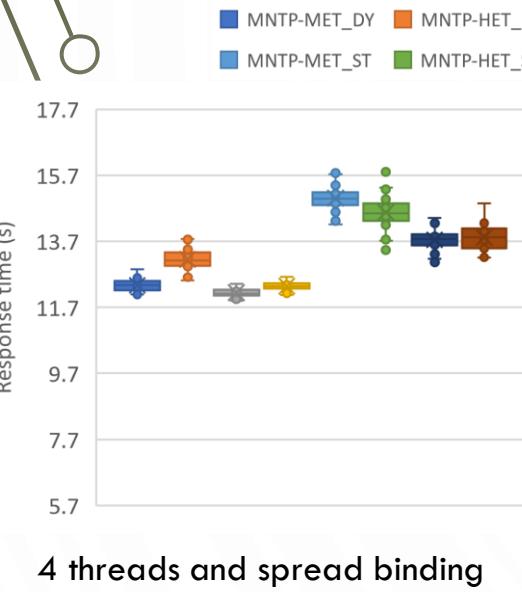
- Variability of MNTP-X in dynamic mapping is smaller than that of the other heuristic pairs.
- Response time with MNTP-X is slightly lower than others.
- Response time with MTET-HET and 8 threads in static mapping is higher than others.
- Response time with 4 threads and spread binding has not been decreased by increasing the number of threads to 8.
- There is not a considerable difference between the results obtained in dynamic and static mapping.

Experiments (Heat)



- Response time and variability of results achieved in dynamic mapping are lower than that in static mapping.
- Both the static and the dynamic versions show the same scalability when changing the number of threads.
- MTET-MET consistently shows slightly better performance and variability than the other pairs.

Experiments (SparseLU)



- Response time and variability given in dynamic mapping are better than those given in static mapping.
- Response time and variability of the results achieved with MTET-X are lower than those achieved with MNTP-X in most of the results.
- In this application with a complex TDG, there is a noticeable difference among the results given with the heuristics.

Discussion

- In the application without data dependencies, the performance of the heuristics in dynamic and static mapping is similar to each other.
- In the applications with data dependencies, their performance in dynamic mapping is better than that in static mapping.
- Variability in the dynamic mapping approach is lower than static mapping, important for real-time systems.

Future Works

The evaluation of the mappings under a wider set of conditions, supporting:

- Various artificial intelligence and data analytics applications
- Different hardware platforms with higher parallel capabilities

THANKS FOR YOUR ATTENTION!

For more information:
mmasa@isep.ipp.pt

