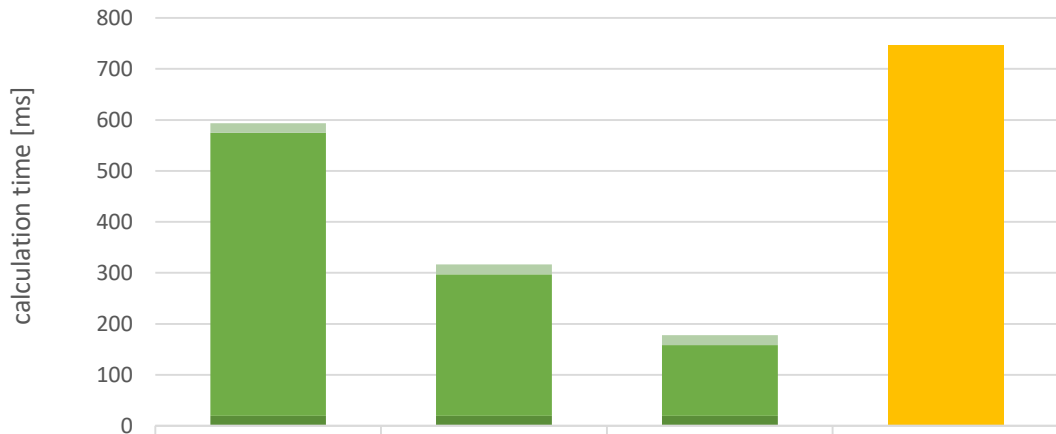


IOS with CC1-4



	CC1-4: each 1thread	CC1-4: each 2thread	CC1-4: each 4thread	IO DDR 4thread(with cache)
input matC	19.38513	19.35969	19.34133	
calculation	554.48349	277.14643	138.70299	745.86402
inverse matB	19.83864	19.79385	19.78137	