

NEURAL PROGRAMMER-INTERPRETERS

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ABSTRACT

We propose the neural programmer-interpreter (NPI): a recurrent and compositional neural network that learns to represent and execute programs. NPI has three learnable components: a task-agnostic recurrent core, a persistent key-value program memory, and domain-specific encoders that enable a single NPI to operate in multiple perceptually diverse environments with distinct affordances. By learning to compose lower-level programs to express higher-level programs, NPI reduces sample complexity and increases generalization ability compared to sequence-to-sequence LSTMs. The program memory allows efficient learning of additional tasks by building on existing programs. NPI can also harness the environment (e.g. a scratch pad with read-write pointers) to cache intermediate results of computation, lessening the long-term memory burden on recurrent hidden units. In this work we train the NPI with fully-supervised execution traces; each program has example sequences of calls to the immediate subprograms conditioned on the input. Rather than training on a huge number of relatively weak labels, NPI learns from a small number of rich examples. We demonstrate the capability of our model to learn several types of compositional programs: addition, sorting, and canonicalizing 3D models. Furthermore, a *single* NPI learns to execute these programs and all 21 associated subprograms.

1 INTRODUCTION

Teaching machines to learn new programs, to rapidly compose new programs from existing programs, and to conditionally execute these programs automatically so as to solve a wide variety of tasks is one of the central challenges of AI. Programs appear in many guises in various AI problems; including motor behaviours, image transformations, reinforcement learning policies, classical algorithms, and symbolic relations.

In this paper, we develop a compositional architecture that learns to represent and interpret programs. We refer to this architecture as the Neural Programmer-Interpreter (NPI). The core module is an LSTM-based sequence model that takes as input a learnable program embedding, program arguments passed on by the calling program, and a feature representation of the environment. The output of the core module is a key indicating what program to call next, arguments for the following program and a flag indicating whether the program should terminate. In addition to the recurrent core, the NPI architecture includes a learnable key-value memory of program embeddings. This program-memory is essential for learning and re-using programs in a continual manner. Figures 1 and 2 illustrate the NPI on two different tasks.

We show in our experiments that the NPI architecture can learn 21 programs, including addition, sorting, and trajectory planning from image pixels. Crucially, this can be achieved using a single core model with the same parameters shared across all tasks. Different environments (for example images, text, and scratch-pads) may require specific perception modules or encoders to produce the features used by the shared core, as well as environment-specific actuators. Both perception modules and actuators can be learned from data when training the NPI architecture.

To train the NPI we use curriculum learning and supervision via example execution traces. Each program has example sequences of calls to the immediate subprograms conditioned on the input.

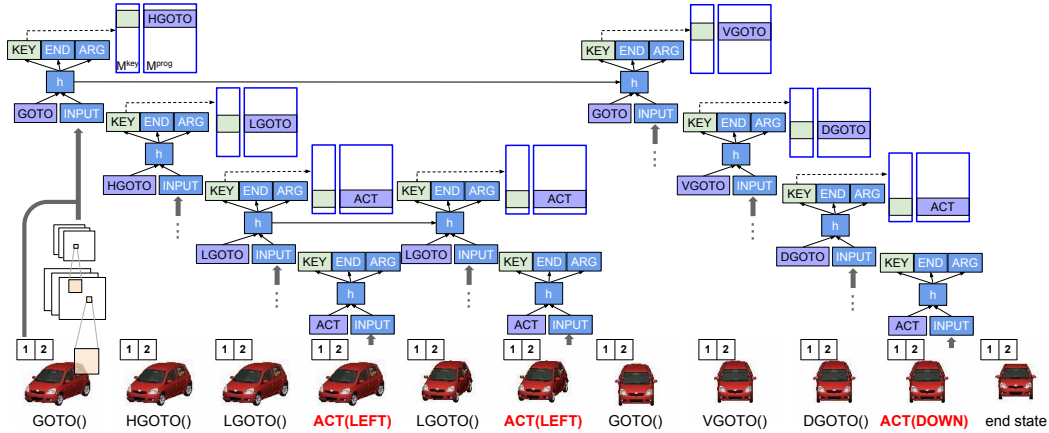


Figure 1: Example execution of canonicalizing 3D car models. The task is to move the camera such that a target angle and elevation are reached. There is a read-only scratch pad containing the target (angle 1, elevation 2 here). The image encoder is a convnet trained from scratch on pixels.

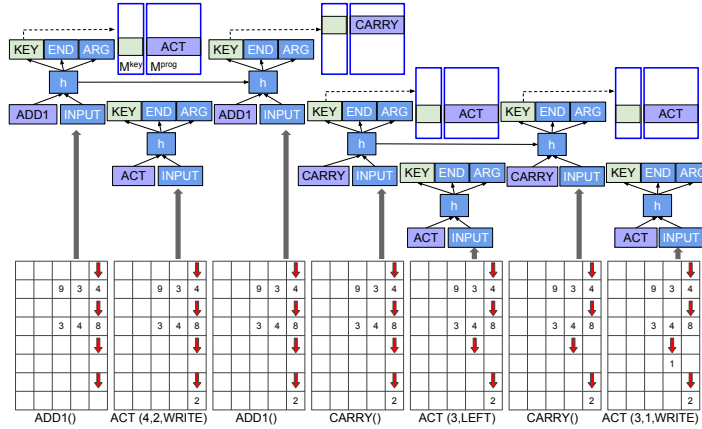


Figure 2: Example execution trace of single-digit addition. The task is to perform a single-digit add on the numbers at pointer locations in the first two rows. The carry (row 3) and output (row 4) should be updated to reflect the addition. At each time step, an observation of the environment (viewed from each pointer on a scratch pad) is encoded into a fixed-length vector.

By using neural networks to represent the subprograms and learning these from data, the approach can generalize on tasks involving rich perceptual inputs and uncertainty.

We may envision two approaches to provide supervision. In one, we provide a very large number of labeled examples, as in object recognition, speech and machine translation. In the other, the approach followed in this paper, the aim is to provide far fewer labeled examples, but where the labels contain richer information allowing the model to learn compositional structure. While unsupervised and reinforcement learning play important roles in perception and motor control, other cognitive abilities are possible thanks to rich supervision and curriculum learning. This is indeed the reason for sending our children to school.

An advantage of our approach to model building and training is that the learned programs exhibit *strong generalization*. Specifically, when trained to sort sequences of up to twenty numbers in length, they can sort much longer sequences at test time. In contrast, the experiments will show that more standard sequence to sequence LSTMs only exhibit *weak generalization*, see Figure 6.

A trained NPI with fixed parameters and a learned library of programs, can act both as an interpreter and as a programmer. As an interpreter, it takes input in the form of a program embedding and input data and subsequently executes the program. As a programmer, it uses samples drawn from a new task to generate a new program embedding that can be added to its library of programs.

2 RELATED WORK

Several ideas related to our approach have a long history. For example, the idea of using dynamically programmable networks in which the activations of one network become the weights (the

program) of a second network was mentioned in the Sigma-Pi units section of the influential PDP paper (Rumelhart et al., 1986). This idea appeared in (Sutskever & Hinton, 2009) in the context of learning higher order symbolic relations and in (Donnarumma et al., 2015) as the key ingredient of an architecture for prefrontal cognitive control. Schmidhuber (1992) proposed a related meta-learning idea, whereby one learns the parameters of a slowly changing network, which in turn generates context dependent weight changes for a second rapidly changing network. These approaches have only been demonstrated in very limited settings.

In cognitive science, several theories of brain areas controlling other brain parts so as to carry out multiple tasks have been proposed; see for example Schneider & Chein (2003); Anderson (2010) and Donnarumma et al. (2012).

Recently, Graves et al. (2014) advanced a neural Turing machine that is capable of learning and executing simple programs such as repeat copying, simple priority sorting and associative recall. Instead of using input and output pairs, our model is trained on program execution traces at varying levels of abstraction. In exchange for this richer supervision, we get the benefit of learning compositionality of programs, and also data efficient training of complex programs.

Related problems have been studied in the literature on hierarchical reinforcement learning (*e.g.*, Dietterich (2000); Andre & Russell (2001); Sutton et al. (1999) and Schaul et al. (2015)), imitation and apprenticeship learning (*e.g.*, Kolter et al. (2008) and Rothkopf & Ballard (2013)) and elicitation of options through human interaction (Subramanian et al., 2011). These ideas have held great promise, but have not enjoyed significant impact. We believe the recurrent compositional neural representations proposed in this paper could help these approaches in the future, and in particular in overcoming feature engineering.

This work is also closely related to program induction. Most previous work on program induction, *i.e.* inducing a program given example input and output pairs, has used genetic programming (Banzhaf et al., 1998) to evolve useful programs from candidate populations.

There have been several recent works extending recurrent networks to solve problems not traditionally cast as simple sequence prediction. Zaremba & Sutskever (2014) trained LSTM models to read in the text of simple programs character-by-character and correctly predict the program output. Though not practical as a real interpreter, it is somewhat astonishing that it works. Mou et al. (2014) process program symbols to learn max-margin program embeddings with the help of parse trees. Vinyals et al. (2015) developed Pointer Networks that generalize the notion of encoder attention in order to provide the decoder a variable-sized output space depending on the input sequence length. This model was shown to be effective for combinatorial optimization problems such as the traveling salesman and Delaunay triangulation. Our work is inspired by these but tackles a different problem.

3 MODEL

The NPI core is a long short-term memory (LSTM) network (Hochreiter & Schmidhuber, 1997) that acts as a router between programs conditioned on the current state observation and previous hidden unit states. At each time step, the core module can select another program to invoke using content-based addressing. It emits the probability of ending the current program with a single binary unit. If this probability is over threshold (we used 0.5), control is returned to the caller by popping the caller’s LSTM hidden units and program embedding off of a program call stack and resuming execution in this context.

The NPI may also optionally write arguments (ARG) that are passed by reference or value to the invoked sub-programs. For example, an argument could indicate a specific location in the input sequence (by reference), or it could specify a number to write down at a particular location in the sequence (by value). The subsequent state consists of these arguments and observations of the environment. The approach is illustrated in Figures 1 and 2.

It must be emphasized that there is a single inference core. That is, all the LSTM instantiations executing arbitrary programs share the same parameters. Different programs correspond to program embeddings, which are stored in a learnable persistent memory. The programs therefore have a more succinct representation than neural programs encoded as the full set of weights in a neural network (Rumelhart et al., 1986; Graves et al., 2014).

The output of an NPI, conditioned on an input state and a program to run, is a sequence of actions in a given environment. In this work, we consider several environments: a 1-D array with read-only pointers and a swap action, a 2-D scratch pad with read-write pointers, and a CAD renderer with controllable elevation and azimuth movements. Note that the sequence of actions for a program is not fixed, but dependent also on the input state.

3.1 INFERENCE

Denote the environment observation at time t as $e_t \in \mathcal{E}$, and the current program arguments as $a_t \in \mathcal{A}$. The form of e_t can vary dramatically by environment; for example it could be a color image or an array of numbers. The program arguments a_t can also vary by environment, but in the experiments for this paper we always used a 3-tuple of integers $(a_t(1), a_t(2), a_t(3))$. Given the environment and arguments at time t , a fixed-length state encoding $s_t \in \mathbb{R}^D$ is extracted by a domain-specific encoder $f_{enc} : \mathcal{E} \times \mathcal{A} \rightarrow \mathbb{R}^D$. In section 4 we provide examples of several encoders. Note that a single NPI network can have multiple encoders for multiple environments, and encoders can potentially also be shared across tasks.

We denote the current program embedding as $p_t \in \mathbb{R}^P$. The previous hidden unit and cell states are $h_{t-1}^{(l)} \in \mathbb{R}^M$ and $c_{t-1}^{(l)} \in \mathbb{R}^M$, $l = 1, \dots, L$ where L is the number of layers in the LSTM. The program and state vectors are then propagated forward through an LSTM mapping f_{lstm} as in (Sutskever et al., 2014). How to fuse p_t and s_t within f_{lstm} is an implementation detail, but in this work we concatenate and feed through a 2-layer MLP with rectified linear (ReLU) hidden activation and linear decoder.

From the top LSTM hidden state h_t^L , several decoders generate the outputs. The probability of finishing the program and returning to the caller¹ is computed by $f_{end} : \mathbb{R}^M \rightarrow [0, 1]$. The lookup key embedding used for retrieving the next program from memory is computed by $f_{prog} : \mathbb{R}^M \rightarrow \mathbb{R}^K$. Note that \mathbb{R}^K can be much smaller than \mathbb{R}^P because the key only need act as the identifier of a program, while the program embedding must have enough capacity to conditionally generate a sequence of actions. The contents of the arguments to the next program to be called are generated by $f_{arg} : \mathbb{R}^M \rightarrow \mathcal{A}$. The feed-forward steps of program inference are summarized below:

$$s_t = f_{enc}(e_t, a_t) \quad (1)$$

$$h_t = f_{lstm}(s_t, p_t, h_{t-1}) \quad (2)$$

$$r_t = f_{end}(h_t), k_t = f_{prog}(h_t), a_{t+1} = f_{arg}(h_t) \quad (3)$$

where r_t , k_t and a_{t+1} correspond to the end-of-program probability, program key embedding, and output arguments at time t , respectively. These yield input arguments at time $t + 1$. To simplify the notation, we have abstracted properties such as layers and cell memory in the sequence-to-sequence LSTM of equation (2); see (Sutskever et al., 2014) for details.

The NPI representation is equipped with key-value memory structures $M^{key} \in \mathbb{R}^{N \times K}$ and $M^{prog} \in \mathbb{R}^{N \times P}$ storing program keys and program embeddings, respectively, where N is the current number of programs in memory. We can add more programs by adding rows to memory.

During training, the next program identifier is provided to the model as ground-truth, so that its embedding can be retrieved from the corresponding row of M^{prog} . At test time, we compute the ‘‘program ID’’ by comparing the key embedding k_t to each row of M^{key} storing all program keys. Then the program embedding is retrieved from M^{prog} as follows:

$$i^* = \arg \max_{i=1..N} (M_{i,:}^{key})^T k_t, \quad p_{t+1} = M_{i^*,:}^{prog} \quad (4)$$

The next environmental state e_{t+1} will be determined by the dynamics of the environment and can be affected by both the choice of program p_t and the contents of the output arguments a_t , *i.e.*

$$e_{t+1} \sim f_{env}(e_t, p_t, a_t) \quad (5)$$

The transition mapping f_{env} is domain-specific and will be discussed in Section 4. A description of the inference procedure is given in Algorithm 1.

¹In our implementation, a program may first call a subprogram before itself finishing. The only exception is the ACT program that signals a low-level action to the environment, e.g. moving a pointer one step left or writing a value. By convention ACT does not call any further sub-programs.

Each task will have a set of actions that affect the environment. For example, in the case of addition there are LEFT and RIGHT actions that move a specified pointer, and a WRITE action which writes a value at a specified location. These actions are encapsulated into a general-purpose ACT program shared across tasks, and the concrete action to be taken is indicated by the NPI-generated arguments a_t .

Note that the core LSTM module of our NPI representation is completely agnostic to the data modality used to produce the state encoding. As long as the same fixed-length embedding is extracted, the same module can in practice route between programs related to sorting arrays just as easily as between programs related to rotating 3D objects. In the experimental sections, we provide details of the modality-specific deep neural networks that we use to produce these fixed-length state vectors.

3.2 TRAINING

To train we use execution traces $\xi_t^{inp} : \{e_t, i_t, a_t\}$ and $\xi_t^{out} : \{i_{t+1}, a_{t+1}, r_t\}$, $t = 1, \dots, T$, where T is the sequence length. Program IDs i_t and i_{t+1} are row-indices in M^{key} and M^{prog} of the programs to run at time t and $t+1$, respectively. We propose to directly maximize the probability of the correct execution trace output ξ_t^{out} conditioned on ξ_t^{inp} :

$$\theta^* = \arg \max_{\theta} \sum_{(\xi_t^{inp}, \xi_t^{out})} \log P(\xi_t^{out} | \xi_t^{inp}; \theta) \quad (6)$$

where θ are the parameters of our model. Since the traces are variable in length depending on the input, we apply the chain rule to model the joint probability over $\xi_1^{out}, \dots, \xi_T^{out}$ as follows:

$$\log P(\xi_{out} | \xi_{inp}; \theta) = \sum_{t=1}^T \log P(\xi_t^{out} | \xi_1^{inp}, \dots, \xi_t^{inp}; \theta) \quad (7)$$

Note that for many problems the input history $\xi_1^{inp}, \dots, \xi_t^{inp}$ is critical to deciding future actions because the environment observation at the current time-step e_t alone does not contain enough information. The hidden unit activations of the LSTM in NPI are capable of capturing these temporal dependencies. The single-step conditional probability in equation (7) can be factorized into three further conditional distributions, corresponding to predicting the next program, next arguments, and whether to halt execution:

$$\log P(\xi_t^{out} | \xi_1^{inp}, \dots, \xi_t^{inp}) = \log P(i_{t+1} | h_t) + \log P(a_{t+1} | h_t) + \log P(r_t | h_t) \quad (8)$$

where h_t is the output of f_{lstm} at time t , carrying information from previous time steps. We train by gradient ascent on the likelihood in equation (7).

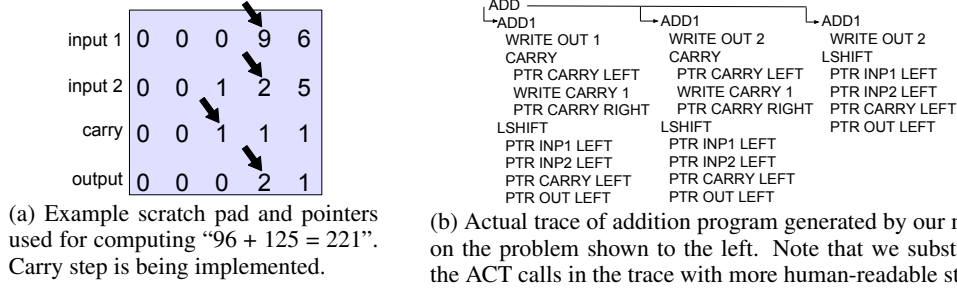
In practice we found it effective to employ a curriculum learning strategy. The intuition is that we first learn the most basic subprograms such as shifting pointers left or right, followed by more challenging programs such as comparing and conditionally swapping two numbers. The knowledge of subprogram difficulty is itself a form of supervision, but there could be many reasonable heuristics such as counting the average number of low-level actions generated by the program.

We also note that our program has a distinct memory advantage over basic LSTMs because all subprograms can be trained in parallel. For programs whose execution length grows *e.g.* quadratically with the input sequence length, an LSTM will be highly constrained by device memory to train on short sequences. By exploiting compositionality, an effective curriculum can often be developed with sublinear-length subprograms, enabling our NPI model to train on order of magnitude larger sequences than the LSTM.

Algorithm 1: Neural programming inference

Data: Program stack S , observation e , program id i^* , args a , stop threshold α
 $h = \mathbf{0}$, $p = M_{i^*,:}^{prog}$, $PUSH(S, (i^*, p, h))$
while $|S| > 0$ **do**
 $s = f_{enc}(e, a)$, $h = f_{lstm}(s, p, h)$
 $r = f_{end}(h)$, $k = f_{prog}(h)$, $a = f_{arg}(h)$
 if $i^* \neq \text{ACT}$ **then**
 $PUSH(S, (i^*, p, h))$, $h = \mathbf{0}$
 $i^* = \arg \max_{i=1..N} (M_{i,:}^{key})^T k$, $p = M_{i^*,:}^{prog}$
 end
 if $r \geq \alpha$ **then**
 $(i^*, p, h) = \text{POP}(S)$
 end
 $e \sim f_{env}(e, p, a)$
end

Figure 3: Illustration of the addition environment used in our experiments.



4 EXPERIMENTS

This section describes the environment and state encoder function for each task, and shows example outputs and prediction accuracy results. For all tasks, the core LSTM had two layers of size 256. We trained the NPI model and all program embeddings jointly using RMSprop with base learning rate 0.0001, batch size 1, and decayed the learning rate by a factor of 0.95 every 10,000 steps.

4.1 TASK AND ENVIRONMENT DESCRIPTIONS

In this section we provide an overview of the tasks used to evaluate our model. Table 2 in the appendix provides a full listing of all the programs and subprograms learned by our model.

ADDITION

The task in this environment is to read in the digits of two base-10 numbers and produce the digits of the answer. Our goal is to teach the model the standard (at least in the US) grade school algorithm of adding, in which one works from right to left applying single-digit add and carry operations.

In this environment, the network is endowed with a “scratch pad” with which to store intermediate computations; *e.g.* to record carries. There are four pointers; one for each of the two input numbers, one for the carry, and another to write the output. At each time step, a pointer can be moved left or right, or it can record a value to the pad. Figure 3a illustrates the environment of this model, and Figure 3b provides a real execution trace generated by our model.

For the state encoder f_{enc} , the model is allowed a view of the scratch pad from the perspective of each of the four pointers. That is, the model sees the current values at pointer locations of the two inputs, the carry row and the output row, as 1-of-K encodings, where K is 10 because we are working in base 10. We also append the values of the input argument tuple a_t :

$$f_{enc}(Q, i_1, i_2, i_3, i_4, a_t) = MLP([Q(1, i_1), Q(2, i_2), Q(3, i_3), Q(4, i_4), a_t(1), a_t(2), a_t(3)]) \quad (9)$$

where $Q \in \mathbb{R}^{4 \times N \times K}$, and i_1, \dots, i_4 are pointers, one per scratch pad row. The first dimension of Q corresponds to scratch pad rows, N is the number of columns (digits) and K is the one-hot encoding dimension. To begin the ADD program, we set the initial arguments to a default value and initialize all pointers to be at the rightmost column. The only subprogram with non-default arguments is ACT, in which case the arguments indicate an action to be taken by a specified pointer.

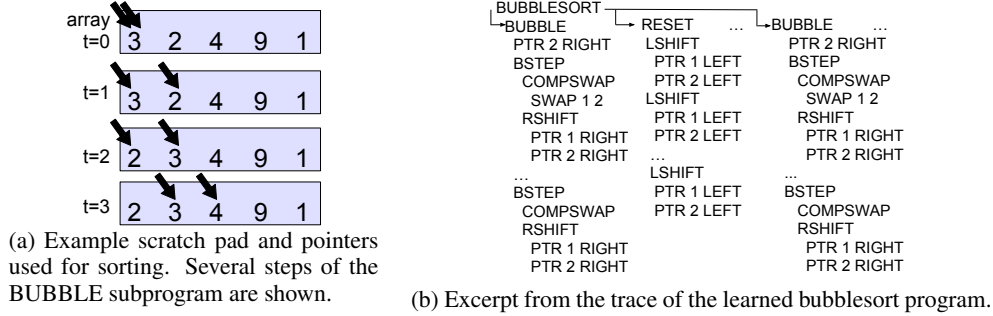
SORTING

In this section we apply our model to a setting with potentially much longer execution traces: sorting an array of numbers using bubblesort. As in the case of addition we can use a scratch pad to store intermediate states of the array. We define the encoder as follows:

$$f_{enc}(Q, i_1, i_2, a_t) = MLP([Q(1, i_1), Q(1, i_2), a_t(1), a_t(2), a_t(3)]) \quad (10)$$

where $Q \in \mathbb{R}^{1 \times N \times K}$ is the pad, N is the array length and K is the array entry embedding dimension. Figure 4 shows an example series of array states and an excerpt of an execution trace.

Figure 4: Illustration of the sorting environment used in our experiments.



CANONICALIZING 3D MODELS

We also apply our model to a vision task with a very different perceptual environment - pixels. Given a rendering of a 3D car, we would like to learn a visual program that “canonicalizes” the model with respect to its pose. Whatever the starting position, the program should generate a trajectory of actions that delivers the camera to the target view, *e.g.* frontal pose at a 15° elevation. For training data, we used renderings of the 3D car CAD models from (Fidler et al., 2012).

This is a nontrivial problem because different starting positions will require quite different trajectories to reach the target. Further complicating the problem is the fact that the model will need to generalize to different car models than it saw during training.

We again use a scratch pad, but here it is a very simple read-only pad that only contains a target camera elevation and azimuth – *i.e.*, the “canonical pose”. Since observations come in the form of image pixels, we use a convolutional neural network f_{CNN} as the image encoder:

$$f_{enc}(Q, x, i_1, i_2, a_t) = MLP([Q(1, i_1), Q(2, i_2), f_{CNN}(x), a_t(1), a_t(2), a_t(3)]) \quad (11)$$

where $x \in \mathbb{R}^{H \times W \times 3}$ is a car rendering at the current pose, $Q \in \mathbb{R}^{2 \times 1 \times K}$ is the pad containing canonical azimuth and elevation, i_1, i_2 are the (fixed at 1) pointer locations, and K is the one-hot encoding dimension of pose coordinates. We set $K = 24$ corresponding to 15° pose increments.

Note, critically, that our NPI model only has access to pixels of the rendering and the target pose, and is not provided the pose of query frames. We are also aware that one solution to this problem would be to train a pose classifier network and then find the shortest path to canonical pose via classical methods. That is also a sensible approach. However, our purpose here is to show that our method generalizes beyond the scratch pad domain to detailed images of 3D objects, and also to other environments with a single multi-task model.

4.2 SAMPLE COMPLEXITY AND GENERALIZATION

Previous works have established that recurrent networks and particularly LSTMs and Neural Turing Machines can in fact sort arrays of numbers. However, we are interested not only in whether sorting can be accomplished, but whether a particular sorting algorithm (*e.g.* bubblesort) can be learned by the model, and how effectively in terms of sample complexity and generalization.

We compare the generalization ability of our model to a flat sequence-to-sequence LSTM (Sutskever et al., 2014), using the same number of layers (2) and hidden units (256). Note that a flat² version of NPI could also learn sorting of short arrays, but because bubblesort runs in $O(N^2)$ for arrays of length N , the execution traces quickly become far too long to store the required number of LSTM states in memory. Our NPI architecture can train on much larger arrays by exploiting compositional structure; the memory requirements of any given subprogram can be restricted to $O(N)$.

A strong indicator of whether a neural network has learned a program well is whether it can run the program on inputs of previously-unseen sizes. To evaluate this property, we train both the sequence-to-sequence LSTM and NPI to perform bubblesort on arrays of single-digit numbers from length 2

²By flat in this case, we mean non-compositional, not making use of subprograms, and only making calls to ACT in order to swap values and move pointers.

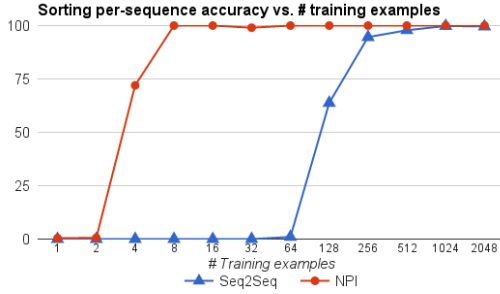


Figure 5: **Sample complexity.** Test accuracy of sequence-to-sequence LSTM versus NPI on length-20 arrays of single-digit numbers. Note that NPI is able to mine and train on subprogram traces from each bubblesort example.

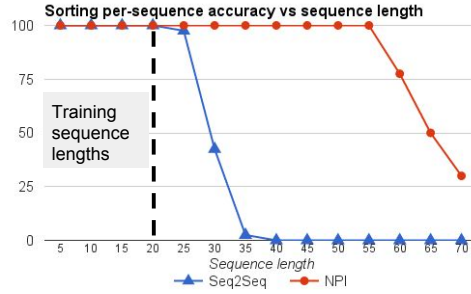


Figure 6: **Strong vs. weak generalization.** Test accuracy of sequence-to-sequence LSTM versus NPI on varying-length arrays of single-digit numbers. Both models were trained on arrays of single-digit numbers up to length 20.

to length 20. Compared to fixed-length inputs this raises the challenge level during training, but in exchange we can get a more flexible and generalizable sorting program.

To handle variable-sized inputs, the state representation must have some information about input sequence length and the number of steps taken so far. For example, the main BUBBLESORT program naturally needs to call its helper function BUBBLE a number of times dependent on the sequence length. We enable this in our model by adding a third pointer that acts as a counter; each time BUBBLE is called the pointer is advanced by one step. The scratch pad environment also provides a bit indicating whether a pointer is at the start or end of a sequence, equivalent in purpose to end tokens used in a sequence-to-sequence model.

For each length, we provided 64 example bubblesort traces, for a total of 1,216 examples. Then, we evaluated whether the network can learn to sort arrays beyond length 20. We found that the trained model generalizes well, and is capable of sorting arrays up to size 60; see Figure 6. At 60 and beyond, we observed a failure mode in which sweeps of pointers across the array would take the wrong number of steps, suggesting that the limiting performance factor is related to counting. In stark contrast, when provided with the 1,216 examples, the sequence-to-sequence LSTMs fail to generalize beyond arrays of length 25 as shown in Figure 6.

To study sample complexity further, we fix the length of the arrays to 20 and vary the number of training examples. We see in Figure 5 that NPI starts learning with 2 examples and is able to sort almost perfectly with only 8 examples. The sequence-to-sequence model on the other hand requires 64 examples to start learning and only manages to sort well with over 250 examples.

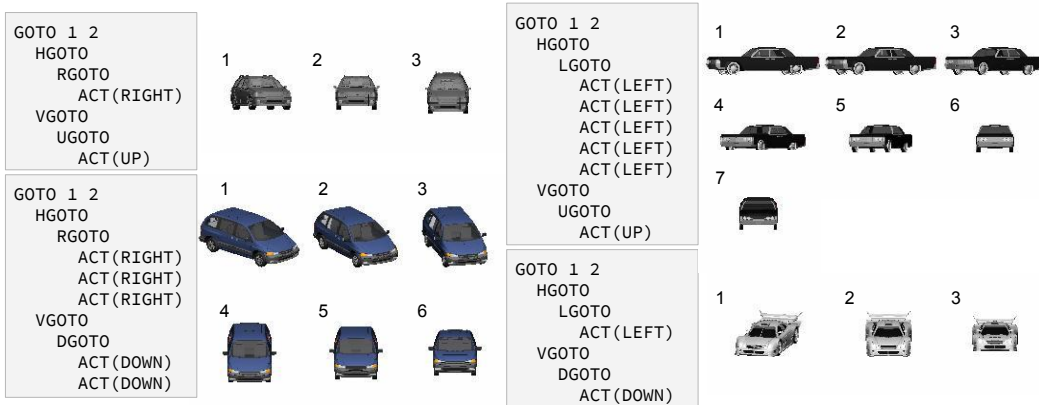


Figure 7: Example canonicalization of several different test set cars, of different appearance than the train set cars. The network is able to generate and execute the appropriate plan based on the starting car image. This NPI was trained on trajectories starting at azimuth ($-75^\circ \dots 75^\circ$), elevation ($0^\circ \dots 60^\circ$) in 15° increments. The training trajectories target azimuth 0° and elevation 15° , as in the generated traces above.

Figure 7 shows several example canonicalization trajectories generated by our model, starting from the leftmost car. The image encoder was a convolutional network with three passes of stride-2 convolution and pooling, trained on renderings of size 128×128 . The canonical target pose in this case is frontal with 15° elevation. At test time, from an initial rendering, NPI is able to canonicalize cars of varying appearance from multiple starting positions. Importantly, it can generalize to car appearances not encountered in the training set as shown in Figure 7.

4.3 LEARNING NEW PROGRAMS WITH A FIXED CORE

One challenge for continual learning of neural-network-based agents is that training on new tasks and experiences can lead to degraded performance in old tasks. The learning of new tasks may require that the network weights change substantially, so care must be taken to avoid catastrophic forgetting (McCloskey & Cohen, 1989; O’Reilly et al., 2014). Using NPI, one solution is to fix the weights of the core routing module, and only make sparse updates to the program memory.

When adding a new program the core module’s routing computation will be completely unaffected; all the learning for a new task occurs in program embedding space. Of course, the addition of new programs to the memory adds a new choice of program at each time step, and an old program could mistakenly call a newly added program. To overcome this, when learning a new set of program vectors with a fixed core, in practice we train not only on example traces of the new program, but also traces of existing programs. Alternatively, a simpler approach is to prevent existing programs from calling subsequently added programs, allowing addition of new programs without ever looking back at training data for known programs. In either case, note that *only the memory slots of the new programs* are updated, and all other weights, including other program embeddings, are fixed.

Table 1 shows the result of adding a maximum-finding program MAX to a multitask NPI trained on addition, sorting and canonicalization. The MAX program works by first calling BUBBLESORT and then a new program RJMP, which moves pointers all the way to the right of the sorted array, where the max element can be read. We performed training as in the from-scratch case, but froze all weights except for the two newly-added program embeddings. Table 1 shows that the NPI learns MAX at high accuracy (95.8) without forgetting the other tasks. In particular, after training a single multi-task model as outlined in the following section, learning the MAX program with this fixed-core multi-task NPI results in no performance deterioration for all three tasks.

4.4 SOLVING MULTIPLE TASKS WITH A SINGLE NETWORK

In this section we perform a controlled experiment to compare the performance of a multi-task NPI with several single-task NPI models. Table 1 shows the results for addition, sorting and canonicalizing 3D car models. We trained and evaluated on 10-digit numbers for addition, length-5 arrays for sorting, and up to four-step trajectories for canonicalization. As shown in Table 1, a single multi-task NPI can learn all three programs (and necessarily the 21 subprograms) with modest performance degradation compared to when it is trained on single-tasks.

Task	Single	Multi	+ Max
Addition	84.8	85.0	85.0
Sorting	100.0	91.6	91.6
Canon. seen car	91.6	78.3	78.3
Canon. unseen	85.0	73.3	73.3
Maximum	-	-	95.8

Table 1: Per-sequence % accuracy. “+ Max” indicates performance after addition of MAX and RJMP programs to the program memory. “Maximum” is the additional max-finding task implemented by calls to BUBBLESORT and RJMP. “seen car” uses a test set with disjoint car models from the training set, while “unseen” uses the same car models but different trajectories.

5 CONCLUSION

We have shown that the NPI can learn programs in very dissimilar environments with different affordances. In the context of sorting we showed that NPI exhibits very strong generalization in comparison to sequence-to-sequence LSTMs. We also showed how a trained NPI with a fixed core can continue to learn new programs without forgetting already learned programs.

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6 APPENDIX

6.1 LISTING OF LEARNED PROGRAMS

Below we list the programs learned by our model:

Program	Descriptions	Calls
ADD	Perform multi-digit addition	ADD1, LSHIFT
ADD1	Perform single-digit addition	ACT, CARRY
CARRY	Mark a 1 in the carry row one unit left	ACT
LSHIFT	Shift a specified pointer one step left	ACT
RSHIFT	Shift a specified pointer one step right	ACT
ACT	Move a pointer or write to the scratch pad	-
BUBBLESORT	Perform bubble sort (ascending order)	BUBBLE, RESET
BUBBLE	Perform one sweep of pointers left to right	ACT, BSTEP
RESET	Move both pointers all the way left	LSHIFT
BSTEP	Conditionally swap and advance pointers	COMPSWAP, RSHIFT
COMPSWAP	Conditionally swap two elements	ACT
LSHIFT	Shift a specified pointer one step left	ACT
RSHIFT	Shift a specified pointer one step right	ACT
ACT	Swap two values at pointer locations or move a pointer	-
GOTO	Change 3D car pose to match the target	HGOTO, VGOTO
HGOTO	Move horizontally to the target angle	LGOTO, RGOTO
LGOTO	Move left to match the target angle	ACT
RGOTO	Move right to match the target angle	ACT
VGOTO	Move vertically to the target elevation	UGOTO, DGOTO
UGOTO	Move up to match the target elevation	ACT
DGOTO	Move down to match the target elevation	ACT
ACT	Move camera 15° up, down, left or right	-
RJMP	Move all pointers to the rightmost position	RSHIFT
MAX	Find maximum element of an array	BUBBLESORT, RJMP

Table 2: Programs learned for addition, sorting and 3D car canonicalization. Note the the ACT program has a different effect depending on the environment and on the passed-in arguments.

6.2 GENERATED EXECUTION TRACE OF BUBBLESORT

Figure 8 shows the sequence of program calls for BUBBLESORT. Pointers 1 and 2 are used to im-

Figure 8: Generated execution trace from our trained NPI sorting the array [9,2,5].

```

BUBBLESORT
  BUBBLE
    PTR 2 RIGHT
    BSTEP
      COMPSWAP
        SWAP 1 2
      RSHIFT
        PTR 1 RIGHT
        PTR 2 RIGHT
    BSTEP
      COMPSWAP
        SWAP 1 2
      RSHIFT
        PTR 1 RIGHT
        PTR 2 RIGHT
  RESET
    LSHIFT
      PTR 1 LEFT
      PTR 2 LEFT
    LSHIFT
      PTR 1 LEFT
      PTR 2 LEFT
      PTR 3 RIGHT
  BUBBLE
    PTR 2 RIGHT
    BSTEP
      COMPSWAP
      RSHIFT
        PTR 1 RIGHT
        PTR 2 RIGHT
    BSTEP
      COMPSWAP
      RSHIFT
        PTR 1 RIGHT
        PTR 2 RIGHT
  RESET
    LSHIFT
      PTR 1 LEFT
      PTR 2 LEFT
    LSHIFT
      PTR 1 LEFT
      PTR 2 LEFT
      PTR 3 RIGHT
  BUBBLE
    PTR 2 RIGHT
    BSTEP
      COMPSWAP
      RSHIFT
        PTR 1 RIGHT
        PTR 2 RIGHT
    BSTEP
      COMPSWAP
      RSHIFT
        PTR 1 RIGHT
        PTR 2 RIGHT
  RESET
    LSHIFT
      PTR 1 LEFT
      PTR 2 LEFT
    LSHIFT
      PTR 1 LEFT
      PTR 2 LEFT
      PTR 3 RIGHT

```

plement the “bubble” operation involving the comparison and swapping of adjacent array elements. The third pointer (referred to in the trace as “PTR 3”) is used to count the number of calls to BUBBLE. After every call to RESET the swapping pointers are moved to the beginning of the array and the counting pointer is advanced by 1. When it has reached the end of the scratch pad, the model learns to halt execution of BUBBLESORT.