

A	B	C	D	E	F	G	H	I	J
COUNTER_CELL									
<p>The schematic shows three DFFs labeled DFF_POS_EDGE_DFF_POS_EDGE_1, DFF_POS_EDGE_DFF_POS_EDGE_2, and DFF_POS_EDGE_DFF_POS_EDGE_3. Each DFF has inputs Din, DQbar, clk, and resbar. The outputs are DQ0, DQ1, and DQ2. The connections are as follows:</p> <ul style="list-style-type: none">Din of DFF1 is connected to clk.DQ0 of DFF1 is connected to DQbar of DFF2.DQ1 of DFF2 is connected to DQbar of DFF3.DQ2 of DFF3 is connected to DQbar of DFF1.clk of all DFFs is connected to a common clk input.resbar of all DFFs is connected to a common resbar input.									
A	B	C	D	E	F	G	H	I	J
8	7	6	5	4	3	2	1	8	7